

HP 13255

PROM CHARACTER MODULE

Manual Part No. 13255-91053

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AUG-01-76

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NOTE: This document is part of the 264XX DATA TERMINAL product series Technical Information Package (HP 13255).

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1.0 INTRODUCTION.

The PROM Character Module provides the means of adding up to two user-generated PROM character sets as described in "HP Character Set Generation Kit Application Note", HP Part Number 13245-90001. When installed, the PROM Character Module can replace either the terminal's base set or any two of the three alternate character sets.

2.0 OPERATING PARAMETERS.

A summary of operating parameters for the PROM Character Module is contained in tables 1.0 through 5.1.

Table 1.0 Physical Parameters

			========
Part Number	Nomenclature	Size (L x W x D) +/-0.100 Inches	
		1	
02640-60053	PRUM Character PCA	12.9 x 4.0 x 0.5	0.38
02640-60070	Rear Connector Assembly	! ! N/A	I N/A
1		i I	i !
!		•	1
, 		1 6	1
 		 	i ========
	Number of Backplane Slots Re	quired: 1	

Table 2.0 Reliability and Environmental Information

==							
1	Environmental:	(X) HP	Class B	()	Other:		
 	Restrictions: Type	e tested	at product	level			
 -	=======================================	1222222	=======================================	=======================================) ====================================
İ	Failur	re Rate:	0.247	(percent	per 1000	hours)	İ
==			=========	:=======		:========	

Table 3.0 Power Supply and Clock Requirements - Measured (At +/-5% Unless Otherwise Specified)

_======================================			
 +5 Volt Supply @ 40 mA	+12 Volt Supply 	-12 Volt Supply -12 Volt Supply 0 mA	-42 Volt Supply -42 Nolt Supply
(without PROMs)	 NOT APPLICABLE 		NOT APPLICABLE
115 yo	lts ac	220 vo	olts ac
, a	A	e	A
NOT APP	LTCABLE	NOT APE	PLICABLE
	Clock Frequency:	MHZ	
, 	NOT APPLI	CABLE	; ====================================

Table 4.0 Jumper Definitions

Function				
(0.1)	 (2)			
=======================================	 ===================================			
Set 0 when the PROM Character PCA is connected to the				
XU11 through XU15 - Replace Set 1 when the PROM Character PCA is connected to the Display Expansion Module				
(2)	-====================================			
(2)	;			
Set 2 when the PROM Character PCA is connected to the				
	XU11 through XU14 - Replace Set 0 when the PROM Character PCA is connected to the Display Control PCA XU11 through XU15 - Replace Set 1 when the PROM Character PCA is connected to the Display Expansion Module XU1 through XU5 - Replace Set 2 when the PROM Character PCA is connected to the			

Table 5.0 Connector Information

		**=====================================
<pre>! Connector</pre>	Signal	Signal
l and Pin No.	Name	Description
=======================================	=====================================	
1	l	
P1, Pin 1	1 + +5 V 1	t +5 Volt Supply I
-2	GND	Ground Common Return (Power and Signal)
P1, Pin 3 through Pin 21	 	
P1, Pin 22	I GND I	Ground Common Return (Power and Signal)
P1, Pin A		Ground Common Return (Power and Signal)
P1, Pin B through Pin S	, 	
P1, Pin T	PRIOR IN	Bus Controller Priority In
-ti	TUO ROTAG	Bus Controller Priority Out
P1, Pin V through Pin Z	' - 	
=======================================		

Table 5.1 Connector Information

=======================================		**=====================================
Connector	Signal	Signal !
and Pin No.	Name	Description
======================================	=======================================	
1 P2, Pin 1	GMD.	Ground
- 2	PG0	Scan Line Counter Bit 0
- 3	PG5	Scan Line Counter Bit 2
- 4	ВІТО	ASCIT Bit 0
- 5	втт2	ASCIT Bit 2
- 6	втт4	ASCII Rit 4
- 7	віт6	ASCIT Bit 6
- 8	BSS1	Negative True, Buffered Set Select Bit 1
- 9	1 1 1	
-10		1)
-11	DBITI	Negative True, Dot 1 Output
-12	DBIT3	Negative True, Dot 3 Output
-13	DBIT5	Megative True, Dot 5 Output
-14	DBIT7	Negative True, Dot 7 Output
-15	GND	Ground
	====================================	



Table 5.1 Connector Information (Cont'd.)

=======================================		
1 Connector	Signal	Signal
l and Pin No.	Name	Description
=====================================		*************************************
i		
P2, Pin A	GND	Ground
-n	LC1	
-с	1.C3	Scan Line Counter Bit 3
-D	BIT1	ASCIT Bit 1
-F	ВІТЗ І	ASCIT Rit 3
-F	ВТТ5	ASCIT Bit 5
-н	BSS0	Negative True, Buffered Set Select Bit 0
-J	SETO	Negative True, Selects Base Set Peplacement When Low and W1 is in "0,1" Position
-к	1	Not lised
-1,	DBITO	
-м	DBIT2	
- N	DBIT4	Negative True, Dot 4 Output
-P	DBIT6	Negative True, Dot 6 Output
-R	DBIT8	Negative True, Dot 8 Output
-s	GND	I Ground
		' ====================================

3.0 FUNCTIONAL DESCRIPTION. Refer to the block diagram (figure 1), schematic diagram (figure 2), timing diagram (figure 3), component location diagram (figure 4), and parts lists (02640-60053 and 02640-60070) located in the appendix.

The major functional groups of the PROM Character Module are the address buffer, output buffer, two PROM arrays, character set decoder, character select decoder, and a microvector data encoder.

- 3.1 ADDRESS BUFFER. The address buffer consists of eight gates to buffer the PROM address lines, thus preventing undue loading on the P2 connector.
- 3.2 OUTPUT BUFFER.
- 3.2.1 The output buffer consists of nine gates which drive either the Display Expansion Module or the Display Control PCA data lines, depending on which PCA is connected to the PROM Character Module.
- 3.2.2 The buffer output (DBTTO through DBTT7), is an 8-bit dot position word that is applied back to the parallel-to-serial converter of either the Display Control PCA or the Display Expansion Module. If a microvector

character set is installed, the ninth dot position is output as DRIT8.

- 3.3 PROM ARRAYS.
- 3.3.1 The PROM arrays are two sets of five PROM sockets. These are each capable of storing a 128-character set of either the alphanumeric or microvector type. Each PROM holds 32 characters, the fifth PROM of each array contains the Microvector Bit 8 for all 128 characters.
- 3.3.2 Each PROM is organized as 512 words of eight bits each. The four LSB of the 9-bit address are LCO through LC3 and the five MSB are the ASCTI BITO through BIT4.

- 3.4 CHARACTER SET DECODER.
- 3.4.1 The character set decoder, in confunction with Jumpers W1 and W2, selects which of the three possible alternate character sets are to be represented in PROMs on the PROM Character PCA.
- 3.4.2 When the PROM Character PCA is connected to the Display Control PCA,

the Base Set Select (SETO) signal is always low and the character set decoder applies the Base Set Enable (0/1 SEN) signal to the PROMs in character set 0 (Jumper W1 must be in the "0,1" position). Alternate

character set select signals (BSSO and BSS1) are also applied to the character set decoder and, if either or both signals go low, the character set decoder disables the PROM set by removing O/ISEN. When this occurs, character set selection and character generation are performed by the Display Expansion Module.

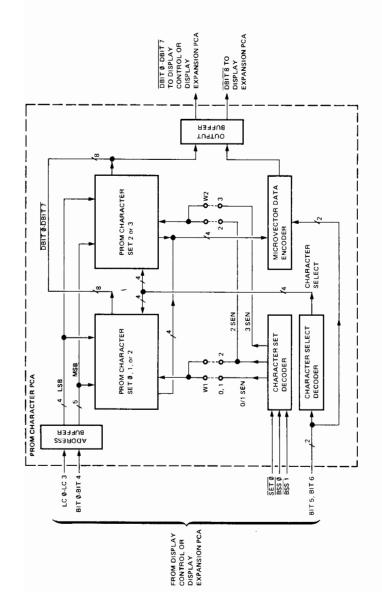
When the PROM Character PCA is connected to the Display Expansion Mod-

ule, SETO is always high and BSSO and BSS1 are decoded by the character set decoder into set enabling signals (0/15EN, 25EN, or 35EN) for the PROMs replacing character sets 1, 2, or 3. The position of Jumpers W1 and W2 determines which set select signal will enable their corresponding PROM sets and thus which alternate character set will be replaced by those PROMs.

- 3.5 CHARACTER SELECT DECUDER.
- 3.5.1 The character select decoder generates a Chip Enable signal for each PROM socket, excluding the Microvector Bit 8 sockets (U5 and U15).
- 3.5.2 Once enabled by the character set decoder, the PROM character set (s) function the same as the replaced ROM character set (s) to generate dot patterns corresponding to received ASCII codes. The BIT5 and BIT6 signals are applied to the character select decoder which determines individual PROM selection (s) of 32 characters each, within a character set.

- 3.6 MICROVECTOR DATA ENCODEP.
- 3.6.1 The microvector data encoder selects, from the Microvector Bit 8 PROM (US or U15), one of four data output lines corresponding to the desired microvector character.
- 3.6.2 The fifth PROM required for each microvector set has its output encoded by the microvector data encoder. One output bit is selected by the ASCII BIT5 and BIT6 which corresponds to the 32-character segment selected. The output of the microvector data encoder is buffered and

then leaves the PROM Character PCA as DBIT8.



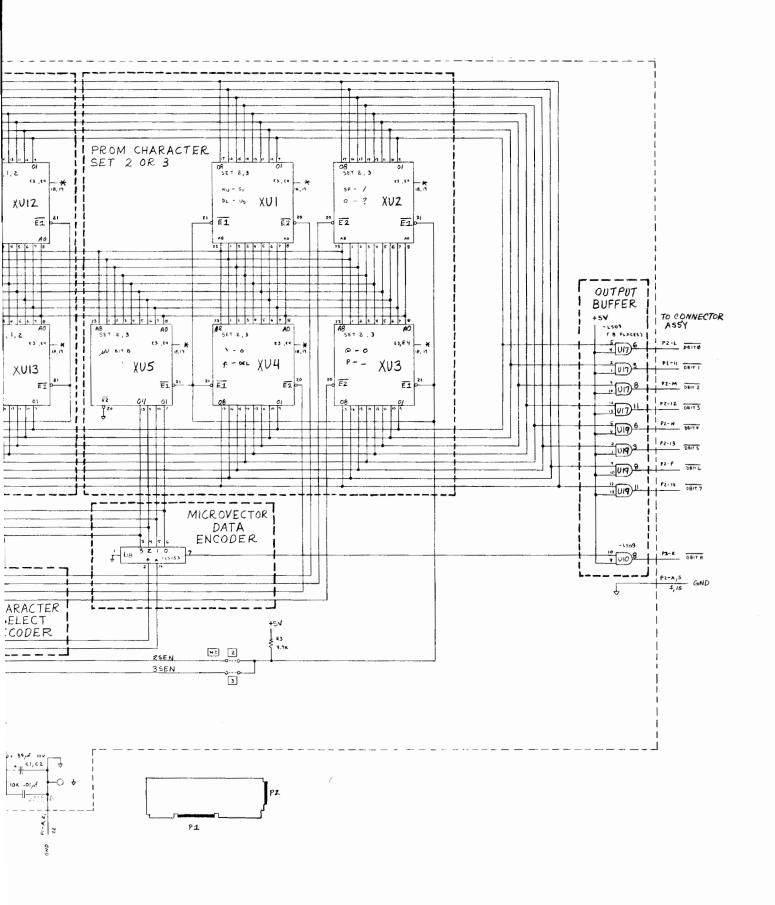


Figure 2
PROM Character PCA Schematic Diagram
AUG-01-76
13255-91053

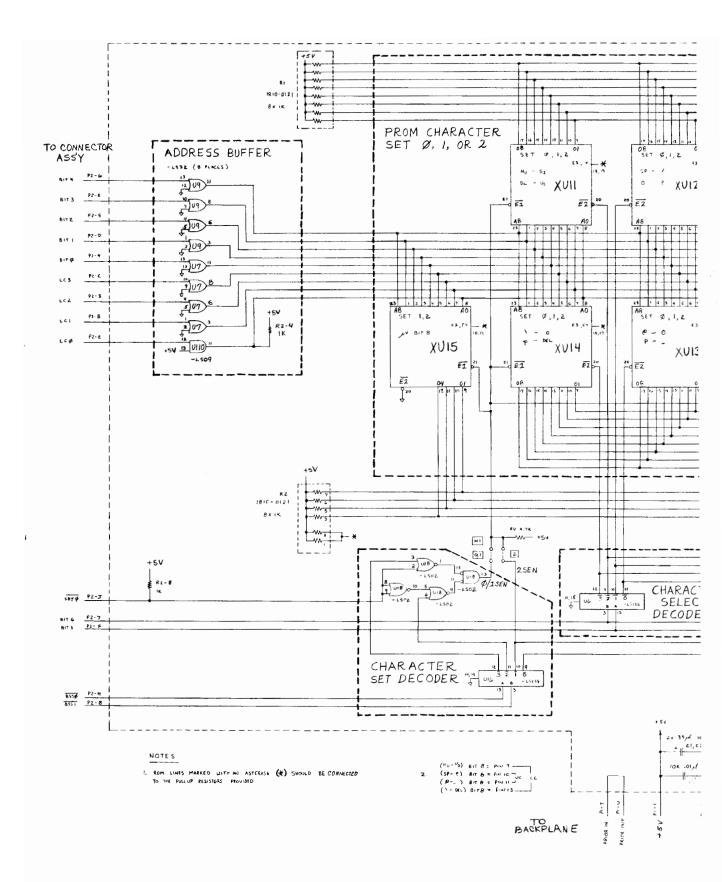
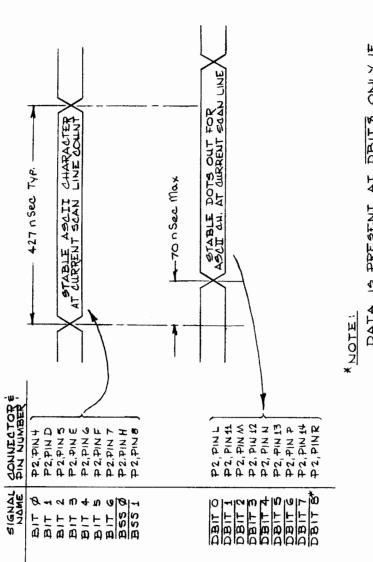
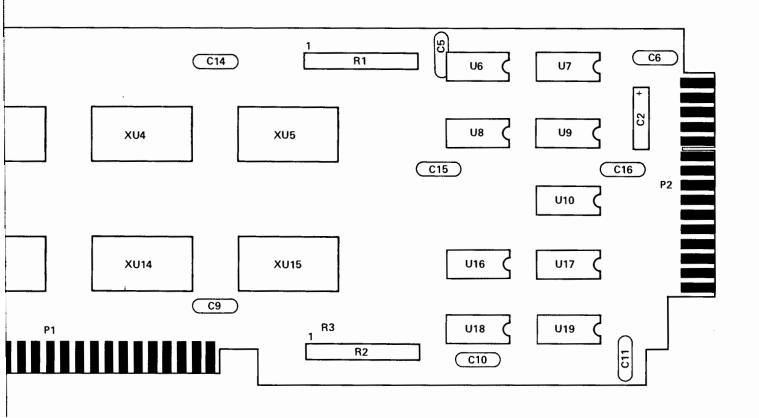
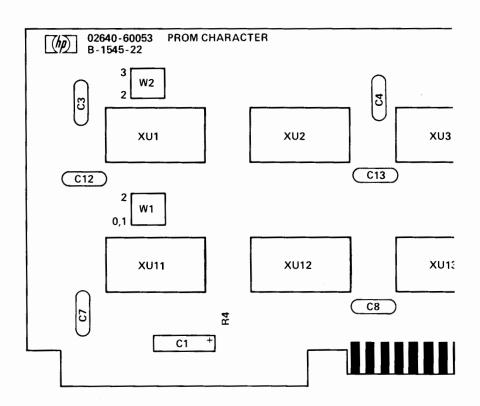


Figure 3 PRGM Character Timing Diagram 13255-91053



DATA 19 PRESENT AT DBIT'S ONLY 1F THE PROM CHARACTER SET ADDRESSED 18 OF THE MICROVECTOR TYPE.





Replaceable Parts

December Color C	
REVISION DATE: 04-15-76	
CAPACITOR-FXC JOUF +80-20X 100WYDC CER 2480 0160-2055 01	
Capacitor=Fxd	
C11	
C10	
R1	
R2	
1820-1208 2 1C-D1G1TAL SN74LS32N TTL LS QUAD 2 OR 1820-1244 1 1C-D1G1TAL SN74LS32N TTL LS QUAD 2 OR 1820-1246 3 1C-D1G1TAL SN74LS32N TTL LS QUAD 2 OR 1820-1246 3 1C-D1G1TAL SN74LS32N TTL LS QUAD 2 AND 1820-1246 1 1C-D1G1TAL SN74LS09N TTL LS QUAD 2 AND 1820-1246 1 1 1 1 1 1 1 1 1	
1820-1246 IC-DIGITAL SN74LSOSN TTL LS QUAD 2 AND 1820-1144 1 IC-DIGITAL SN74LSOSN TTL LS QUAD 2 NOR 1820-1144 1 IC-DIGITAL SN74LSOSN TTL LS QUAD 2 NOR 1820-1246 1 IC-DIGITAL SN74LSOSN TTL LS QUAD 2 NOR 1295 SN74LSOSN SN74LSO	
XU2	
XU12	
1272 0124	

Replaceable Parts

Reference Designation	HP Part Number	Qty	Description	Mfr Code	Mfr Part Number
	02440-60070	1	CONNECTOR ASSEMBLY (30-PIN) REVISION DATE: 03-26-76	28480	02640-60070
J1 J2	1251-1886 1251-1886	2	CONNECTOR-PC EDGE 15-CONT/ROW 2-ROWS CONNECTOR-PC EDGE 15-CONT/ROW 2-ROWS	71785 71785	252-15-30-340 252-15-30-340
	0360-0003 02440-0003 2150-0003 2240-0002 02440-00032	4 1 4 4 1	SPACER-RND .125LG .181D .25DD BRS NI-PL HANDLE, CCNNECTOR MASHER-LK HLCL NO4 .115-IN-ID NUT-HEX-DBL-CHAM 4-40-THD .062-THK INSULATOR	28480 28480 28480 28480 28480	0380-0004 02640-00033 2190-0003 2260-0005 02640-00032