HP 12798B

BCD OUTPUT INTERFACE KIT Model 2570A/75A Coupler/Controller

OPERATING AND SERVICE MANUAL

MAY 1971



This manual applies directly to HP 12798B BCD Output Interface Kits having revision code number A 1108 6. Cards with later revision code numbers will be covered in an Updating Supplement at the front of this manual.

CERTIFICATION

The Hewlett-Packard Company certifies that this instrument was thoroughly tested and inspected and found to meet its published specifications when it was shipped from the factory. The Hewlett-Packard Company further certifies that its calibration measurements are traceable to the U.S. National Bureau of Standards to the extent allowed by the Bureau's calibration facility.

WARRANTY AND ASSISTANCE

This Hewlett-Packard Model 12798B General Purpose 40-Bit Output Interface Kit is warranted against defects in materials and workmanship. This warranty applies for one year from the date of delivery. We will repair or replace products which prove to be defective during the warranty period, provided they are returned to Hewlett-Packard. No other warranty is expressed or implied. We are not liable for consequential damages.

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BCD OUTPUT INTERFACE KIT for Model 2570A/75A Coupler/Controller

model

12798B

MANUAL UPDATING SUPPLEMENT 22 OCT 1971

MANUAL IDENTIFICATION

SUPPLEMENT DESCRIPTION

Manual Printed: May 1971

1. Changes and corrections.

Manual Part Number: 12798-90007

2. Optional Applications Information.

INSTRUMENT CHANGES

Prefix-Serial	Changes
ALL	1
ALL	2

CHANGE

DESCRIPTION

- Page 2-13, Figure 2-4. Change jumper W6 on card HP 12798-60011 from "A" position to "B" position.
- Options 011 and 012 provide additional customer applications by interfacing the HP 12798B BCD Output Card to the following accessories, respectively:
 - a. HP 6936A Multiprogrammer
 - b. HP 3485A Scanning Unit

Make the following manual changes.

Page iv, List of Illustrations, after Figure 2-12 add:

Figure 2-12A, HP 6936A Programming Interface, Page US-5

Figure 2-12B, HP 3485A Scanning Unit Interface, Page US-6

Page iv, List of Tables, after Table 2-9 add:

Table 2-9A. Cable HP Stock No. 12798-60013 Wiring Information (Part of Option 011, HP 12798B - HP 6936A Interface)

, Page US-3

Table 2-9B. Cable HP Stock No. 12798-60014 Wiring Information (Part of Option 012, HP 12798B - HP 3485A Interface)

, Page US-4

Page 1-2, Table 1-1. After Option 010 add:

Option 011 HP 12798-60013 Cable to interconnect with HP 6936A Multiprogrammer.

Includes logic compatibility (ground-true output).

Option 012 HP 12798-60014 Cable to interconnect with HP 3485A Scanning Unit.

Includes logic compatibility (ground-true output).

CHANGE

DESCRIPTION

2 (Continued)

Page 2-1, Paragraph 2-10. Change line 1 to:

"Tables 2-4 through 2-9B provide wiring in ..."

After Page 2-10, Table 2-9, add the following inserts:

Page US-3, Table 2-9A. Cable HP Stock No. 12798-60013 Wiring Information.

Page US-4, Table 2-9B. Cable HP Stock No. 12798-60014 Wiring Information.

After Page 2-22, Figure 2-12, add the following inserts:

Page US-5, Figure 2-12A. HP 6936A Programming Interface.

Page US-6, Figure 2-12B. HP 3485A Scanning Unit Interface.

Page 5-4, Table 5-1. Replaceable Parts. After the HP 12798-60009 Cable components, add the following:

12798-60013	1	CABLE (PART OF OPTION 011)	04404	12798-60013
1251-0086	1	CONNECTOR:50 CONTACT R & P	28480	1251-0086
1251-0171	1	BUSHING: FOR AN TYPE CONNECTOR	28480	1251-0000
1251-2518	1	CONNECTOR: PC 48 (2×24) CONTACT	71785	251-24-30-261
8120-1041	8 FT	CABLE 36 COND	28480	8120-1041
02116-4001	1	HOLDER:CONNECTOR	28480	02116-4001
02116-4003	1	CLAMP CABLE: LARGE	28480	02116-4001
12798-60014	1	CABLE (PART OF OPTION 012)	04404	12798-60014
1251-2518	1	CONNECTOR: PC 48 (2×24) CONTACT	71785	251-24-30-261
1251-2815	1	CONNECTOR KIT	28480	1251-24-30-201
8120-0181	$8 \; FT$	CABLE SHIELDED	28480	8120-0181
02116-4001	1	HOLDER: CONNECTOR	28480	02116-4001
02116-4003	1	CLAMP CABLE:LARGE	28480	02116-4001

Table 2-9A. Cable HP Stock No. 12798-60013 Wiring Information (Part of Option 011, HP 12798B - HP 6936A Interface)

	-	96"		
HP 127 98B	A		B	HP 6936A

BCD OUTPUT SIGNALS

Ass	in ign- ent	HP 127 BCD Ou		нр 6936А	Pi Assi me	gn-	HP 127 BCD Ou		HP 6936A Control Signals
A	В	Character	BCD Weight	Control Signals	A	В	Character	BCD Weight	Control Digitals
19 2ø W	49 5ø	Most Significant Digit 9 (Loaded First)	$\begin{bmatrix} 1 \\ 2 \\ 4 \\ 8 \end{bmatrix}$	B14 B15	9 1Ø K L	39 4ø	4	1 2 4 8	ВØ4 ВØ5
17 18 U V	47 48	8	$\left\{\begin{array}{c}1\\2\\4\\8\end{array}\right.$	B12 B13	7 8 H J	37 38	3	\ \begin{cases} 1 & 2 & 4 & 8 & 8 & \end{cases}	В Ø2 В Ø 3
15 16 S T	45 46	7	$\left\{\begin{array}{c}1\\2\\4\\8\end{array}\right.$	B1Ø B11	5 6 E F	35 36	2	$\left\{\begin{array}{c}1\\2\\4\\8\end{array}\right.$	ВØØ ВØ1
13 14 P R	43 44	6	$\left\{\begin{array}{c}1\\2\\4\\8\end{array}\right.$	в ø 8 в ø 9	3 4 C D		1	\begin{cases} 1 & 2 & 4 & 8 & 8 & 8 & 8 & 8 & 8 & 8 & 8 & 8	(Note 1)
11 12 M N	41 42	5	$\left\{\begin{array}{c}1\\2\\4\\8\end{array}\right.$	ВØ6 ВØ7	1 2 A B		Least Significant Digit Ø (Loaded Last)	$\left\{\begin{array}{c}1\\2\\4\\8\end{array}\right.$	(Note 1)

INTERFACE CONTROL SIGNALS

Α	В	Signal			
21	33	Busy - Flag			
22	32	Store Data - Gate			
ВВ	26	Ground			
	18 19	Jumpered Together			

- 1. Not normally used.
- 2. A parts list for the cable is contained in Table 5-1.
- 3. Overscored signals are ground true.

Table 2-9B. Cable HP Stock No. 12798-60014 Wiring Information (Part of Option 012, HP 12798B - HP 3485A Interface)

	(Part of Option 012, HP 12798B - HP 3485A Interface)									
HI	P 1279	98B A		BCI	OUTPU	3'' —		S		В НР 3485А
As	Pin sign- nent	HP 12'BCD O		HP 348	5A	Ass	Pin sign-	UD 197		HP 3485A
A	В	Character	BCD Weight	Control Si	gnals	A	В	Character	BCD Weight	Control Signals
19 2ø W X		Most Significant Digit 9 (Loaded First)	$ \begin{bmatrix} 1 \\ 2 \\ 4 \\ 8 \end{bmatrix} $			9 1ø K L	N L	4	$\begin{bmatrix} 1 \\ 2 \\ 4 \\ 8 \end{bmatrix}$	1000 mV 10V
17 18 U V	H X J C	8	$ \left\{ \begin{array}{c} 1 \\ 2 \\ 4 \\ 8 \end{array} \right. $	STEP SINGLE SCA CONTINUOUS		7 8 H J	S	3	$ \begin{cases} 1 \\ 2 \\ 4 \\ 8 \end{cases} $	FILTER ENABLE
15 16 S T	d	7	$ \left\{ \begin{array}{c} 1 \\ 2 \\ 4 \\ 8 \end{array} \right. $	RANDOM		5 6 E F		2	$ \begin{cases} 1 \\ 2 \\ 4 \\ 8 \end{cases} $	(Note 1)
13 14 P R	V U T	6	1 2 4 8	CH 1Ø CH 2Ø CH 4Ø		3 4 C D		1	$ \begin{cases} 1 \\ 2 \\ 4 \\ 8 \end{cases} $	(Note 1)
11 12 M N	Y R Z W	5	$ \left\{ \begin{array}{c} 1\\2\\4\\8 \end{array}\right. $	CH 1 CH 2 CH 4 CH 8		1 2 A B		Least Significant Digit Ø (Loaded Last)	$ \begin{cases} 1 \\ 2 \\ 4 \\ 8 \end{cases} $	(Note 1)
		INTERFACE	CONTRO	L SIGNALS						
A	В		Signal							
21	b	Busy (Prog	ram Ackn	owledge)					NOTES	
21	c	Busy (Initia	te)			1. Not normally used.				
BB	р	Ground					2.	A parts list f	or the cal	ole is contained in
BB		Shield		İ			•	Table 5-1.		

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Store Data (Program Executive)

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SECTION I INTRODUCTION AND DESCRIPTION

1-1. GENERAL INFORMATION

- 1-2. The HP12798B BCD Output Interface Kit contains a general purpose 40-bit (10 BCD digit) parallel data output register as a means to allow the HP2570A Coupler/Controller* to provide:
- a. 10-digit parallel data output for digital recorders and frequency synthesizer programmers.
- b. 40 bits of parallel programming information for programmable voltmeters, counters, digital voltage sources, etc.
- 1-3. The kit consists of a BCD output plug-in card with positive-true output and a connector for making an interconnecting cable to the interfacing device. An optional BCD output card with ground-true output is available. Also, the card may be ordered with interconnect cables to interface with certain HP instruments. Full details of all options available are listed in Table 1-1.
- 1-4. Sections II through V provide information on installation and operation, principles of operation, servicing, and replacement parts for the BCD output card.

1-5. EQUIPMENT SUPPLIED

- 1-6. The HP 12798B BCD Output Interface Kit consists of:
- a. BCD Output Card, positive-true output. HP Part No. 12798-60010.
 - b. Connector Kit. HP Part No. 02116-6178.

NOTE

The connector kit is not supplied if HP 12798B is ordered with Options 001 through 010.

1-7. OPTIONS

1-8. The HP 12798B BCD Output Interface Kit may be ordered with the options listed in Table 1-1.

1-9. DESCRIPTION

1-10. The BCD Output Card permits transfer of information from the HP 2570A Coupler/Controller

to parallel entry digital devices. The card is jumper programmable to operate in either a record or program mode. When the record mode is chosen, for example, to interface an HP 5050B Digital Recorder, operation is as follows: The card converts, upon command from the HP 2570A, the ASCII signals on the backplane to 8421 BCD and these are sequentially clocked by a decade counter, digit by digit into a storage/output register on the card. (A typical source of data may be a digital voltmeter interfaced to the HP 2570A with an HP 12797A BCD Input Card.) A total of 10 4-bit data latches in the register permit up to 10 digits to be stored. The last digit in the word is accompanied with or followed by a signal that causes the card to initiate a Store Data command. When the recorder receives the Store Data command, it returns a Busy signal. The Busy signal activates a circuit on the card that halts the main instruction program on the HP 2570A if an attempt is made to output more data before the recorder has completed the printout. At the end of the printout action, the Busy signal is released, allowing the instruction program of the HP 2570A to continue. The number of characters accepted and their position in the output is determined by the sequence of the input data. The data register is returned to the preset value before new data is entered.

1-11. When the program mode is selected, for example, to program a DVM, operation of the card is basically similar to the record mode previously discussed. The card converts, upon command from the HP 2570A, the ASCII signals on the backplane to 8421 BCD, and these are sequentially clocked into the register. (A typical source of input data may be a teleprinter, tape reader, calculator, etc., interfaced with the appropriate I/O card; or the pinboard of the coupler/controller.) The last digit in the word is either accompanied with or followed by a signal that causes the card to initiate a Store Data command. This signal is interpreted by the programmed device as a program accept pulse and causes the program to be executed. When the device receives the Store Data command, it returns a Busy pulse to the card. Depending on the positions of jumpers W9 and W51, the card may halt the main instruction program of the HP 2570A until the instrument is programmed. In any event, if an attempt is made to alter the output register code before the Busy pulse is released, the HP 2570A is halted. If the programmed device is incapable of generating this signal, the BCD output card will allow the HP 2570A program to continue nominally 300 ms after the Store Data pulse. The information stored in the data register is retained until the card is addressed with another command from the HP 2570A.

1-12. SPECIFICATIONS

1-13. Input/output characteristics for the HP 12798B are given in Table 1-2.

^{*}In general the information given in this manual also applies to the HP2575A Coupler/Controller which is a four-slot version of the HP2570A. Consult the HP2575A Operating and Service Manual for the differences between the two instruments.

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Table 1-1. HP 12798B List of Options

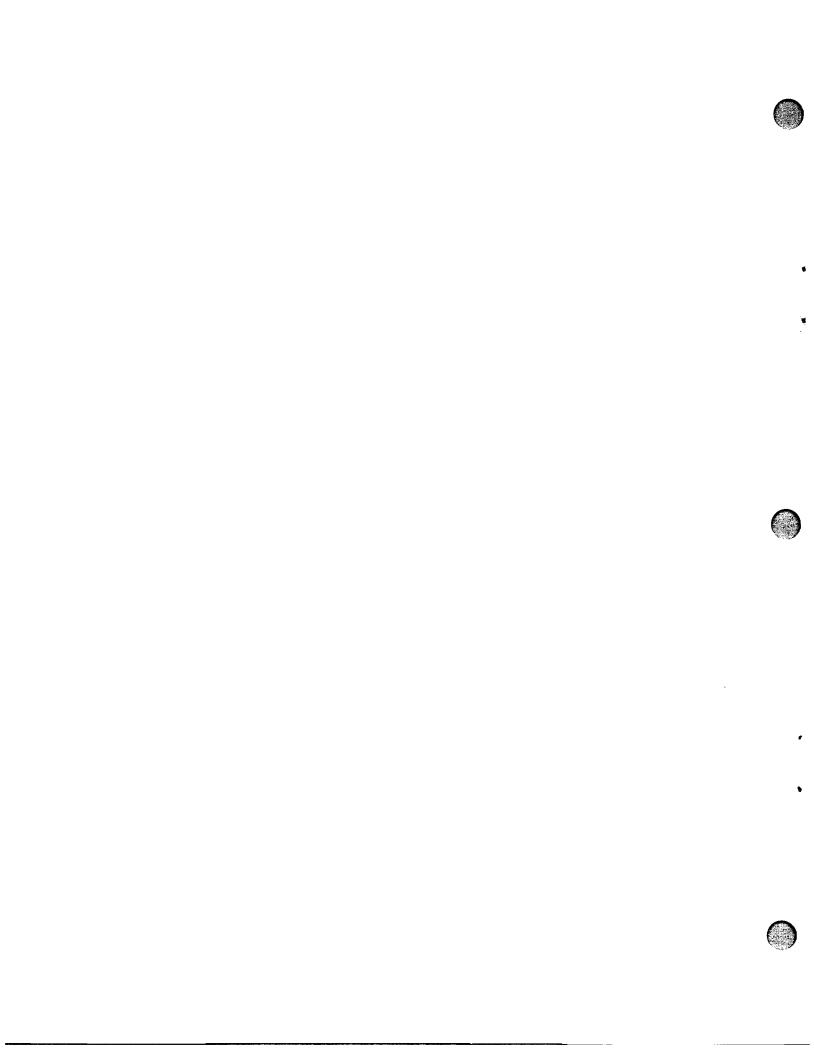
		-	•
Option 001	HP 02547-6040 Cable to interconnect with HP 5050A/B or HP 562A Digital Recorder. Includes logic compatibility (+12V-true output).	Option 005	HP 12798-60009 Cable to interconnect with HP 6130B or HP 6131B Voltage Source. Includes logic compatibility (ground-true output).
Option 002	HP 12798-60010 BCD Output Card (positive-true output) to interface with HP 2759B Synthesizer Programmer.	Option 007	HP 12798-60005 Cable to interconnect with HP 5325B Universal Counter. Includes logic compatibility (ground-true output).
	The HP 2759B is supplied with an interconnecting cable assembly which mates with the BCD output card.	Option 008	HP 12798-60006 Cable to interconnect with HP 5326A/B Timer/Counter/DVM. Includes logic compatibility (ground-true output).
Option 003	HP 12798-60002 Cable to interconnect with HP 3450A Multi-Function Meter. Includes logic compatibility (positive-true output).	Option 010	HP 02547-6040 Cable to interconnect with HP 5055A Digital Recorder. Includes logic compatibility (positive-true output).
Option 004	HP 12798-60003 Cable to interconnect with HP 3482A/84A Plug-In in an HP 3480B Digital Voltmeter. Includes logic compatibility (ground-true	Option 020	a) BCD Output Card (ground-true output). HP Part No. 12798-60011.
	output).		b) Connector Kit, HP Part No. 02116-6178.

Table 1-2. HP 12798B Specifications

DATA OUTPUT		STORE DATA (to	external device)
Number of Digits:	10	Amplitude:	+12V ±1V or +5V ±0.5V (Jumper selectable through
Bit Voltage, HP 12798-60010 (Positive-true	Logic ''1'' $+5V \pm 0.5V$ through 10K ohm resistor		330 ohms)
output):	Logic "0" +0.4V ±0.4V	Duration:	400 ms max., controlled in-
	(Logic ''1'' is jumper select- able through 10K ohm to +12V ±1V)		ternally. Can be reset after 10 us via external Busy signal
		Rise Time:	$>$ 1V per μ s
Bit Voltage, HP 12798-60012 (+12V-true output):	Logic ''1'' +12V ±1.0V through 10K ohm resistor	Coupling:	de
	Logic ''0'' +0.4V ±0.4V		
	(Logic "1" is jumper select- able through 10K ohm to +5V ±0.5V)	Output Impedance:	<480 ohms
		Current Sink:	\leq 3.6 mA
Bit Voltage, HP 12798-60011	Logic "1" $+0.4V \pm 0.4V$		
(Ground-true output):	Logic "0" +5V ±0.5V through 10K ohm resistor	REFERENCE VOL	rages supplied
	(Logic ''0'' is jumper selectable through 10K ohm to	High Level: +7	7.2V ±1.0V
	+12V ±1V)	Low Level: +0	0.7V ±0.3V

Table 1-2. HP 12798B Specifications (Continued)

BUSY (From extern	al device)	"K"	(Continuous Output) Input data words from the data
Amplitude:	High: $+10.0V\pm6.5V$ Low: $<+0.8V$		bus and continue to output these words to an external device until commanded to stop by an
Duration:	≥ 2 0 µ s		"N" command. This command is not reset after one data word
Input Impedance:	>1500 ohms		but is reset by an "N" com- mand. The "K" command is
Pull-Up Resistors:	+5V or $+16V$ is provided.		inoperable in the PGM (pro- gram) mode of operation.
	Minimum resistance: 100 ohms to ±5V or 3.9K ohms to ±16V	N	(Negate the ''K'' command.) Stops the output of data until another ''I'' or ''K'' command is issued.
COMMANDS		PHYSICAL	The BCD Output Card plugs in-
''I''	(Single Word Output) Input one word of data from the data bus and output this	COMPATIBILITY	to any I/O slot in the HP 2570A or HP 2575A.
	data to an external device. This command is reset after	WEIGHT	Net: 3 lbs (1,4 kg)
	one word is output.		Shipping: 5 lbs (2,5 kg)



12798B Section II

SECTION II

INSTALLATION AND OPERATION

2-1. UNPACKING AND INSPECTION

2-2. If the shipping package is damaged, ask that the carrier's agent be present when the kit is unpacked. Carefully unpack the PC card and any accessories and inspect for damage (scratches, broken components, etc.) If the card is damaged or fails to meet specifications, notify the carrier and nearest Hewlett-Packard Sales and Service Office immediately (Sales and Service Offices are listed at the back of the manual). Retain the shipping material for the carrier's inspection.

2-3. ACCEPTANCE CHECK

2-4. Check the performance of the kit as soon as possible after receipt. The instructions provided in Paragraphs 2-38 through 2-49 will verify operation of the kit.

2-5. REPACKING FOR SHIPMENT

2-6. If the kit is to be shipped to Hewlett-Packard for service or repair, wrap carefully in heavy paper. Use adequate packing material around the PC card. Mark package with "Fragile", etc. Additional packaging information can be found in Section II of the HP 2570A and HP 2575A Manuals.

NOTE

In any correspondence regarding the kit, identify the PC card by model number, revision code number, and serial number. See Figure 4-2 for the location of these numbers. If you have any questions, please contact your local Hewlett-Packard Sales and Service Office.

2-7. CABLE FABRICATION

2-8. Since the BCD output card is designed for use as an interface for a variety of equipment, it may be necessary to prepare an interconnecting cable for the particular device being used. Connector kit HP Part No. 02116-6178 is furnished as part of kit HP 12798B for this purpose. The 48-pin connector in the kit slides onto the front of the BCD output card containing 48 printed-circuit paths (24 on each side of the card). Table 2-1 lists the signals to and from the card as an aid in the preparation of the interconnecting cable. Table 2-2 explains how to fabricate the cable and Table 2-3 is a parts list for the connector kit. Figure 2-1 depicts details of cable assembly.

2-9. INTERCONNECTING CABLES

2-10. Tables 2-4 through 2-9 provide wiring information for interconnect cables available for use with the card. See Table 1-1. In each case connector A mates with the BCD output card and connector B mates with the interfaced device. The BCD output card has the capability of transmitting 10 digits of BCD information. The BCD output column in the tables identifies these digits. Note that the most significant character is loaded first and the least significant character is loaded last. The number of digits transmitted will depend on the character word output by the coupler/controller.

2-11. BCD OUTPUT CARD

2-12. GENERAL

2-13. Paragraphs 2-14 through 2-21 in this section provide the user with an overall description of the BCD output card as an aid to interfacing a digital recorder (record mode) or programming an instrument (program mode) with the HP 2570A. Included are a basic description of the operating principles of the card, commands, and typical system operation.

2-14. OPERATING PRINCIPLES

- 2-15. Upon command from the HP 2570A Coupler/Controller, the BCD output card translates ASCII signals present at the backplane to 8421 BCD and these are sequentially clocked by a decade counter, digit by digit (most significant digit first) into a storage/output register on the card. The operation is controlled by strobe pulses generated by the HP 2570A control logic; one strobe signal pulses the first BCD digit into storage and at the end of the character, another pulse advances the decade counter. Successive cycles clock information into the register in a similar manner. A total of 10 4-bit data latches in the register permit up to 10 digits to be stored.
- 2-16. When the jumper programmable record mode is selected, the last digit in the word is accompanied with or followed by a command character which causes the BCD output card to initiate transfer of a Store Data command to the recording device. Attempts to input more than 10 digits will result in the 11th and succeeding digits being truncated provided all the characters are permissible characters (any number, a space, a minus sign, or the letter E). When the recording device receives the Store Data command, it returns a Busy signal to the BCD output card. If another command attempts to input data to the card, the Busy signal activates a circuit that halts the main control program of the HP 2570A, preventing the sampling of further data until the recording device has

Table 2-1. HP 12798B 48-Pin PC Connector Pin Assignment

BCD OUTPUT SIGNALS

Pin	Record/Program Character	BCD Weight	Pin	Record/Program Character	BCD Weight
19 20 W X	Most significant Digit 9 (Loaded First)	$\left\{\begin{array}{c}1\\2\\4\\8\end{array}\right.$	9 10 K L	4	$\left\{\begin{array}{c}1\\2\\4\\8\end{array}\right.$
17 18 U V	8	$\left\{\begin{array}{c}1\\2\\4\\8\end{array}\right.$	7 8 H J	3	$\left\{\begin{array}{c}1\\2\\4\\8\end{array}\right.$
15 16 S T	7	\begin{cases} 1 & 2 & 4 & 8 & 8 & 8 & 8 & 8 & 8 & 8 & 8 & 8	5 6 E F	2	\begin{cases} 1 & 2 & 4 & 8 & 8 & 8 & 8 & 8 & 8 & 8 & 8 & 8
13 14 P R	6	\begin{cases} 1 & 2 & 4 & 8 & 8 & 8 & 8 & 8 & 8 & 8 & 8 & 8	3 4 C D	1	$\left\{\begin{array}{c}1\\2\\4\\8\end{array}\right.$
11 12 M N	5	$\left\{\begin{array}{c}1\\2\\4\\8\end{array}\right.$	1 2 A B	Least significant Digit Ø (Loaded Last)	$\left\{\begin{array}{c}1\\2\\4\\8\end{array}\right.$

CONTROL SIGNALS

Pin	Signal						
AA	Reference Voltage, High Level						
23	Reference Voltage, Low Level						
22	Store Data (To External Device)						
BB	Ground						
21	Busy (From External Device)						

NOTE

48-pin PC connector illustrated above is shown viewed from the component side of the card.

Table 2-2. Cable Fabrication Instructions

STEP

INSTRUCTION

- Insert approximately 10 inches of cable into hood.
- 2. Strip outer jacket of cable back five inches.
- 3. Start making connections at the bottom of the connector by soldering first wire to pin BB (ground). Continue connecting successive wires to the next pins, following the order 24, AA, 23, Z, 22, etc.
- After all wires have been connected, carefully insert connector into hood and attach with self-tapping screws. See Figure 2-1.
 Install cable clamp

ECAUTION

Hood is made of plastic. Do not force screws or cable clamp. Firm tightening is sufficient. Overtightening will split the connector hood.

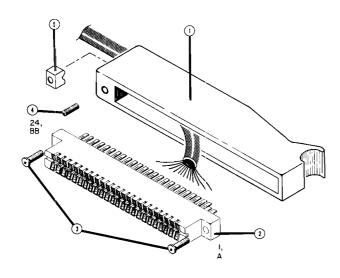


Figure 2-1. Cable Assembly Diagram

Table 2-3. Connector Kit, HP Part No. 02116-6178

ITEM	QTY	DESCRIPTION	PART NO.
1	1	Hood	02116-4001
2	1	Connector, 48 Pin	1251-0335
3	2	Tapping Screw	0624-0096
4	1	Set Screw	3030-0143
5	1	Cable Clamp	02116 -4003

completed the printout. Thereafter, the Busy signal is released indicating that the device is ready for more data, and the main program of the HP 2570A can continue.

2-17. When the jumper programmable program mode is selected, a signal accompanying or following the last digit of the entered word initiates a Store Data command. This causes the programmed device to execute the program. Depending on the positions of jumpers W9 and W51, the card may halt the main instruction program of the HP 2570A until the instrument is programmed. If an attempt is made to alter the output register code before a Busy pulse is released, the HP 2570A is halted. If the programmed instrument is not capable of generating a Busy signal, the BCD output card will allow programming to resume approximately 300 ms after the Store Data pulse. The information stored in the data register is retained until the card is addressed with another command from the HP 2570A.

2-18. COMMANDS

- 2-19. The following three commands issued by the HP 2570A control card pinboard matrix (or any program source interfaced with the HP 2570A), are applicable to the BCD output card:
- I Input one word of data (1 to 10 digits) from the data bus and output this data to an external device. This command is reset after one word is input.
- K Input data words from the data bus and continue to output these words to an external device until commanded to stop by an N command. This command is not reset after one data word, but is reset by an N command. The K command is not operable for the program (PGM) mode of operation.
- N Reset K command. Stops output of data to the external device.

2-20. TYPICAL SYSTEM APPLICATIONS

2-21. Figures 2-2 through 2-4 show three typical system applications of the BCD output card. The first example illustrates the card being operated in the record (RCD) mode; the remaining examples show the card set for the program (PGM) mode, and remotely programming instruments by means of their control lines. Each illustration includes: a description of the system, a block diagram of the coupler/controller showing slot location of the I/O cards, system programming and BCD output card jumper settings.

2-22. PROGRAMMING



When interfacing the coupler/controller with microcircuit devices, ensure that level select jumper W50 described in Table 2-10 is in the 5V position before applying power to the coupler/controller. Failure to do so may result in damage to the associated equipment.

Section II 12798B

2-23. GENERAL INFORMATION

2-24. The BCD output card has two basic operating modes - Record (RCD) and Program (PGM). selected by jumper W5 on the card. These modes are defined as follows:

Record (RCD)

Program (PGM)

Used when card is employed to interface a parallel input recording device such as a digital recorder. The card accepts space, minus and decimal point and outputs a modified 4-bit code for each character.

Used when card is required to program an instrument. The card ignores any space, minus or decimal point information.

2-25. Programming the BCD output card consists of choosing the desired mode of operation for the card and then placing the circuit board jumpers in the required positions for the desired operation.

Table 2-10 defines the functions of the jumpers on the card. Consult Figures 2-5 through 2-12 for programming information required to interface HP instruments listed in Table 1-1. Information provided includes details of instrument programming or printout format, interface cable required, BCD output card jumper settings, and examples of typical programming.

2-26. CARD INSTALLATION

CAUTION

Always set the coupler/controller LINE ON/OFF switch to OFF before installing or removing cards. Failure to observe this precaution may result in damage to the equipment.

2-27. HP 2570A COUPLER/CONTROLLER

- 2-28. The HP 12798B BCD Output Card may be inserted into any one of the eight I/O slots in the HP 2570A; the choice is arbitrary and is determined by the person preparing the system program.
- 2-29. To install the card, proceed as follows:
- a. Set the LINE ON/OFF switch on the front of the HP 2570A cabinet to OFF and open the front panel of the HP 2570A.
- b. Hold the BCD output card vertical with the component side facing right and the plastic card extractors facing away from the HP 2570A mainframe.
- c. Carefully slide the card forward into the desired slot.
- d. Check that the card is fully inserted into the connector on the backplane by pressing on the card extractors.

- 2-30. HP 2575A COUPLER/CONTROLLER
- 2-31. The HP 12798B BCD Output Card may be inserted into any of the four I/O slots in the HP 2575A.
- 2-32. To install the BCD output card, carry out the following steps:
- a. Set the LINE ON/OFF switch on the front panel of the HP 2575A to OFF.
- b. Locate the rear cover on the back of the instrument. See Figure 2-3 in the HP 2575A Operating and Service Manual. Release the rear cover latches and remove the cover.
 - c. Remove the I/O card retaining bracket.
- d. Hold the BCD output card horizontally with the component side facing upward and the plastic card extractors facing away from the HP 2575A.
- e. Carefully slide the card forward into the desired slot. The card I/O slot numbers ("Addresses") are marked on the rear panel.
- f. Check that the card is fully inserted into the connector on the mother board by pressing on the card extractors.
 - g. Replace the I/O card retaining bracket.

2-33. CABLE INSTALLATION



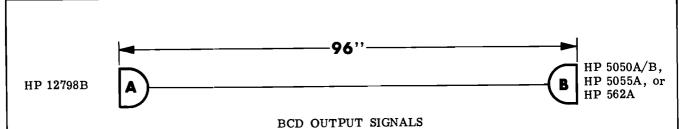
Always set the coupler/controller LINE ON/OFF switch to OFF and de-energize all associated equipment before installing or removing cables. Failure to observe this precaution may result in damage to the equipment.

- 2-34. HP 2570A COUPLER/CONTROLLER
- 2-35. To connect a peripheral device to a BCD output card installed in the HP 2570A, carry out the following steps:
- a. Set the HP 2570A LINE ON/OFF switch to OFF and open front panel.
- b. Pass the cable connector which mates with the BCD output card forward through the slot at the rear of the HP 2570A cabinet.
- c. Carefully slide the cable connector onto the pin connections on the front of the BCD output card. Check that the card is firmly seated in the backplane connector.
- d. Pull back any excess cable from the rear of the HP 2570A and connect the other end of the cable to the input/output device.





Table 2-4. Cable HP Stock No. 02547-6040 Wiring Information (Part of Option 001 and 010, HP 12798B - HP 5050A/B, HP 5055A, HP 562A Interface)



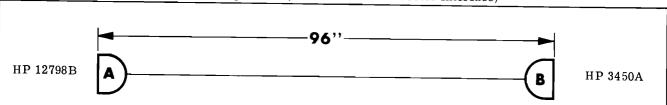
Ass	Pin Assign- ment HP 1279 BCD Ou			HP 5050A/B and HP 5055A Digital Recorder	Pi Assi me	gn-	HP 1279 BCD Ou		HP 5050A/B and HP 5055A Digital Recorder	
A	В	Character	BCD Weight	Printout Columns (Note 1)	A	В	Character	BCD Weight	Printout Columns (Note 1)	
19 20 W	19 20 44 45	Most Significant Digit 9 (Loaded First)	$\begin{cases} 1\\2\\4\\8 \end{cases}$	Most Significant Digit 1Ø (Loaded First)	9 10 K L	9 10 34 35	4	$\begin{cases} 1\\2\\4\\8 \end{cases}$	5	
17 18 U V	17 18 42 43	8	$ \begin{cases} 1 \\ 2 \\ 4 \\ 8 \end{cases} $	9	7 8 H J	7 8 32 33	3	$\begin{cases} 1 \\ 2 \\ 4 \\ 8 \end{cases}$	4	
15 16 S T	15 16 40 41	7	\begin{cases} 1 & 2 & 4 & 8 & 8 & 8 & 8 & 8 & 8 & 8 & 8 & 8	8	5 6 E F	5 6 30 31	2		3	
13 14 P R	13 14 38 39	6	$ \begin{cases} 1 \\ 2 \\ 4 \\ 8 \end{cases} $	7	3 4 C D	3 4 28 29	1	$ \begin{cases} 1 \\ 2 \\ 4 \\ 8 \end{cases} $	2	
11 12 M N	11 12 36 37	5	$ \begin{cases} 1 \\ 2 \\ 4 \\ 8 \end{cases} $	6	1 2 A B	1 2 26 27	Least Significant Digit Ø (Loaded Last)	$ \begin{cases} 1 $	Least Significant Digit 1 (Loaded Last)	

INTERFACE CONTROL SIGNALS

Α	В	Signal
21	22	Busy
22	23	Store Data
23	24	Reference Voltage, Low Level
AA	25	Reference Voltage, High Level
ВВ	50	Ground

- Order of column printout may be changed internally in the HP 5050A/B and HP 562A.
- 2. The cable contains 3 spare wires, connected from pins Y, Z, and 24 of connector A to pins 47, 48, and 46, respectively, of connector B.
- 3. A parts list for the cable is contained in Table 5-1.

Table 2-5. Cable HP Stock No. 12798-60002 Wiring Information (Part of Option 003, HP 12798B - HP 3450A Interface)



BCD OUTPUT SIGNALS

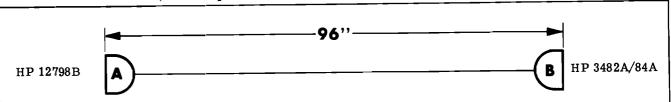
							_			
As	Pin sign- ent	HP 12' BCD O		HP 3450A Control Signals	As	Pin sign- nent	HP 12 BCD (HP 3450A	
A	В	Character	BCD Weight	Control Signals	A	В	Character	BCD Weight	Control Signals	
19 20 W X	1 16	Most Significant Digit 9 (Loaded First)	$\left\{\begin{array}{c}1\\2\\4\\8\end{array}\right.$	1/60s Gate Remote Control	9 10 K L	10 11 28 29	4	$ \begin{cases} 1 $	Decimal Point Display	
17 18 U V	2	8	$\left\{\begin{array}{c}1\\2\\4\\8\end{array}\right.$	10M ohm Input	7 8 H J	22	3	$ \begin{cases} 1 \\ 2 \\ 4 \\ 8 \end{cases} $	Function	
15 16 S T	20	7	$ \begin{cases} 1 \\ 2 \\ 4 \\ 8 \end{cases} $	Integration Delay	5 6 E F	3 4 21	2	$ \begin{cases} 1 \\ 2 \\ 4 \\ 8 \end{cases} $	Function	
13 14 P R	12	6	$\begin{cases} 1 \\ 2 \\ 4 \\ 8 \end{cases}$	Internal Trig. Inhibit	3 4 C D	5 6 23 24	1	$\begin{cases} 1\\2\\4\\8 \end{cases}$	Non-Ratio Range	
11 12 M N	14	5	$\left\{\begin{array}{c}1\\2\\4\\8\end{array}\right.$	Manual Trig. Inhibit	1 2 A B	8 9 26	Least Significant Digit Ø (Loaded Last)	$\left\{\begin{array}{c}1\\2\\4\\8\end{array}\right.$	Ratio Range	

INTERFACE CONTROL SIGNALS

A	В		Signal	
ВВ	36	Ground		_
		_		

- 1. The cable contains 3 spare wires, connected to pins B, F, and P of connector A. The wires are unconnected at connector B.
- 2. A parts list for the cable is contained in Table 5-1.

Table 2-6. Cable HP Stock No. 12798-60003 Wiring Information (Part of Option 004, HP 12798B - HP 3482A/84A Interface)



BCD OUTPUT SIGNALS

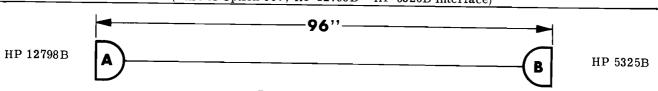
Ass	in ign- ent	HP 12798 BCD Outp		HP 3482A/3484A Control Signals	Pi Assi me	gn-	HP 127 BCD O		HP 3482/3484A Control Signals
A	В	Character	BCD Weight	Control biginas	A	В	Character	BCD Weight	
19 20 W		Most Significant Digit 9 (Loaded First)	$\left\{\begin{array}{c}1\\2\\4\\8\end{array}\right.$		9 10 K L	16 15	4	$\left\{\begin{matrix}1\\2\\4\\8\end{matrix}\right.$	Filter A Filter B
17 18 U V	6 7 8 9	8	\begin{cases} 1 & 2 & 4 & 8 & 8 & 8 & 8 & 8 & 8 & 8 & 8 & 8	100V/100K ohm 10V/10K ohm 1000 mV/1000 ohm 100 mV/100 ohm	7 8 H J		3		(Note 3)
15 16 S T	4	7	$\left\{\begin{array}{l}1\\2\\4\\8\end{array}\right.$	1000K ohm (Note 1)	5 6 E F		2	$\begin{cases} 1\\2\\4\\8 \end{cases}$	(Note 3)
13 14 P R	1 2 3	6	$ \begin{cases} 1 \\ 2 \\ 4 \\ 8 \end{cases} $	VAC (AC) VAC (DC) Ohms (Note 1)	3 4 C D		1	$\left\{\begin{array}{c}1\\2\\4\\8\end{array}\right.$	(Note 3)
11 12 M N	5	5	$\left\{\begin{array}{c}1\\2\\4\\8\end{array}\right.$	Ratio (Note 2)	1 2 A B		Least Significant Digit Ø (Loaded Last)	$ \begin{cases} 1 \\ 2 \\ 4 \\ 8 \end{cases} $	(Note 3)

INTERFACE CONTROL SIGNALS

	A	В	Signal					
	21	20	Busy (Program Flag)					
ı	22	14	Store Data (Program Accept)					
	вв	2 5	Ground					

- 1. HP 3484A only.
- 2. HP 3480A/B Option 002 required.
- 3. Not normally used.
- 4. A parts list for the cable is contained in Table 5-1.

Table 2-7. Cable HP Stock No. 12798-60005 Wiring Information (Part of Option 007, HP 12798B - HP 5325B Interface)



				BCD	OUT	PUT S	SIGNALS		_	
Pin Assign- ment		HP 127 BCD O		HP 5325B Control Signals		Pin sign- nent	HP 12' BCD O		нР 5325B	
A	В	Character	BCD Weight	omi of Digmas	A	В	Character	BCD Weight	Control Signals	
19 20 W X	33 34	Most Significant Digit 9 (Loaded First)	$\begin{cases} 1\\2\\4\\8 \end{cases}$	+Slope A -Slope A	9 10 K L	44 18 43	4	$\begin{cases} 1\\2\\4\\8 \end{cases}$	$0.1 \text{ ms/}10^3$ $1 \text{ ms/}10^4$ $10 \text{ ms/}10^5$	
17 18 U V	32 35	8	$ \begin{cases} 1 \\ 2 \\ 4 \\ 8 \end{cases} $	+Slope B -Slope B	7 8 H J	17 42 16	3	$\begin{cases} 1\\2\\4\\8 \end{cases}$	$0.1s/10^{6}$ $1s/10^{7}$ $10s/10^{8}$	
15 16 S T	22 23 47	7	$\begin{cases} 1 \\ 2 \\ 4 \\ 8 \end{cases}$	Frequency A Period A Check	5 6 E F	7 9 8	2	$\left\{\begin{array}{c}1\\2\\4\\8\end{array}\right.$	DC, Chan. A X10, Chan. A X100, Chan. A	
13 14 P R	24 49 48	6	$\left\{\begin{matrix} 1\\2\\4\\8\end{matrix}\right.$	Period Avg. A Ratio A/B Time Interval A to B	3 4 C D	10 31 12	1	$\begin{cases} 1\\2\\4\\8 \end{cases}$	DC, Chan. B X10, Chan. B X100, Chan. B (Note 1)	
11 12 M N	20 45 19	5	$\left\{\begin{matrix}1\\2\\4\\8\end{matrix}\right.$	0.1 μs/1 1 μs/10 10 μs/10 ²	1 2 A B		Least Significant Digit Ø (Loaded Last)	$\begin{cases} 1 \\ 2 \\ 4 \\ 8 \end{cases}$		

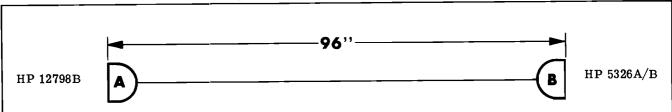
NOTES

INTERFACE CONTROL SIGNALS

A	В	Signal
вв	26	Ground

- 1. HP 5325B Option 001 required.
- 2. The cable contains 9 spare wires, connected to pins 1, 2, A, B, D, F, U, X, and W of connector A. The wires are unconnected at connector B.
- 3. A parts list for the cable is contained in Table 5-1.

Table 2-8. Cable HP Stock No. 12798-60006 Wiring Information (Part of Option 008, HP 12798B - HP 5326A/B Interface)



BCD OUTPUT SIGNALS

Assi	Pin Assign- ment HP 12798 BCD Outp			HP 5326A/B Control Signals	Ass	in ign- ent	HP 1279 BCD Ou		HP 5326A/B Control Signals
A	В	Character	BCD Weight		A	В	Character	BCD Weight	
19 20 W	17 14	Most Significant Digit 9 (Loaded First)	$\begin{cases} 1\\2\\4\\8 \end{cases}$	External Check	9 10 K L	22 23 24	4	$\begin{cases} 1\\2\\4\\8 \end{cases}$	0.1 ms/10 ³ 1 ms/10 ⁴ 10 ms/10 ⁵
17 18 U V	6 7 5	8	$\begin{cases} 1\\2\\4\\8 \end{cases}$	Frequency A Frequency C Period	7 8 H J	25 26 27	3	$\begin{cases} 1\\2\\4\\8 \end{cases}$	0.1s/10 ⁶ 1s/10 ⁷ 10s/10 ⁸
15 16 S	2 4 3	7		Period Average A Time Interval A to B Time Interval Avg.	5 6 E F	28	2	$\begin{cases} 1 \\ 2 \\ 4 \\ 8 \end{cases}$	-Slope A
13 14 P R	8 9 10	6	$ \begin{cases} 1 \\ 2 \\ 4 \\ 8 \end{cases} $	Read A Read B DVM (Note 1)	3 4 C D	29	1		-Slope B
11 12 M N	19 20 21	5	$\begin{cases} 1\\2\\4\\8 \end{cases}$	0.1 μs/1 1 μs/10 10 μs/10 ²	1 2 A B	11 12 13	Least Significant Digit Ø (Loaded Last)	$\begin{cases} 1 \\ 2 \\ 4 \\ 8 \end{cases}$	10V 100V 1000V (Note 1)

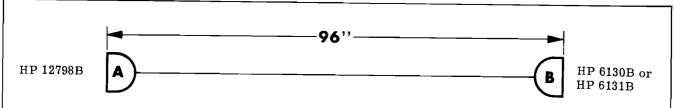
NOTES

INTERFACE CONTROL SIGNALS

A	В	Signal
ВВ	18	Ground

- 1. HP 5326B only.
- 2. The cable contains 9 spare wires, connected to pins B, D, C, 4, F, E, 6, X, and W of connector A. The wires are unconnected at connector B.
- 3. A parts list for the cable is contained in Table 5-1.

Table 2-9. Cable Stock No. 12798-60009 Wiring Information (Part of Option 005, HP 12798B - HP 6130B, HP 6131B Interface)



BCD OUTPUT SIGNALS

Ass	in ign- ent	HP 127 BCD O		HP 6130B/6131B Control Signals	As	Pin sign- ient	HP 12 BCD C		HP 6130B/6131B Control Signals				
A	В	Character	BCD Weight	Control digitals	A	В	Character	BCD Weight		roi Signais			
19 20 W		Most Significant Digit 9 (Loaded First)	$ \begin{cases} 1 \\ 2 \\ 4 \end{cases} $		9 10 K	43 44 45	4	$\begin{cases} 1 \\ 2 \\ 4 \end{cases}$	B1 B2 B4	(Note 1)			
X			l 8		L	46		(8	В8				
17 18 U	22 23 24	8	$\left\{egin{array}{c} 1 \ 2 \ 4 \end{array} ight.$	30 mA 50 mA Latch Range Program	7 8 H	39 40 41	3	$\begin{cases} 1 \\ 2 \\ 4 \end{cases}$	C1 C2 C4	(Note 1)			
V			<u> 8</u>		J	42		<u></u> 8	C8 J				
15 16 S T	20	7	$egin{cases} 1 \ 2 \ 4 \ 8 \end{cases}$	Voltage Range	5 6 E F	35 36 37 38	2	$\left\{\begin{array}{c}1\\2\\4\\8\end{array}\right.$	D1 D2 D4 D8	(Note 1)			
13 14 P R	34	6	$ \begin{cases} 1 \\ 2 \\ 4 \\ 8 \end{cases} $	Voltage Sign	3 4 C D		1	$ \begin{bmatrix} 1 \\ 2 \\ 4 \\ 8 \end{bmatrix} $					
11 12 M N	47 48 49 50	5	$ \begin{cases} 1 \\ 2 \\ 4 \\ 8 \end{cases} $	A1 A2 A4 A8 (Note 1)	1 2 A B		Least Significant Digit Ø (Loaded Last)	$\begin{cases} 1 \\ 2 \\ 4 \\ 8 \end{cases}$					

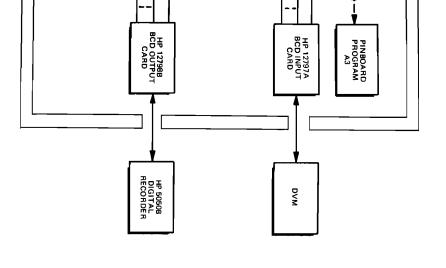
INTERFACE CONTROL SIGNALS

A	В	Signal
24	21	Program Source Select
	2 5	Cable Continuity Interlock
22	32	Store Data (Gate)
21	33	Busy (Flag)
BB	26	Ground

- 1. Voltage magnitude: A is most significant digit and D is least significant digit.
- 2. A parts list for the cable is contained in Table 5-1.

HP 12798-60012

JUMPER SETTINGS

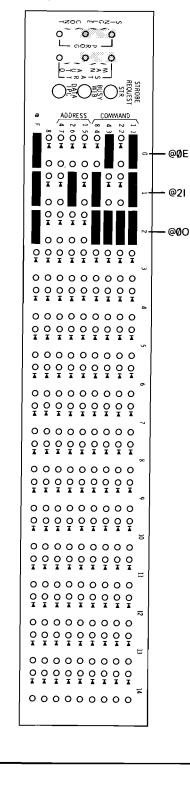


COUPLER/CONTROLLER PROGRAM

8	н	Ø	Step	HP 257
@ Ø O	@21	@ØE	Instruction	HP 2575A Pinboard Matrix
Output data from slot \emptyset (HP 12797A) to the HP 2575A backplane. If no data is available, the main program will wait until data is available and then transmitted to the backplane.	Command slot 2 (HP 12798B) to input data from the HP 2575A backplane and print this data on the digital recorder.	Command DVM connected at slot \emptyset (HP 12797A) to encode (take reading).		Function

0009-86251 **(4)** ● 5v W25 H W40 H ¥43 • ₩42 • W37 ₩33 • H # 62M W28 83 W32 • H ₩21 **•** W20 ¥12 • W35 H W31 H W27 WIA H ¥49 O₂ w₃ %51 ₹0

HP 2575A PINBOARD MATRIX



ard matrix instruction program imper settings required to in are shown opposite. Note that cam jumper is set to the SINGLE

corder,

HP 2575A Coupler/Controller is a digital voltmeter and an corder. The DVM is interfaced HP 12797A BCD Input Card and is interfaced at I/O slot 2 with Output Card. It is desired to con the DVM and record the re-

Figure 2-2. HP12798B Application No. 1

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In this example, the HP 2575A Coupler/Controller is being used to control a digital voltmeter and an HP 5050B Digital Recorder. The DVM is interfaced at I/O slot \emptyset with an HP 12797A BCD Input Card and the digital recorder is interfaced at I/O slot 2 with the HP 12798B BCD Output Card. It is desired to take a single reading on the DVM and record the result on the digital recorder.

The HP 2575A pinboard matrix instruction program and the HP 12798B jumper settings required to achieve this operation are shown opposite. Note that the HP 2575A PROGram jumper is set to the SINGLE position.

COUPLER/CONTROLLER PROGRAM

HP 257	HP 2575A Pinboard Matrix	Function
Step	Instruction	
Ø	@ØE	Command DVM connected at slot \emptyset (HP 12797A) to encode (take reading).
1	@21	Command slot 2 (HP 12798B) to input data from the HP 2575A backplane and print this data on the digital recorder.
ю	@ØO	Output data from slot Ø (HP 12797A) to the HP 2575A backplane. If no data is available, the main program will wait until data is available and then transmitted to the backplane.

9 8011 ¥ (4) W13 ₩. W19 H WIO H # T . ₩49 • □

HP 2575A PINBOARD MATRIX

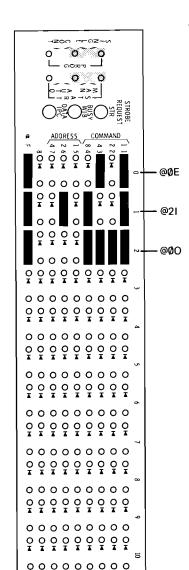


Figure 2-2.

HP 12798B

Application No. 1

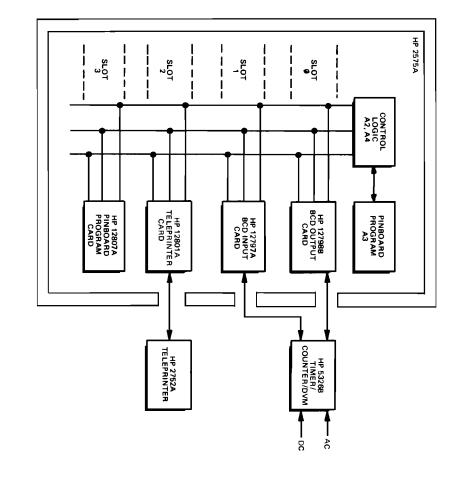
Figure

127

98B Application No. 2

UNFOLD FOR

12798B



In this example, the HP 2575A is being used to control an HP 5326B Timer/Counter/DVM and an HP 2752A Teleprinter. The Timer/Counter/DVM is interfaced with an HP 12797A BCD Input Card (to input data) at I/O slot 1 and an HP 12798B BCD Output Card (for instrument programming) at I/O slot Ø. The teleprinter is interfaced at I/O slot 2 with an HP 12801A Teleprinter Card and an HP 12807A Pinboard Program Card located in I/O slot 3 augments the programming capability of the HP 2575A.

It is desired to operate the system as follows:

- a. Program the Timer/Counter DVM to measure an external ac signal at input channel A using a 1ms time base and triggering on the negative slope of the input signal at channel A.
- b. Take six consecutive readings on the Timer/Counter/DVM and print out the test results in three lines on the teleprinter, as shown below:

c. Change the Counter/DVM program to measure a slowly changing voltage at the DVM input using a 0.1s time base and a range setting of 10V.

d. Take six consecutive readings on the Timer/Counter/DVM and print out the test results in three lines on the teleprinter, as shown below:

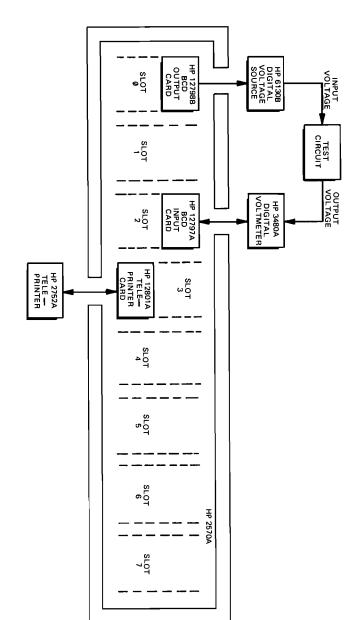
e. Return system to a system normalize (reset) condition.

The HP 2575A pinboard matrix instructions, HP 12807A Pinboard Program Card instructions, and HP 12798B BCD Output Card jumper settings required to achieve this operation are shown opposite. Note that the @ (flag) bit is set for instructions and not set for ASCII characters. Also, note that the HP 2575A PROGram Jumper is set to SINGLE.

COUPLER/CONTROLLER PROGRAM

HP 2575A assumes control. Since PROGram Jumper is set to SINGLE. system halts.					Command slot 3 (HP 12807A) to select sub- routine A (starting at step 0) and take control with instruction programmed at step 0.			@3C	ω
Blank Program Step - control is automatically transferred back to HP 2575A.		8			Blank Program Step - control is automatically transferred back to the HP 2575A.		&		
Similar to previously described Hr 12807A Program Steps Ø through 7.	@010 @010	76543			Output data from slot 1 (HP 12797A) to the HP 2575A backplane. If no data is available, the main program will wait until data is available and data transmitted to the backplane.	@10	7		
	@@@ @2K BE	2 - 6			Command Timer/Counter/DVM connected at slot 1 (HP 12797A) to encode (take a reading).	@1E	თ		
Command slot 3 (HP 12807A) to select subroutine A (sarting with step \emptyset) and take control with instruction programmed at step \emptyset .			@3C	7	Output data from slot 1 (HP 12797A) to the HP 2575A backplane. If no data is available, the main program will wait until data is available and then transmitted to the backplane.	@10	IJ		
Blank Program Step - control is automatically transferred back to HP 2575A.		8			at slot 1 (HP 12797A) to encode (take a reading).	,			
Similar to previously described HP 12807A Program Steps I through 7.	@@@@@@ @@10 @10	24CC4FOF			Output data from slot 1 (HP 12797A) to the HP 2575A backplane. If no data is available, the main program will wait until data is available and then transmitted to the backplane. Command Timer/Counter/DVM connected	@10	ω 4.		
routine A (starting at step 0) and take control with instruction programmed at step 0.		2	(B)	o	Command Timer/Counter/DVM connected at slot 1 (HP 12797A) to encode (take a reading).	@1E	2		
Blank Program Step - control is automatically transferred back to HP 2575A. Command slot 3 (HP 12807A) to select sub-		39))	,	Command slot 2 (HP 12801A) to input data from the HP 2575A backplane and print this data on the teleprinter.	@ 2K	1		
\ End of word character	ASCII \	38			Command slot 2 (HP 12801A) to carriage return and line feed the teleprinter.	@2J	Ø		
slopes A and B are not used DVM range is 10V	ASCII Ø ASCII Ø	35 36 37			Command slot 3 (HP 12807A) to select subroutine A (start-at step \emptyset) and take control with the instruction programmed at step \emptyset .			@3C	2
$\begin{pmatrix} \emptyset \\ 1 \end{pmatrix}$ using a 0.1s time base	ASCII 9	3 3 6 4 3 8			Blank Program Step - control is automati- cally transferred back to the HP 2575A.		21		
voltage at DVM input of the HP 5326B	ASCII Ø	3 3 3 6 3			chamel B trigger stope and DVM range are not used End of word character	ASCII Ø	18 19 20		_
1 measure external signal		28			,		17	•	
Remote Program word instructs HP 5326B to:					g using a 1ms time case	ASCII Ø	15 16		
Command slot Ø (HP 12798B) to input the next data word that appears on the HP 2575A backplane and output that data to the HP 5326B Timer (Counter-INVM	@Ø1	27					13		
Command slot 3 (HP 12807A) to take control with instruction programmed at step 27.			@3C	5	_	ASCII 1	11		
Command slot 3 (HP 12807A) to select subroutine E (starting at step 27).			@3E	4	HP 5326B) to:		à		
Blank Program Step - control is automatically transferred back to HP 2575A.		&			Sackplane and output that wate to the nr Sa26B Timer/Counter/DVM.				
	@1E @10	760			Command slot Ø(HP 12798B) to input the next data word that appears on the HP 2575A had large and output that data to the HP	0 ØI	9		
Similar to previously described HP 12807A Program Steps Ø through 7.	9000	з ω 4, π		_	Command slot 3 (HP 12807A) to take control with instruction programmed at step 9.			@3C	1
	@2J @2K @1F	υ μ Ø			Command slot 3 (HP 12807A) to select subroutine B (starting at step 9).			@3B	Ø
	Instruction	Step	Instruction	Step		Instruction	Step	Instruction	Step
Function	HP 12807 Pinboard Card	HP 128	HP 2575A Pinboard Matrix	HP 2575 №	Function	HP 12807 Pinboard Card	HP 128	2575A Pinboard Matrix	HP 257

12798B



4

The example shows the HP 2570A being used to control an HP 3480A Digital Voltmeter, an HP 6130B Digital Voltage Source, and an HP 2752A Teleprinter. The DVM is interfaced at I/O slot 2 with an HP 12797A BCD Input Card, the voltage source at I/O slot \emptyset , with an HP 12798B BCD Output Card, and the teleprinter at I/O slot 3 with an HP 12801A Teleprinter Interface Card.

Using the teleprinter tape reader as a program source the following system operation is desired:

- a. Program the voltage source to apply +5V to the test circuit.
- b. Carriage return and line feed the teleprinter.
- c. Print the heading "5V TEST" on the teleprinter. Carriage return and line feed the teleprinter.
- d. Take a DVM reading of the test circuit output voltage.
- e. Print the test result on the teleprinter.
- f. Change the voltage source output to +6V.
- g. Carriage return and line feed the teleprinter.
- h. Print the heading "6V TEST" on the teleprinter. Carriage return and line feed the teleprinter.
- Take a DVM reading of the test circuit output voltage.
- j. Print the test result on the teleprinter. Carriage return and line feed the teleprinter.
- k. Print the heading "END OF TESTS" on the teleprinter. Carriage return and line feed the teleprinter.

To prepare the system for operation, proceed as follows:

a. Set the teleprinter to LOCAL and energize the tape punch. Punch out the following tape with sufficient leader to form a loop:

@\$1171\$5\$\$\$,@3J@3H5V TEST@3J@3I@2E@2O@\$1171**\$6\$\$\$**,

@3J@3H6V TEST@3J@3I@2E@2O@3J@3HEND OF TESTS@3J

The tape is shown opposite. Note that the teleprinter keyboard carriage return and line feed keys were activated following the second comma in the program, causing CR LF characters to be punched on the tape. These characters are ignored by the system provided the rules noted below the tape illustration are followed.

- b. Splice the tape into a loop and install it on the teleprinter tape reader.
- c. Energize the peripheral equipment and adjust it for correct operation. Set the HP 2570A LINE ON/OFF switch to ON.
- d. Set the teleprinter to LINE and the tape reader to START. $\,$
- e. Type @3C on the teleprinter keyboard. This instructs the teleprinter to take control and cause the instructions programmed on the tape to be carried out. Alternatively, @3C can be programmed on the control card pinboard matrix. In the later case, system operation will begin when the HP 2570A START button is pressed. Operation in both cases will be continuous.

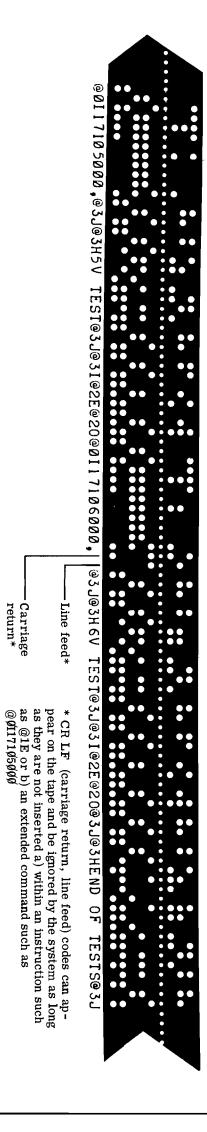
COUPLER/CONTROLLER PROGRAM

Comm return	@3J		
Headin	END OF TESTS	2570A backplane and output that data to the HP 6130B.	
next cl and pr		Command slot \emptyset (HP 12798B) to input the next data word that appears on the HP	@Ø1
Comm	@3H	mitted to the backplane.	
Comm return	@3J	the HP 2570A backplane. If no data is available, the main program will wait until data is available, and then trans-	
backpl		Output data from slot 2 (HP 12797A) to	@20
HP 25' able, t	,	Command HP 3480A connected at slot 2 to encode (take a reading).	@2E
Comm encode Output	@2E @2O	Command slot 3 (HP 12801A) to input data from the HP 2570A backplane and print this data on the teleprinter.	@31
from t data or		Command slot 3 (HP 12801A) to carriage return and line feed the teleprinter.	@3J
Comm	@31	Heading information.	5V TEST
Comm	@3J	characters as heading information and print the data on the teleprinter.	
Headin	6V TEST	Command slot 3 (HP 12801A) to input next	@3H
charac this da		Command slot 3 (HP 12801A) to carriage return and line feed the teleprinter.	@3J
Comm	@3H	, End of word character	ų
Comm turn aı	@3J	_	<i>\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\</i>
, Eı	ÿ	5 Output 5000 mV (5V)	. <i>(</i> € 0.1
O 0	ଷଷଷଠ	1 Enable 7 1000 mA latch 1 X1 Range Ø Plus polarity	7 7 7 7 7 9 9 9 9 9 9 9 9 9 9 9 9 9 9 9
7 10 1 X1 Ø P1	Ø 1 7	Remote programming word instructs HP 6130B to:	
Remot 6130B 1 Er	1	Command slot \emptyset (HP 12798B) to input the next data word that appears on the HP 2570A backplane and output that data to the HP 6130B.	@91
	Instruction/ ASCII Character	Function	Instruction/ ASCII Character

HP 12798-60011 JUMPER SETTINGS

ction/ haracter	Function
	Remote programming word instructs slot HP 6130B to:
	1 Enable 7 1000 mA latch 1 X1 Range Ø Plus Polarity
	6 g Q Output 6000 mV (6V)
	, End of word character
	Command slot 3 (HP 12801A) to carriage return and line feed the teleprinter.
	Command slot 3 (HP 12801A) to input next characters as heading information and print this data on the teleprinter.
EST	Heading information.
	Command slot 3 (HP 12801A) to carriage return and line feed the teleprinter.
	Command slot 3 (HP 12801A) to input data from the HP 2570A backplane and print this data on the teleprinter.
	Command HP 3480A connected at slot 2 to encode (take a reading).
	Output data from slot 2 (HP 12797A) to the HP 2570A backplane. If no data is available, the main program will wait until data is available, and then transmitted to the backplane.
	Command slot 3 (HP 12801A) to carriage return and line feed the teleprinter.
	Command slot 3 (HP 12801A) to input next characters as heading information and print this data on the teleprinter.
F TESTS	Heading information.
	Command slot 3 (HP 12801A) to carriage return and line feed the teleprinter.

PROGRAM TAPE



			_	æ			W8										_	W1 through W4	Jumper
	O16V = Resistor connected to +5V supply. Minimum value 100 ohms.	O5V = Resistor connected to +16V supply. Minimum value 3.9K ohms.	on external busy line.	Terminals permit a pull-up resistor to be connected for use by	$W8 = +5V \pm 0.5V \text{ through } 330 \text{ ohms}$	O = +12V ±1V through 330 ohms 5V 12V	Selects amplitude of Store Data signal programmed by jumper W7.		$\begin{array}{cccccccccccccccccccccccccccccccccccc$	Preset bit pattern 8CD 1248	PROGRAMMING EXAMPLE	<u>ĕ</u>	±0.4V ''0'' = +5V ±0.5V	HP 12798-60010	W2, W3, and W4 similarly program BCD bit 2, BCD bit 4, and BCD bit 8, respectively.	$\frac{W1}{O1} = BCD \text{ bit } 1, "1" \text{ level}$	W1 O $\frac{1}{1}$ = BCD bit 1, "0" level	Select a preset bit pattern for all BCD output digits as shown below:	Function
														-	_		-	W10 through	Jumper
0	Least Significant Digit (Loaded last)	6 \begin{cases} 1 & W34 \\ 2 & W35 & 1 & \begin{cases} 1 & W15 \\ 4 & W36 & 1 & \end{cases} \ 2 & W15 \\ 8 & W37 & \end{cases} \ \ 8 & W17 \end{cases} \]	7 \begin{cases} 1 & W38 & 2 & \begin{cases} 2 & W19 & W20 & 8 & W21 \end{cases} \begin{cases} 2 & W18 & W20 & W21	3 8 4 2	(8 W49 (8	Character BCD Jumper Character Weight Jumper Most C 1 W46 Significant C 2 W47 C 2 W48 C 2 W27 C 2 W48 C 4 W28 C 2 W27 C 2 W48 C 4 W28 C 2 W27 C 2 W48 C 2 W48 C 2 W28 C 2 W48	The individual jumpers are identified in the table below:	Note: Logic "0" = $+12V \pm 1V$ may be selected by moving hard-wired jumper W50 to "12V".	Logic "0" = +0.4V ±0.4V Logic "0" = +5V ±0.5V	Orange Levels and	All jumpers are set to:	HP 12798-60011 CARD	Note: Level is selected by hard-wired jumper W50.	Output levels are: 12798-60010 Card Logic "1" = +5V ±0.5V Logic "0" = +0.4V ±0.4V Logic "0" = +0.4V ±0.4V	H O L	HP 12798-60010 and HP 12798-60012 CARDS All jumpers are set to:	Note: These jumpers are hard-wired in the factory and field changeover is not recommended.		Function
					W7						٠	₩6	5					W5	Jumper
Note: Amplitude of Store Data signal is programmed by jumper W8.		W7 OH	O L = Store Data	WZ	Selects sense of Store Data signal output at card terminal 22.	signal and // allows card to Trigger accept new data		OA = Trigger	v data.	Negative-going Busy trigger resets Inputs Store Data Trigger	OB Tropical	ts Irigger sense of Busy signal applied at card termin			W5 Program mode of operation chosen when	and decimal point and outputs a modified 4-bit code for each character.)	W5 O PGM = Record mode of operation used when card RCD is employed to interface a parallel input re-	Selects operating mode of card.	Function
_	_											W51	, ew					W50	Jumper
W9 OS Wait-Busy (NWIB)	W51		W9 S Wait:Busy		Note: W51 "H" position can only be used with ground-true Busy signals. H Store Data	W9 O S Wait-Busy (NWIB)	Busy 1		W9 O S Wait-Busy	HO Store Data	Delow:	allowing selection of the start and end of the NWIB signal within the Store Data/Busy cycle. The four settings of W9, W51 and corresponding Wait Busy signals selected are detailed	Control action of the Wait-Busy (NWIB) circuitry on the card,	Note: Jumper W50 is hard-wired as follows: 12798-60010 card: 5V 12798-60011 card: 5V 12798-60012 card: 12V Jumper may be set as desired.		5V O12V = +5V ±0.5V through 10K ohms	O 5V 12V = +12V ±1V through 10K ohms	Selects bit voltage level for all BCD output lines.	Function

Figure 2-4. HP 12798B Application No. 3 2-13

2-14

DATA WORD

\$ 8011 A CAD

W13

₩16 ●

● 5V

HP 12798-60012 JUMPER SETTINGS

ad

z	×	12	11	R	P	14	13	т	ß	16	15	V	U	18	17	Х	W	20	19	Pin
		თ				7				œ			,	9		(Loaded First)	; ;	16	Most Significant Digit	HP 5050A/B Printout Columns (1)
8	4	22	f 1	L 8	4	2	$\int 1$	8 }	4		<u> </u>	l 8	4	22	$\int 1$	8	4	2	$\int 1$	BCD Weight
В	۶	N	1	D	C	4	ယ	Ŧ	Ħ	6	5	J	н	8	7	L	K	10	6	Pin
(Loaded Last)		_	Least Significant Digit		•	2				ယ			,	4.				ပာ		HP 5050A/B Printout Columns (1)
8	4	22	_1	8	4.	2	_1	8	.4	2	ſ 1	8	-4-	2	ſ 1	8		2	ſ 1	BCD Weight



W28 ♣ H

W25

₩29 •

W25

H WZ3

W21 ₩20 •

₩.

W32 • H

M30 H

₩36 •

CONTROL SIGNALS

Busy	21
Ground	вв
Store Data	22
Reference Voltage, Low Level	23
Reference Voltage, High Level	AA
Signal	Pin

① Order of column printout may be changed internally in the HP 5050A/B and HP 562A.

INSTRUMENT OPTIONS REQUIRED

Any one of the following is required:

d.

- Option H06: Option H66: 11 column systems version.
- Option H106: 11 column systems version with data storage.

INST

c.

- 11 column systems version with digital clock (clock uses 6 columns, but not any of the 11 data columns).
- Option H166:

d.

: 11 column systems version with digital clock and data storage (clock uses 6 columns, but not any of the 11 data columns). 12 column systems version.

е.

Option J11:

HP 5050A/B DIGITAL RECORDER

₩43, ● ₩42 • ¥41 ₩40 •

HUHU

W49

The f

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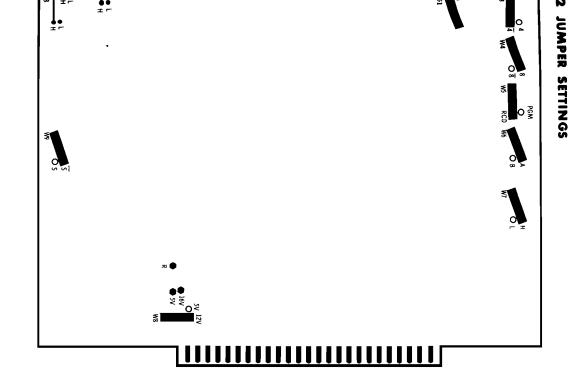
OH

EQUIPMENT REQUIRED

- HP 12798-60012 BCD Output Card (+12V-true
- HP 02547-6040 Cable to interconnect with HP 5050A/B Digital Recorder.

Note: The above items comprise HP 12798B Option 001. If either item is required separately, order by HP part number.

c. HP 5050A Digital Recorder Serial Prefix 720, 740, 744, 752, 828; or HP 5050B Digital Recorder Serial Prefix 836, 908.



INSTRUMENT OPTIONS REQUIRED

Any one of the following is required:

a. Option H06: 11 column systems version.

b. Option H66: 11 column systems version
 with data storage.

Option H106: 11 column systems version with digital clock (clock uses 6 columns, but not any of the 11 data columns).

c.

Option H166: 11 column systems version with digital clock and data storage (clock uses 6 columns, but not any of the 11 data

d.

Option J11: 12 column systems version.

Ф.

f. Option J22: 12 column systems version with data storage.

Option J33: 12 column systems version with digital clock (clock uses 6 columns, but not any of the 12 data columns).

Option J44: 12 column systems version with digital clock and data storage (clock uses 6 columns but not any of the 12 data columns).

Note: Only 10 data columns can be driven from each HP 12798A or B. The column boards must all be in the slots connected to either A1J1 (slots A10 through A14) or A1J2 (slots A5 through A9). The above options are shipped with some boards connected to connector A1J1 and some connected to A1J2. The boards should be rearranged for use with the HP 12798A or B.

Option M07: Systems version without column boards (one Option 020
column board required for
each two data columns printed.
Only 10 columns can be used to
print data from each HP 12798 A
or HP 12798B).

OTHER INSTRUMENT OPTIONS ALLOWED

The following options are for use with Option M07:

a. Option 015: Motor Control.

Option 020: Column Board (one board for each two data columns; maximum of 10 columns can be used with an HP 12798B).

c. Option 050: Data Storage for 20 columns

Option 051: Data Storage for 10 columns

d.

Option 055: Digital Clock (does not require column boards; uses up to 7 print columns).

INSTRUMENT CONTROL SETTINGS

REAR PANEL

When interfacing the HP 5050A/B to an HP 12798A/B, connect the data cable to either A1J1 or A1J2, depending on which slots the column boards are in. (Slots A10 through A14 connect to A1J1. Slots A5 through A9 connect to A1J2.) If the HP 5050A/B does not have storage, set the Print Command switch for the connector used to the upper position.

Set the other Print Command switch to the lower position.

If the HP 5050A/B has storage, set the Print Command switch (instrument will have only one Print Command switch) to the lower position (marked with a "1" on the panel).

b. FRONT PANEL

Press ON-OFF switch
Press OPERate switch.

OUTPUT FORMAT

The number of words accepted and their position in the output is determined by the input. A total of up to 10 characters may be output by the BCD output card. The printed output is left-hand justified and presents the following characters (standard character set on digital recorder):

•	
ASCII Input to Card	
Input	
ਰਿ	
Card	
ВСД	
Output	
엺	
Output on Recorder	

Digits 0 through 9

E (See Note)

, (Comma) (See Note)

Blank

Blank

Note: BCD character equivalent applies to HP 5050A/B Digital Recorder only. Character print on HP 562A depends on specific unit.

(Minus)

HP 562A DIGITAL RECORDER

EQUIPMENT REQUIRED

- a. HP 12798-60012 BCD Output Card (+12V-true output).
- b. HP 02547-6040 Cable to interconnect with HP 562A Digital Recorder.

Note: The above items comprise HP 12798B Option 001. If either item is required separately, order by HP part number.

HP 562A Digital Recorder, Serial Prefix 134, 147, 214, 246, 247, 312, 334, 812.

c.

INSTRUMENT OPTIONS REQUIRED

Option 022: +1248 Column Board. One board is required for each print column.

Option 030: BCD Connector Assembly for up to 9 columns.

Ġ,

Option 031: BCD Connector Assembly for up to 6 columns.

Θ

Option 034: BCD Connector Assembly for up to 10 columns.

ġ

c.

(1) Only one of these options is required. If Option 030 or 031 is used, consult HP 562A Operating and Service Manual Connector Assembly diagram to determine input location of data digits.

12798B

HP 12798-60010 JUMPER SETTINGS



9 8011 ¥ (4)

W13 • • • H

DATA WORD

×	×	12	11	R	Ą	14	13	T	ß	16	15	V	Ϥ	18	17	×	¥	20	19	Pin
		တ				7				æ				9		(Loaded First)	ì · ! ;	10	Most Significant Digit	HP 5055A Printout Columns
8	4	2	$\int 1$	8	4.	2	$\int 1$	l 8	4	2	$\int 1$	l 8	4	22	$\int 1$	l 8	4	2	ſ 1	BCD Weight
В	Α	2	1	D	C	4	သ	Ħ	Ħ	6	5	J	Н	8	7	L	×	10	6	Pin
(Loaded Last)	ì	-	Least Significant Digit			22				ယ				4				CJ.		HP 5055A Printout Columns
8	4	2	ſ1	8	4	2	_1	8	4.	2	_1	8	4	2	1	۱8	4	2	1	BCD Weight





W28 - H W25 - H

L MS2 L

W19 W18

CONTROL SIGNALS

					ı
21	вв	22	23	AA	Pin
Busy	Ground	Store Data	Reference Voltage, Low Level	Reference Voltage, High Level	Signal

EQUIPMENT REQUIRED

- HP 12798-60010 BCD Output Card (positivetrue output).
- HP 02547-6040 Cable to interconnect with HP 5055A Digital Recorder.

Note: The above items comprise HP 12798B Option 010. If either part is required separately, order by HP part number.

HP 5055A Digital Recorder, Serial Prefix 0960, 0972, or 1108A.

INSTRUMENT OPTION REQUIRED

Option H15: Systems version

OTHER INSTRUMENT OPTIONS ALLOWED

- Option 001: 50 Hz Power.
- Option 005: Cabinet color.

CONTROL SETTINGS

- FRONT PANEL
- Power:
- OPERATE-STANDBY: OPERATE
- REAR PANEL
- -8421/+8421: +8421
- BLANKING ON/OFF: As desired.

Figure 2-5. HP 5050A/B and HP 562A
Digital Recorder Interface

Figure 2-6. HP 5055A Digital Recorder Interface

Section II

HP 12798-60010

PROGRAM WORD FORMAT

9 8011 ¥ (4)

NZI NZI

O 4 W4

										_									_	
z	×	12	11	Ħ	ď	14	13	Ŧ	ß	16	15	۷	u	18	17	x	¥	20	19	Pin
		os.				-7				&				9		(Loaded First)		1,6	Most Significant Digit	HP 2759B Programming Digit Position
8	4	22		8	.4	22		8	.4.	22		8	-4	2	<u> </u>	8	44	123		BCD Weight
В	Α	2	1	ם	a	4	ယ	দ্য	Ħ	6	υ	J	Н	8	7	L	×	10	9	Pin
(Loaden Fast)	(I caded Last)	—	Least Significant Digit			2				ယ				4				IJ.		HP 2759B Programming Digit Position
8	- 44	. 2		8	. 4		, 1	8	4.			8	-4				4. (· N		BCD Weight



W24 H H

WZ

W21

WIS WIS

CONTROL SIGNALS

	_				
21	ВВ	22	23	AA	Pin
Busy (Flag)	Ground	Store Data (Frequency Change Command)	Not Used	Not Used	Signal

EQUIPMENT REQUIRED

¥42 ¥41

W49

- a. HP 12798-60010 BCD Output Card (positivetrue output).
- b. HP 02547-6055 Cable to interface with HP 2759B Synthesizer Programmer.

Note: Cable HP 02547-6055 is supplied with the HP 2759B.

 c. HP 2759B Synthesizer Programmer, Serial Prefix 903.

OTHER INSTRUMENT OPTIONS ALLOWED

- a. Option 100: 7 Digits, High True
- Note: Use only the 7 most significant digits of the data register on the HP 12798B BCD Output Card.

CONTROL SETTINGS

Set jumper W2 on the HP 2759B Control Card (A1) to the +6V position.

INSTRUMENT OPTIONS REQUIRED

No instrument options are required.

PROGRAM WORD FORMAT

HP 12798-60012 JUMPER

- (1) If Local Control (front panel) is desired, make HP 12798B programming digit number 9 a 5 for Local, 1/10s gate, or a 6 for Local, 1/60s gate. Also be sure to program digits 8, 7 and 6 as desired. Digit 5 must be 1 to enable Local control. After digits 9, 8, 7, 6, 5 have been programmed, the remaining 5 digits may be programmed or not. Then press LOCAL on the front panel. If digits 4 through \$\tilde{y}\$ were not programmed. Θ grammed before LOCAL was pressed, the appropriate items should be selected with the front panel switches. The decimal point position is not selectable in the LOCAL mode, but is always automatically selected by the HP 3450A. Local Control may also be obtained by disconnecting the HP 3450A from the HP 12798B and pressing LOCAL on the HP 3450A front
- **⊗** The >10G ohm Input Impedance is effective only on the 0.1V, 1V and 10V DC ranges.
- (3) Integration Delay inserts a 100 ms delay interval into the Sample Period prior to the integration of each Input Voltage (once per Sample Period on Non-Ratio and three times on Ratio). It is required that Integration Delay be programmed when 1/60s Gate is programmed on the 10M ohm range. AC function automatically adds an Integration Delay of 2.4s (3 × 2.4s = 7.2s total delay per Sample Period on AC Θ
- **(4**) (4) Internal Trigger should be inhibited if the HP 12797A BCD Input Interface Card is used to interface the HP 3450A digital output to the coupler/controller. If the HP 12797A is not used, the Internal Trigger should be enabled unless the front panel MAN/EXT switch is to be used to manually trigger the HP 3450A. The HP 12798B BCD Output card has no provision for externally triggering the HP 3450A.
- <u></u> Manual Trigger should be inhibited to prevent triggering of the HP 3450A from the front panel MAN/EXT switch. Manual Trigger Inhibit also prevents operation of all front panel controls including the LOCAL switch. See Note 1 for instructions to return the HP 3450A to LOCAL
- (E) If decimal Point Position is programmed to AUTO, the decimal point will be displayed in its correct position as determined by the range and function of the HP 3450A. If the Decimal Point Position is programmed to something other than Auto, the decimal point will be displayed in that position regardless of the range and function of the HP 3450A. The program-6

The Auto positions are:

Ratio	Ohms	AC	DC		
	10.000 Κ Ω			Е	
X 1000	1000 KΩ	1000V	1000 V	D	
X1 00	100Ω 100 K Ω	100V	100 mV 100 V	С	
X1 0	10 K Ω	10V	10V	В	
1X	1 ΚΩ	1000 mV	1000 mV	Α	

In Local Mode, the Decimal Point Position is always automatic. $% \left(1\right) =\left(1\right) \left(1\right) +\left(1\right) \left(1\right) \left(1\right) +\left(1\right) \left(

- ② All of the DC functions have two possible codes either of which may be used. All function codes are operative only if the appropriate option is present in the HP 3450A (e.g., Option 001 for ACV, 002 for Ohms). If the function selected is not present, the data display will blank and an overload indication will be given. **(**3) Note that the Limit Test Limits are not programmed with the interface used for Remote Control.
- ⊚ If Ratio function is programmed, a \emptyset or an 8 is normally used for the non-ratio range digit. The HP 3450A will automatically autorange when measuring the Y (denominator) input for a ratio measurement.
- (9) The Ratio Range should normally be programmed to Auto or to X1. In general, the first Y reading made in Ratio, after the HP 12798B program word is changed, will result in the primary range of the HP 3450A recycling to the 1000V or 10M ohm range. Since only the X1 ratio range is compatible with this primary range, the ratio range will recycle to the X1 range. Even if the Y input is small enough to allow the primary range to auto range downward, the ratio range cannot change during the current measurement period because the range circuitry is in a Hold state. The resulting reading will probably be an overload reading. When the next reading is taken, the ratio range will change to the programmed range provided the Y input primary range is compatible with the programmed ratio range. To avoid this prochlem the ratio range should be an overload reading the programmed ratio range. 9 problem, the ratio range should be programmed to Auto. If a ratio range other than Auto is programmed in spite of these problems, the second and subsequent readings following a program change or a Y input range change will usually be correct. Use zero (β) for Non-Ratio function.

		X1000
У	Choose one Normally	>
) Not	Ratio Range: X1
		100 mV/100 ohms AUTO
	(1V/1K ohms
	(Range	10V/10K ohms
	Non-Ratio	100V/100K ohms
	Choose one	1 KV/1M ohms
		101
		Non-Ratio Range: (Ratio)
		DC Ratio Limit Test
		Ohms Ratio Limit Test
		AC Ratio Limit Test
		DC Ratio Limit Test
		DC Limit Test
		Ohms Limit Test
		AC Limit Test
	9	DC Limit Test
	Function	DC-Ratio
	Choose onc	Ohms-Ratio
		AC-Ratio
		DC-Ratio
		DC
		Ohms
		AC
		Function: DC
		Auto
		A
	6	В
	Display Position	С
	Choose one	Position: D
	_	Decimal Point Display E
	Trigger Function (3)	Manual Trigger Enable
9	Choose one Manual	Manual Trigger Inhibit
1	Trigger Function (4)	Internal Trigger Enable
8	<u> </u>	Internal Trigger Inhibit
1	ј ретау	No Integration Delay
æ	Choose one Integration	100 ms Integration Delay
1	Impedance ©	10G ohm Input Impedance
100	Choose one Input	10M ohn Input Impedance
2	Length	1/60s Gate
1	Choose one Gate	1/10s Gate
9 8 7 6	Remarks	Function
111. 12:300		

PROGRAM WORD EXAMPLES

1/60s Gate, 10G ohm Input Impedance, 100 ms Integration Delay, Internal Trigger Inhibit, Manual Trigger Inhibit, Auto Decimal Point, AC, 10V, Not Ratio	1/10s Gate. 10G ohm Input Impedance, No Integration Delay, Internal Trigger Inhibit. Manual Trigger Inhibit. Auto Decimal Point. Ohms Ratio. (Ratio), Auto Ratio Range
2	-
-	
-	
2	10
8	
150	20.
2 1 6 6 8 6 1 4 6	1 1 8 8 6 9 4
\vdash	X D
44	

9 8011 ¥ (49) ● 5V ₩24 •

₩42 •

ē **UIPMENT REQUIRED**

INSTRU

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c.

- HP 12798-60012 BCD Output Card (+12V-true
- HP 12798-60002 Cable to interconnect with HP 3450A Multi-Function Meter.

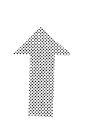
Note: The above items comprise HP 12798B Option 003. If either part is required separately, order by HP part number.

HP 3450A Multi-Function Meter, Serial Prefix 916, 931, 935, 941, 946, 950, 953, 956, or 959.

UNFOLD Figure 2-7. HP 2759B Programmer Interd

HP 12798-60012 JUMPER SETTINGS

8B Programming Digits



9 8011 ¥ (4) ৰ্ক্ত 🕈 ●12V W28 H W32 • ₩16 W24 <u>₹21</u> ž5 W27 - L W23 H W31 H W19 H # E WIN

EQUIPMENT REQUIRED

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#49 • - -

- HP 12798-60012 BCD Output Card (+12V-true output).
- þ. HP 12798-60002 Cable to interconnect with HP 3450A Multi-Function Meter.

Note: The above items comprise HP 12798B Option 003. If either part is required separately, order by HP part number.

c. HP 3450A Multi-Function Meter, Serial Prefix 916, 931, 935, 941, 946, 950, 953, 956, or 959.

g 8 g 1 4 g

INSTRUMENT OPTIONS REQUIRED

- Option 005: Remote Control.
- Option H20: Systems version of HP 3450A See Note.
- Option H25: Option H20 plus Option H50.

Note: Either Option H20 or Option H25 is required for Serial Number 953-01300 and below.

ER INSTRUMENT OPTIONS ALLOWED

OTH

Option H50: Optimum Noise Rejection for 50 Hz line.

Option 001: AC Converter.

Option 002: Ohms Converter.

Option 003: Limit Test.

Option 004: Digital Output.

f.

Option 006: Rear Input Terminals

CONTROL SETTINGS

The following controls on the HP 3450A are not programmable and should be set as detailed below:

FRONT PANEL

RATE Knob:

Not effective if HP 3450A is externally triggered. Set as desired if internally triggered. (Do not internally trigger when used with HP 12797A BCD Input Inter-HP 12797A is not used and face.) Set as desired if the

INPUT FRONT/ As desired.

Internal Trigger is selected

JUMPER S/D ON PRINTED CIRCUIT BOARD A7

Set to the S position if the HP 12797A Input Interface is used to interface the HP 3450A Digital Output to the coupler/controller.

Note: This jumper is usually in the D position when the instrument is shipped.

LOGIC LEVEL JUMPERS

ECAUTION3

Logic Level jumpers on HP 3450A printed circuit assemblies A3, A6 and A24 must be in the "Standard" position (+12V level) rather than "Alternate" position (+4.4V level).

Set Logic Level jumpers on A3 and A6 (on A24, if present) to the "Standard" position (+12V level).

LIMIT TEST OPTION

Consult factory for information regarding programming of Limit Test option.

Figure 2-8. HP 3450A Multi-Function Meter Programming Interface

2 - 18

11005-897S1 (48)

W13 3 H W15 H W15

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5v

HP 12798-60011 JUMPER SETTINGS

HP 3482A/84A Control Signals	ntrol Signals	HP 12798B Programming Digits
Function	Remarks	9 8 7 6 5 4 3 2 1
Enable Program (1)	Enter Enable Digit	1
Range: 1000V/10M ohm		8 8
1000K ohm		g 1
100V/100K ohm	Choose one	1 0
10V/10K ohm	Range	2 8
1000 mV/1000 ohm		4 0
100 mV/100 ohm		8 0
Function: VDC		B
VAC (AC)	Choose one	$\frac{1}{1}$
VAC (DC) ②	Function	2
Ohms		4
Not Ratio	Choose one	8
Ratio 3	3.000	$\frac{1}{1}$
Filter: Out	Choose one	B
Α	Filter	1
В	Position	2
Last four digits are not used (4)		x x x



W32 • H

W29 ...

W24 H

W20 H

PROGRAM WORD EXAMPLES

(1) Enable digit may be any non-zero digit if the HP 2570A system includes an HP 9100A/B	NOTES			Enable, 1000V, VDC, Not Ratio, Filter Out	Enable, 10V, VAC (AC), Not Ratio Filter Out
3 HP 3480A/B Option 002 only.					
, ,			Ì		
g g			را		2
tion			11	20	20
002				1 8 8 8 8 8 8 8	1 2 8 1 8 8 8 8 8
9				25	æ
ly.		<u></u>	 	20	20
i. •				100	25
í			[<i>1</i> 00	100
				162	20.
5			\ [.es	<i>S</i>
			-		

- Θ Calculator. This digit may be any digit including zero if the system does not include a calculator.
- ② HP 3484A only.
- (4) Last four digits may be any digit or may be omitted.

c.

HP 3480A Digital Voltmeter, Serial Prefix 928 or HP 3480B Digital Voltmeter, Serial Prefix 960.

HP 3482A DC Range Unit or HP 3484A Multi-Function Unit.

Note: The above items comprise HP 12798B Option 004. If either part is required separately, order by HP part number.

(5) Trailing zeroes may be omitted from the HP 12798B programming word.

EQUIPMENT REQUIRED

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Voltmeter.

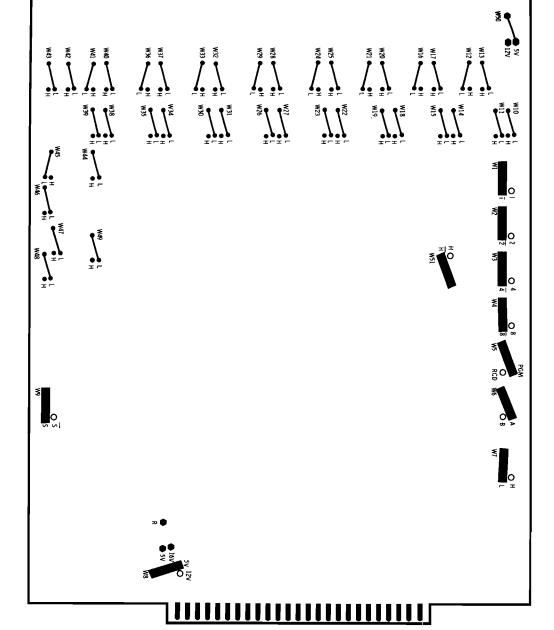
HP 12798-60003 Cable to interconnect with HP 3482A/84A Plug-In in an HP 3480A/B

 $\ensuremath{\mathsf{HP}}\xspace\,12798\text{-}60011$ BCD Output Card (ground-true output).

- For HP 3480A:
- Option 003:
- BCD Output, any Serial Suffix, or
- Option 004: Isolated BCD Output, only Serial Suffix 00325 and up.
- ь. For HP 3480B: As for HP 3480A.

INSTRUMENT OPTIONS REQUIRED

HP 12798-60011 JUMPER SETTINGS



MENT REQUIRED

HP 12798-60011 BCD Output Card (groundtrue output).

HP 12798-60003 Cable to interconnect with HP 3482A/84A Plug-In in an HP 3480A/B Voltmeter.

Note: The above items comprise HP 12798B Option 004. If either part is required separately, order by HP part number.

HP 3480A Digital Voltmeter, Serial Prefix 928 or HP 3480B Digital Voltmeter, Serial Prefix 960.

HP 3482A DC Range Unit or HP 3484A Multi-Function Unit.

INSTRUMENT OPTIONS REQUIRED

For HP 3480A:

Option 003: BCD Output, any Serial Suffix, or

Option 004: Isolated BCD Output, only Serial Suffix 00325 and up.

For HP 3480B:

o. As for HP 3480A.

OTHER INSTRUMENT OPTIONS ALLOWED

For HP 3480A/B:

Option 002: Ratio.

Option 003: BCD Output. Not recommended for systems applications.

Option 004: Isolated BCD Output (only for above). Recommended for Serial Suffix 00325 and systems applications.

Ġ. For HP 3482A:

Standard

Option 021: Isolated Remote Control (only for Serial Suffix 00351 and above). Requires HP3480A/B Option 004. Recommended for systems applications.

c. For HP 3484A:

Standard

Option 042: Option 041: Isolated Remote Control (only for Serial Suffix 00325 and above). Requires HP3480A/B Option 004. Recommended Ohms Converter. for systems applications.

CONTROL SETTINGS

Option 043:

True RMS AC Converter.

The controls listed below are not programmable and should be set as follows:

SAMPLE RATE:

Set to HOLD if the HP 12797A BCD Input Interface Card is used to interface the digital output of the HP 3480A/B mainframe to the HP 2570A Coupler/Controller. Do not set to HOLD if the BCD input card is not used.

FRONT/REAR Terminal Selector: As desired

NOT REMOTELY PROGRAMMABLE

The HP 3481A Buffer Amplifier has no selectable ranges or functions to be remotely controlled.

TRIGGER DELAY

Some delay for instrument response time must occur between a program change or a change in input signal and the beginning of the measurement. This de-

lay may be incorporated in the HP 2570A program or it may be generated internally by the HP 3480A/B plug-in when a jumper is properly set.

nally generated delay is not always equal to the response time, but ranges from 10% to 400% of the If the plug-in generated delay is desired, set the jumper on the A3 board to IN. Note that this intermeasuring rate of the meter is reduced to 200 readjumper is IN. response time. In particular, note that the maximum ngs per second when the External Trigger Delay

ger Delay jumper is set to IN and they apply only to the external triggers. (The front panel Trigger switch action is not delayed by this jumper.) delay times are generated only if the External Trigdelay times are shown in the following table. instrument response times and internally generated These

Function	Delay Time	Response Time
VDC Filter Out	$4~\mathrm{ms}$	1 ms
VDC Filter A	250 ms	$200 \mathrm{ms}$
VDC Filter B	1 s	1 s
VAC (AC)	1 s	1 s
VAC (DC)	ယ	15 s
OHMS, 100 ohm		
Range through \	4 ms	$1 \mathrm{ms}$
100K ohm Range,		
Filter Out		
1000K ohms Range Filter A	200 ms	200 ms
10M ohms Range,	200 ms	2 ms

If the HP 12797A BCD Input Interface Card is used to interface the digital output of the HP 3480A/B to the HP 2570A Coupler/Controller, the position of the jumper can be determined by operating the HP 2570A program or while running a program at high speed with no other delays (e.g., @1E@1O pinned on the control card, with the HP 12797A in I/O Slot 1). should be $\approx 1s$ if the jumper is IN. The delay can be tween the issuance of an E command to the HP 12797A and the flashing of the SAMPLE RATE indijumper can be determined by operating the HP 3480A/B from an HP 2570A. Observe the delay bechecked either while single stepping through an HP cator. On VDC, Filter B, or VAC (AC), the delay

The external Trigger Delay jumper on PC card A3 may be exposed by removing the bottom cover from the HP 3482A or HP 3484A plug-in. If the HP 3484A has the Option 043, True RMS AC Converter, the converter board must be moved to expose the jumper. may be tilted up to expose card A3 and the Delay Remove the two screws marked by arrows on the and Service Manual for further information. jumper. Refer to the appropriate plug-in Operating This card is hinge-mounted at the opposite edge and panel of the plug-in) of the AC Converter card A7. bottom, left-hand side (with respect to the front

Figure 2-9. HP 3482A/84A Programming Interface

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W17 •

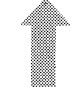
¥_==

•12V

HP 12798-60011 JUMPER

SETTINGS

X X		Last two digits are not used ③
ם מן מן מן מן מן	Enter Magnitude (2)	Voltage Magnitude
1	Folarity	Minus (-)
<u>ø</u>	Choose one	Voltage Polarity: Plus (+)
1	Kange	X1
B	Choose one	Voltage Range: X10
7		1000 mA
6	HP 6130B only	700 mA
<u></u>	,	500 mA
4	Current Laten	200 mA
3	Choose one	100 mA
2		70 mA
		50 mA
<u> </u>	_	Current Latch: 20 mA
1	Enter Enable Digit	Enable Program (1)
9 8 7 6 5 4 3 2 1 Ø	Remarks	Function
12798B Programming Dig	Control Signals	HP 6130B/6131B Control Signals



W24 *****

W25

₩21 ₩20

₩28 **7 1**

PROGRAM WORD EXAMPLES

NOTES	Enable, 1000 mA Latch, X1 Range, Plus, 5 volts	Enable, 200 mA Latch, X10 Range, Minus, 38.25 volts	
	ш	1	
	7	4	
	1	В	
	250	1	
	5	1 3	
ſ	Ø	8	
⊕ {	ъ	2	
	200	8 2 5 Ø	
آہ		762	
Θ		754	

NOTES

- Θ Enable digit may be any non-zero digit if the HP 2570A system includes an HP 9100A/B Calculator. This digit may be any digit including zero if the system does not include a calculator.
- ② Voltage magnitude is in millivolts if X1 range is selected, tens of millivolts if X10 range is selected. D³ is most significant digit.
- ၜ The ninth and tenth digits in the programmed word do not have to be given to the HP 12798B. If they are given, they may be any
- ⊕ Trailing zeros may be omitted from the magnitude, only if the jumpers W1 through W4 on the HP 12798B card are positioned for an HP 12798B output of zero (1 2 4 8) during preset state.

0 NTROL SETTINGS

EQUIPMENT REQUIRED

W43

W. C.

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W37 ♥

W32 *****

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Note:

The above items comprise HP12798B Option 005. If either part is required separately, order by HP part number.

HP 12798-60009 Cable to interconnect with HP 6130B/6131B Digital Voltage Source.

<u>ب</u>

output).

HP 12798-60011 Output Card (ground-true

The HP 6130B/6131B controls listed below are not remotely programmable and should be set as follows:

STORAGE switch: STORE (On PC Board A2, behind front panel).

As desired.

METER RANGE, VOLTAGE:

METER RANGE, CURRENT: As desired.

CURRENT LATCH DELAY: Determined by capacitor $C_{\mathbf{T}}$ attached to terminals on rear panel.

LAY:

INSTRUMENT OPTIONS REQUIRED

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HP 6130B BCD Digital Voltage Source, Serial Prefix 8J, 9E, 9G, 9L, \(\beta\)D, or HP 6131B BCD Digital Voltage Source, Serial Prefix 1018A.

- 'n For HP 6130B: J80
- For HP 6131B: J80

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Figure 2-9. HP 3482A/84A Programming Interface UNFOLD FOR

Figure 2-10.

WORD FORMAT

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HP 12798-60011 JUMPER SETTINGS

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	١	ĺ	l	ı	ı	ĺ	ı	ı	ĺ	
	HР	12	12798B		301°	Ţaı	mmi	Programming Digits	Dig	its
emarks	9 MSD	∞	7	6	ບາ	4	ယ	2	_	10 0
one	3 1									
	ļ	1	. B	B						
		2 2	a sa	2 2						
	W,X	<i>5</i> 4	1 2	2 2						
one		10	· -	18						
n One		100	2	ø						
	, 	Ø	4	B						
		Ø	æ	-						
		ø	ø	2						
		ø	ø	44	<u>,</u>					
				,	-	æ	8			
					2	ø	ø			
					4	Ø	þ			
				,	æ	1	100			
one a.se				, 888	20	2	<i>S</i>			
				,	æ	4	8			
					8	ъ	-			
					ø	Ø	N			
					, 6 2	8	4			
one								B		
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one									- <i>e</i>	
)										æ
one (1)									,	-
										20
		₩				₩	▒		▓	4

₩32 • ¥33 **₽** W25 W28 ¥24 W41 W37 W22 H

EQUIPMENT REQUIRED

W42

WORD EXAMPLES

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Ø 2

2

4 Ø

8 8

0 1 *10*

- HP 12798-60011 BCD Output Card (groundtrue output).
- HP 12798-60006 Cable to interconnect with HP 5326A/B Universal Counter.

Note: The above items comprise HP 12798B Option 008. If either part is required separately, order by respective HP part number.

c HP 5326A or HP 5326B Universal Counter, Serial Prefix 945 (both instruments).

INSTRUMENT OPTIONS REQUIRED

Option H18: Includes 002 Remote Program-ming, 003 +8421 Digital Output, Zero Suppress Defeated, External Reset at Digital Recorder Output, and BNC Input Connector for DVM (HP 5326B only).

OTHER INSTRUMENT OPTIONS ALLOWED

Option 001: 8-Digit Display.

INSTRUMENT OPTIONS NOT ALLOWED

Option 003: +8421 Output Option 002: Remote Programming apart from Option H18. Not allowed

INSTRUMENT CONTROL SETTINGS

*Not applicable. Jumpers maybe in either position.

grammable and should be set as follows: The HP 5326A/B controls listed below are not pro-

FAST/NORM/HOLD: Set to HOLD if an HP 12797A

SEP/COM: BCD Input Card is used to interface the digital output of the HP 5326 A/B to the HP 2570A Coupler/Controller. As desired.

STORAGE: OSC INT/EXT: LEVER controls: Input Attenuators: Coupling: AC/DC Input Signal As desired As desired. As desired As desired As desired.

STOP/START Not programmable.

PROGRAM RESPONSE TIME

the last program word. This delay may be incorporated in the program source (e.g., ≥ 6 ms to input the three ASCII characters of an HP 2570A I/O instruction from the HP 2748A Tape Reader or 300 ms word includes a DVM programming function, even if the range and function have not been changed since The response time of the Function, Time Base, and Slope Select lines is less than 5 μ s and no extra delay is required in the coupler/controller program jumper W9 to S to generate a nominal 300 ms delay. delay may also be incorporated by setting HP 12798B serted between the programming of the range line and the encoding of the counter. The delay must be if the DVM is programmed in the HP 5326B (DVM, READ A or READ B), a 5 ms delay must be inincluded every time an HP 12798B programming between programming these lines and encoding the HP 5326A/B using the HP 12797A BCD Input Interto input the instruction from a teleprinter). at least 5 μ s between these two operations. However, The speed of the HP 2570A forces a delay of

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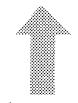
W12 H W14 H W15 H W16 H W15 H W16 H W15 H W16 H W15 H W16 H

*Not applicable. Jumpers maybe in either position.

12V

HP 12798-60011 JUMPER SETTINGS

100V
Choose one ①
Not DVM
-Slope B
+Slope B Choose one
-Slope A J Slop
+Slope A Choose one
10s/10 ⁸
1s/10 ⁷
$0.1s/10^6$ (2)
$10 \text{ ms}/10^5$
1 ms/10 ⁴ Choose one Time Base
) <u>3</u>
$10 \ \mu s/10^2$
$1 \mu s/10^1$
Base: $0.1 \mu\text{s}/1$
DVM J J
Read B (Trig. level)
Read A (Trig. level)
Time Interval Average
Time Interval A to B Function
Period Average A
Period
Frequency C
Function: Frequency A
Check
Not Check Choose one
Function
HP 5326A/B Control Signals
nals ose cttio





W28 H W25 H

PROGRAM WORD EXAMPLES

]		łΘ		J
Check, Frequency A, 1 μ s, +Slope A, +Slope B, Not DVM	3 1 8 8 2 8 8 8 8 8	Ø	82	2	В	B	20	100	В
Not Check, Read B, 10 ms, +Slope A, -Slope B, 10V	æ	B	8 2 8 4 8 8 1 1	В	4	ъ	20	1	1

NOTES

- For HP 5326B only.
- ② One of these required for DVM reading.
- (3) Trailing zeros may be omitted from the HP 12798B programming word.

EQUIPMENT REQUIRED

- HP 12798-60011 BCD Output Card (groundtrue output).
- HP 12798-60006 Cable to interconnect with HP 5326A/B Universal Counter.
- Note: The above items comprise HP 12798B Option 008. If either part is required separately, order by respective HP part number.
- c. HP 5326A or HP 5326B Universal Counter, Serial Prefix 945 (both instruments).

INSTRUMENT OPTIONS REQUIRED

- Option H18: Includes 002 Remote Programming, 003 +8421 Digital Output, Zero Suppress Defeated, External Reset at Digital Recorder Output, and BNC Input Connector for DVM (HP 5326B only).

OTHER INSTRUMENT OPTIONS ALLOWED

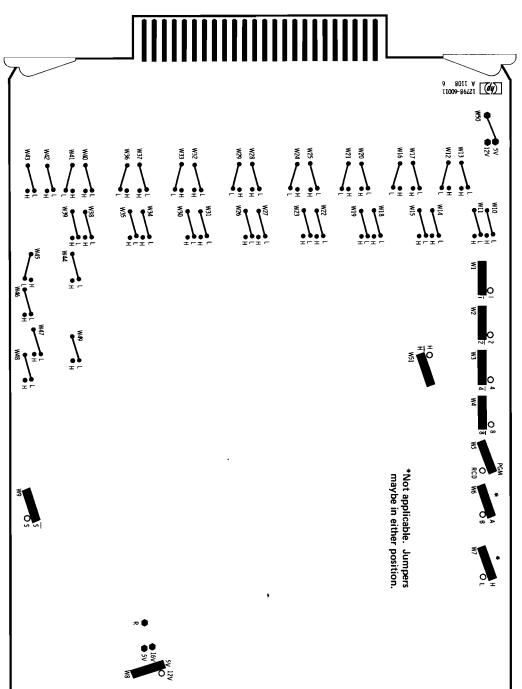
Option 001: 8-Digit Display.

ORMAT

9 HP 12798B Programming Digits N 8 7 6 5 4 ယ 2 **6**0

4 2 6 4 Ø 2 2 1 8 8 8 4 9 2 2 2 2 4 2 6 6 4 8 2 2 4 4 W C 0 2 4 4 0 0

HP 12798-60011 JUMPER SETTINGS



EQUIPMENT REQUIRED

- HP 12798-60011 BCD Output Card (groundtrue output).
- HP 12798-60005 Cable to interconnect with HP 5325B Universal Counter.

The above two items comprise HP 12798B Option 007. If either part is required separately, order by HP part number.

CAMPLES

Last digit and trailing zeros may be omitted

N

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4

8

8

2

2

5

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2

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c. HP 5325B Universal Counter, Serial Prefix 820, 844, 846, 904, 930, 932, 936, or 944.

INSTRUMENT OPTIONS REQUIRED

- For HP 5325B, Serial Prefix Option H39 820, 844, 846, 904, 930, or H62 or 932 or 936: H70.
- For HP 5325B, Serial Prefix No Options required.

Note: If the instrument is used with an HP 12797A BCD Input Card, the customer must move an internal jumper. See remarks below:

OTHER INSTRUMENT OPTIONS ALLOWED

- Option 001: Remotely programmable attenuator switch and AC/DC switch.
- Options H62 Allowable for instrument Serial Prefixes not requiring them.

CONTROL SETTINGS

The HP 5325B controls listed below are not remotely programmable, and should be set as follows:

FAST/ function: NORM/HOLD NORM if the HP 12797A BCD out such interface, FAST position may be used. Input Card is used to interface the digital output of the HP As desired. 5325B to the HP 2570A. With-

STAR' STORAGE: SEP/COM: EXT Base: INT/EXT Time LEVEL: T/STOP As desired. As desired. As desired. As desired.

function Input Attenuators: AC/DC: 8 Programmable if the HP 5325B has Option 001. Set front panel switches to AC and X1, respectively.

Not programmable.

PROGRAM RESPONSE TIME

The speed of the HP 2570A forces a delay of at least 5 µs between these two operations. However, if the Input Attenuator is programmed in the HP 5325B, a 5 ms delay must be inserted between the programming of the attenuator lines and the encoding of the counter. This delay must be included every time a programming word is given to the HP 12798B, even if the attenuator range has not been changed since the last program word (unless both attenuator channels are programmed to AC, XI each time). This delay may be incorporated in the program source (e.g., ≥ 6 ms to input the three ASCII characters of an HP 2570A I/O instruction from the HP 2748A Tape Reader or 300 ms to input the instruction from a teleprinter). The delay may also be incorporated by setting HP 12798B jumper W9 to S to generate a nominal 300 ms delay. Slope HP 5325B using the HP 12797A BCD Input Interface lay is required in the coupler/controller program between programming these lines and encoding the The response time of the Function, Time Base, and Reader or 300 ms to input the instruction from Select lines is less than 5 μ s and no extra de-

REMA RKS

For use in a coupler/controller system containing an HP Calculator and an HP 12797A Interface Card to interface the digital output of the HP 5325B to the HP 2570A, the customer must make an internal modification to the HP 5325B. (See HP 12797A application inform

2 - 22

9 8011 ¥ 11009-86421 **(49)**

\$ × ×

O2 72 W3 4

*Not applicable. Jumpers maybe in either position.

HP 12798-60011 JUMPER SETTINGS

	†	
9 9 4		$10s/10^{8}$
Ø Ø 2		$1s/10^{7}$
Ø Ø 1		$0.1s/10^{6}$
Ø 4 Ø	Dase	10 ms/10 ⁵
Ø 2 Ø	Time	$1 \text{ ms}/10^4$
ø 1 ø	Choose one	0.1 ms/10 ³
4 0 0	_	$10 \ \mu s/10^2$
2 0 0	<u> </u>	$1 \mu s/10$
1 0 0		Time Base: $0.1 \mu s/1$
0 4		Time Interval A to B
<u>Ø 2</u>	<u> </u>	Ratio A/B
<u>ø</u> 1	Function	Period Average A
4 0	Choose one	Check
2 0		Period A
1 β		Function: Frequency A
2	J Slope B	-Slope B
1	Choose one	+Slope B
2	Slope A	-Slope A
1	Choose one	+Slope A
9 8 7 6 5 4 3 2	Remarks	Function
nr 121905 rrogramming Digits	ignals	HP 5325B Control Signals

Attenuator: Channel A, AC, X1

Attenuator: Channel B, AC, X1

DC,X1

DC,X100

AC,X10 AC,X100

Choose one Attenuator A Setting ①

2 2 4 4 W W

W40 H W38

W42 • H

AC,X100 DC,X1

Choose one Attenuator B Setting ①

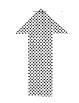
2 2 4 4 2 2

AC,X10

DC,X10



W23 W22



PROGRAM WORD EXAMPLES

Least significant digit may be any character

(0)

TROGRAM TORU EXAMPLES	Ĺ						1	$\ _{\mathbb{Q}}$	ြိ)
+Slope A, +Slope B, Check, 1 ms, (A) AC X1, (B) AC X1	H	1	4.	20	8 8 8 8 8	123	<i>, 20</i>	750	ю	200
-Slope A, +Slope B, Time Interval A to B, 1s, (A) AC X10, (B) DC X100	2	1	200	4.	<i>b</i> 4 <i>b</i> 2 2 5	, p2	22	12	51	20

Notes: (1) HP 5325B - Option 001 only.

(2) Last digit and trailing zeros may be omitted

EQUIPMENT REQUIRED

- $\ensuremath{\mathsf{HP}}\xspace\,12798\text{-}60011$ BCD Output Card (ground-true output).
- HP 12798-60005 Cable to interconnect with HP 5325B Universal Counter.

Note: The above two items comprise HP 12798B Option 007. If either part required separately, order by HP part number. S.

HP 5325B Universal Counter, Serial Prefix 820, 844, 846, 904, 930, 932, 936, or 944.

INSTRUMENT OPTIONS REQUIRED

- For HP 5325B, Serial Prefix Option H39 820, 844, 846, 904, 930, or H62 or 932 or 936: or H62 or H70.

þ.

For HP 5325B, Serial Prefix No Options 944: required.

Note: If the instrument is used with an HP 12797A BCD Input Card, the customer must move an internal jumper. See remarks below:

OTHER INSTRUMENT OPTIONS ALLOWED

- Option 001: Remotely programmable attenuator switch and AC/DC switch.
- Options H62 Allowable for instrument Serial and H70: Prefixes not requiring them.

ბ.

HP 5326A/B Programming Interface UNFOLD FOR

Figure 2-11.

2 - 21

2 - 22

Ε

Program Bits I and \$\psi\$ of Programmed Word Last two digits not used. They may be any characters or may be omitted.	Program Bits 3 and 2 of B3 B2: Ø 1 Programmed Word 1 1	Program Bits 5 and 4 of B5 B4: Ø 1 Programmed Word 1 1	Program Bits 7 and 6 of 87 B6: 0 1 Programmed Word 1 1	Program Bits 9 and 8 of Programmed Word B9 B8: 6 1 6	Program Bits 11 and 10 of Programmed Word B11 B10: 0 1 Programmed Word 1 1	Program Bits 13 and 12 of Programmed Word B13 B12: 0 1 Programmed Word 1	Program Bits B15 B14: 0 : Programmed Word B15 B14: 1 :	HP 6936A Control Signals
9 11 8 3 2 11 8 × × ×	2 1 5	3 2 1 5	3 2 1 9	3 2 1 0	3 2 1 9	3 2 1 9	1 1 g	HP 12798B Programming Digits MSD

11008-89721 8 8011 A **(2)**

W13

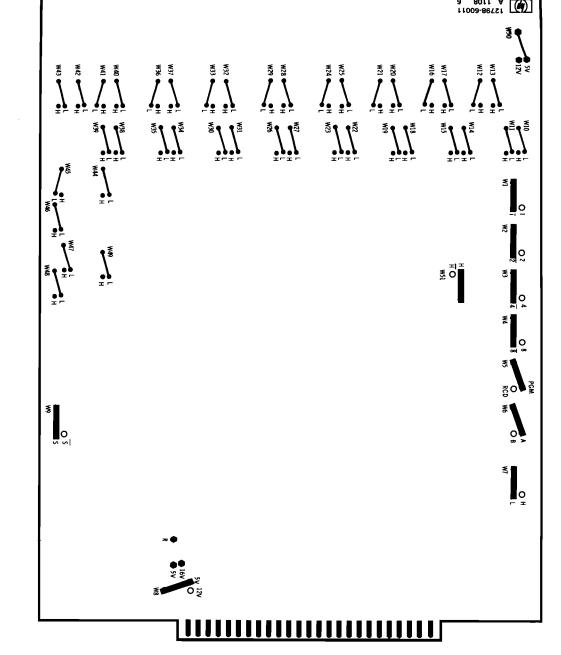
PROGRAM WORD EXAMPLES
(See Table 3-1, HP 6936A Operating & Service Manual, HP 06936-90001)

⊕ ⊕ ⊕	Control	Data	Data	Control	Data	Control	Data	Data	Data	Control		Data	Control	Data	Data	Data	Control	Word	Type
Last two digits are not used. They may be any characters or may be or Trailing zeros may be omitted from the HP 12798B programming word.				<u></u>	- G	1			-			10					1	15	\dashv
wo di 1g ze			_		9		_	~	~	_		~	_	_	70	70	_	5 14	
gits			~		~	_	_	×6	~	_		, n	_	_	760	70	_	4 13	
are		_		_	, F		_		_			_		, Q	_	~			
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nsed.	_	•	-			70	•	• • • · · ·	_	• ·		9	19 2	160	~	~	,	11 1	rogi
ed fr	160	_	- 62	-	5 2	~				۵ ح		9. 19.		160	~	~	, s	10/9	ramn
ey n		•	_	5 2	_		_		_	ω _		-,	.		- 01	<u>,</u>	, ,		ned 1
They may be any characters or may be omitted. from the HP 12798B programming word.	-			6	6	,e	•		_	ă ~		5 0.	15 0	20 e	- 2	<u>م</u>	3	8 7	Programmed Word for HP 6936A
e an P 12				- P				• <u> </u>	— -	- 	-	• 52. 	. 			- -	- -	7 6	for
y cha 7981	_	10	,e0	16 2	1	,e	_		_	62		50.	5 0.	5 0.	- 62	-	-		HP
aract	1	1	_	_	1	-	80.	160	6 0	_		20	60	150		50	62	5	6936.
ters	- -				<u> </u>			- - -	<u>-</u> -	· •			. -	 	- -		. -	4	A
or n	,e	1	_	2	1	8	-	1	_	160		62	150	160	.00	<i>,</i> 60	90	ယ	
nay k	100	10	_	Ø	1	να	10	1	0	Ø		62	5 2	80	20	, 6 2	90	2	
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nitte	16		, <u> </u>	20	1	1	-	1	1	10	_	50.		160	20	.ea	<i>'</i> Ø	ø	
, <u>.</u>	ω	ယ	_	ယ	10	ω	ω	160	Ø	ω		62	ω	ω	.60	9	ယ	9	
	ω	2	_	ယ	10	3	N	-	160	ω		150.	ω	120	_	8	ယ	·	HH.
	20	_	Ø	Ø	80	Ø	-	-	Ø	Ø		9	6 0.	150	.00	9	9	7	1279
	20	•	•	19 0	2	Ø	٥.	•	ယ	20		• 53. •	6 2	<i>'</i> 53.	•	, 6 0	Ø	6	HP 12798B Programming Digits
	-	N	100	Ø	အ	Ø	ω	1	_	10		62	150.	20	,ea	, 6 2	100	5	Prog
	2	ω	ယ	ω	2	2	-	100	_	2		150.	6 2	200	, 6 0	, 6 0	(2)	4	ram
	2	ю	ယ	9	ω	Ø	10	ω	10	Ø		5 0.	150	150.	.60	9	å	ယ	mine
	2	ω	_	10	-	1	ω	-	ω	Ø		159.	_	150.	160	160	20	N	Dig
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	×	×	×	×	×	×	×	×	×	×		×	×	×	×	×	œ	10	
	DTE on	Data to Slot 14	Data to Unit ØØ, Slot Ø5	SYE, TME, Unit 00	Data to Unit Ø1, Slot ØØ	Select Unit Ø1 Retain SYE	Data to Unit 1919, Slot 14	Data to Unit ØØ, Slot Ø1	Data to Unit 100, Slot 100	SYE = 1		Slots ØØ Through 14			Slots ØØ Through 14	Taitialian II-it did		I. differen	Finetion

DATA OUTPUT DATA INPUT HP 6937A EXTENDER

(HP 6937A EXTENDER AND HP 145

HP 12798-60011 JUMPER SETTINGS



INTERCONNECTIONS



TENDER AND HP 14541A CABLE ARE OPTIONAL)

EQUIPMENT REQUIRED

- **a** HP 12798-60011 BCD Output Card (Ground-true output)
- ٠, HP 6936A Multiprogrammer to the BCD HP 12798-60013 Cable to interconnect the Output Card.
- HP 6936A Multiprogrammer, Serial Prefix 1033A (-00101 and up).

HP 6936A INSTRUMENT OPTIONS REQUIRED

No option required.

AND ACCESSORIES ALLOWABLE HP 6936A OPTIONS

- No options available at the time of this printing.
- The following list includes the currently availthe means or method of communication between the HP 6936A Multiprogrammer and the HP 12798B BCD Output Card may be used. Any HP 6936A accessory which does not alter able programmable accessories. (See also "OPERATING NOTES, Accessories.")

ACCESSORY EXTENDERS AND PROGRAMMABLE OUTPUT CARDS

- **а** HP 6937A Extender, Serial Prefix 1033A (-00101 and up).
- Ġ. HP 69300A through HP 69304A Resistance Output Cards, Run 1.
- င္ HP 69310A through HP 69313A Resistance Output Cards, Run 1.
- ٩ HP 69320A Low-Speed D/A Converter Card, Run 1. (See HP 69351A Voltage Regulator Card.)
- Φ. HP 69321A High-Speed D/A Converter Card, Run 1. (See HP 69351A Voltage Regulator Card.)
- HP 69330A Relay Register Card, Run 1.
- å HP 69331A TTL Output Card, Run 1.

Standard - Ground True

Option 073 - Positive True

Þ. HP 69351A Voltage Regulator Card, Run 1. (One each required for one or more HP 69320A or HP 69321A D/A Converter Cards used in HP 6936A or HP 6937A mainframe.

HP 6936A CONTROL SETTINGS

Data Source: REMOTE (See OPERATING NOTES)

HP 12798B JUMPER SETTINGS

$W4 = \overline{8}$	$W3 = \overline{4}$	$W2 = \overline{2}$	$W1 = \overline{1}$
W8 = +5	W7 = L	W6 = A	W5 = PGM
R = Empty	W51 = H	W50 = +5	W9 = S

OPERATING NOTES

DATA SOURCE

is currently being issued by the HP 12798B and no BUSY (FLAG) signal is being received by the REMOTE, or vice versa, without affecting the data already given to the I/O cards in the multiprogram-HP 12798B. mer, provided that no STORE DATA (GATE) signal The DATA SOURCE may be changed from LOCAL to

This feature allows the user to issue infrequently used data or control words (e.g., initialization data) outside of the coupler/controller program.

INITIALIZATION OF HP 6936A

switch register. The HP 6936A requires initialization data at time of power turn-on. If this data is to be given outside the coupler/controller program, the DATA SOURCE programming the multiprogrammer with the coupler/controller, he need only switch the DATA SOURCE the data entered manually by means of the front panel switch on the front panel may be set to LOCAL and to REMOTE and proceed with the coupler/controller When the user is ready to begin

If means are available for entering non-repeating instructions into the coupler/controller (e.g., calculator, TTY, or photoreader), the initialization program may be entered into the HP 6936A from the coupler/controller.

ACCESSORIES

put cards for the HP 6936A Multiprogrammer. These existing accessories are listed under "Allowable Options and Accessories." existing accessory extenders and programmable outcedures. The following general rules apply to all plete listing of them and of their operating proavailable and yet to be introduced preclude a com-The number and variety of HP 6936A accessories

- words into the multiprogrammer from the HP 12798B is independent of the accessories used The method of entering control words and data with the multiprogrammer.
- 2 words does depend on the accessories used. The individual accessory manual, as well as the HP 6936A Operating and Service Manual, should be The content of the control words and the data gramming words. consulted to determine the content of these pro-

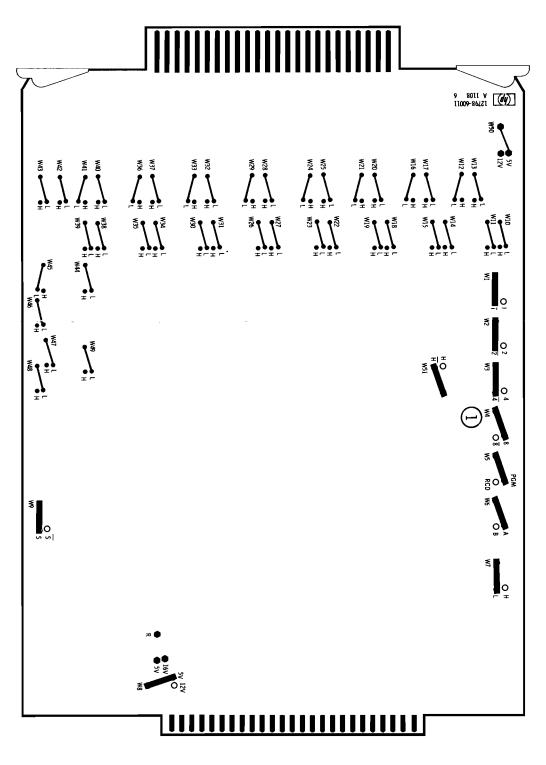
Last three digits not used (2)	Filter: Out	Range: 100 mV 1000 mV 10 V	Scan Mode: Step Scan Mode: Step Single Scan Continuous Scan Home Random/Last Channel: Ø 1 Ø 2 Ø 3 Ø 4 Ø 4 Ø 5 Ø 5	Function	HP 3485A Control Signals
	Choose one Filter Position	Choose one Range	Choose one Scan Mode Choose one Scan Mode Choose Random or Last Channel	Remarks	
x x x	1 0	2 - 2		9 8 7 6 5 4 3 2 1 Ø	HP 12798B Programming Digits

PROGRAM WORD EXAMPLES

	Enable, Home (3)	Enable, Continuous Scan, Last Channel 40, 100 mV Filter Out	Enable, Random, Channel 23, 1000 mV, Filter In	
	1	1	-	
	8	4	ø	
	B	ø	-	
		4.	2	
_		æ	ω	
ſ		æ	_	
1		æ	-	
₽		25	æ	
ł		<i>,</i>	æ	
Į		ø	ø	

- (1) Enable digit may be any non-zero digit if the coupler/controller system includes an 9100A/B Calculator. This digit may be any digit including zero if the system does include a calculator.
- (%) Last three digits may be any digit or may be omitted.
- Θ Only programming digits 9 and 8 need be given for the Home Scan Mode. If programming digit 7 is given it should be zero. Programming digits 7 through 9 will be ignored if they are given, provided digit 7 is a zero.
- (4) Trailing zeros following the Random/Last Channel digits may be omitted. A zero in the Random/Last Channel digits must not be omitted unless the Home Scan Mode is selected.

HP 12798-60011 JUMPER SETTINGS



EQUIPMENT REQUIRED

- Ġ. Interface Card: Interface Cable: HP 12798-60014 HP 12798-60011 (ground true output)
- c. HP 3480A-004 Digital Voltmeter, Serial Prefix 928 or HP 3480B-004 Digital Voltmeter, Serial Prefix 960.
- <u>d</u>. HP 3485A-057 Scanning Unit, Serial Prefix

HP 3480A/B AND HP 3485A **OPTIONS REQUIRED**

For HP 3480A/B

- HP 12798-012 ò For HP 3485A: Option 004: Isolated BCD Output, Serial Suffix 00325 and up.
- Option 057: Remote Control
- Any one of the following options: a) Option 051: 10 Channels
- b) Option 052: 20 Channels
- d) Option 054: c) Option 053: 30 Channels 40 Channels
- Option 055: 50 Channels

OPTIONS ALLOWED OTHER HP 3480A/B AN

For HP 3480A/B:

Option 002: Ratio.

with the instrum

No other options are (July, 1971). For HP 3485A:

Ġ.

HP 3480A/B CONTROL

RATIO MODE (Rear panel, 3480A/B, Option 002 only):

RATIO REFERENCE RANGE panel, HP 3480A/B, Option

HP 3485A CONTROL SE

RANDOM/ LAST CHANNEL: RANGE: FILTER: REMREM

CHANNEL DELAY: As desi REM progran

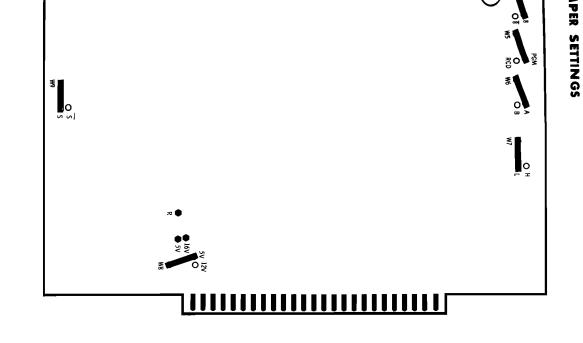
REM

MODE:

HP 12798-60011 JUMPER

 $W3 = \overline{4}$ W4 = 8 $\mathbf{W2}=\overline{\mathbf{2}}$ $W1 = \overline{1}$ W7 = LW6 = AW8 = 5VW5 = PGI

(1) The position of W4 dependence used. See OPERATING information.



3480A/B AND HP 3485A OPTIONS REQUIRED

For HP 3480A/B

Option 004: Isolated BCD Output, Serial Suffix 00325 and up.

Option 057: Remote Control

For HP 3485A:

2. Any one of the following options:

a) Option 051: 10 Channels

b) Option 052: 20 Channels

c) Option 053: 30 Channels

d) Option 054: Option 055: 40 Channels 50 Channels

OTHER HP 3480A/B AND HP 3485A OPTIONS ALLOWED

For HP 3480A/B:

Option 002: Ratio. This option may be in the instrument but may not be used with the HP 3485A.

Ģ. For HP 3485A:

No other options are available at this time (July, 1971).

HP 3480A/B CONTROL SETTINGS

RATIO MODE (Rear panel, HP 3480A/B, Option 002 only): OFF

RATIO REFERENCE RANGE (Rear panel, HP 3480A/B, Option 002 only):

Either position

HP 3485A CONTROL SETTINGS

FILTER: REM

RANGE: REM

RANDOM/ LAST CHANNEL: REM

CHANNEL DELAY: As desired (This control is not programmable.)

MODE: REM

HP 12798-60011 JUMPER SETTINGS

W4 = 8 $W1 = \overline{1}$ $W2 = \overline{2}$ $W3 = \overline{4}$ W7 = LW8 = 5VW6 = AW5 = PGM $W51 = \overline{H}$ W50 = 5VW9 = SR = Empty

(1) The position of W4 depends upon the scan mode used. See OPERATING NOTES for further information.



SUMMARY OF COMMAND SEQUENCE FOR REMOTE OPERATION OF HP 3485A SCANNING UNIT

Section II

12798B

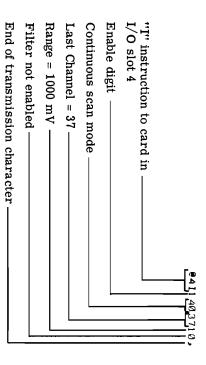
·	All "E" and all "O" commands are addressed to the HP 12797A BCD Input Card.	Inds are addressed to the little of the litt	2. All "E" and al
	TIP 19700D DOD Octoor Cond		Ŋ
	Coupler/controller halts because O scanner was not reprogrammed after Last Channel		
	E		
	$\begin{bmatrix} \mathbf{E} \\ \mathbf{O} \end{bmatrix}$ Last Channel		
•••	•••		•••
$\left. \begin{array}{c} E \\ O \end{array} \right\} Channel\ 2$	E Channel 2		E Channel 2
E O Channel 1	E Channel 1		E Channel 1
	I Reprogram scanner to resume scan		
	3		0 Last Chames
E Last Channel	E Last Channel		
•••	•••	• • •	•••
E O Channel 3	E Channel 3	E I > Selected Channel O	E Channel 3
E Channel 2	E Channel 2	E I Selected Channel O	E Channel 2
I E Channel 1 O	I E Channel 1	I Selected Channel O	E I Channel 1
(W4 = 8)	(W4 = 8)	$(W4 = 8 \text{ or } \overline{8})$	$(W4 = \overline{8})$
Continuous Scan Mode	Single Scan Mode	Random Mode	Step Mode

- 1. 2. 3. ' and all "O" commands are addressed to the HP 12797A BCD Input Card.
- Home Mode: If W4 = 8, @aI sends the scanner Home. If W4 = $\overline{8}$, @aI180, sends the scanner Home.

OPERATING NOTES

In the example I/O instructions in the following notes, the HP 12798B is assumed to be in I/O slot 4. The programming digits are assumed to come from a device which generates a Done signal (pulls NDNE low) when it transmits a comma (,) to indicate the end of a transmission. The control card and some I/O cards must transmit some other character (e.g., reverse slash (\) or space () in order to activate the Done signal. Consult the appropriate coupler/controller or I/O card manual to determine when the Done signal is activated (NDNE pulled low). In the Scan Mode example below, the reference to the comma following the "I" command actually refers to the appropriate end of the transmission character of the I/O card transmitting the programming digits to the HP 12798B

Typical instruction used in OPERATING NOTES



STEP MODE

An "I" command to the HP 12798B is given between an "E" and an "O" command to the HP 12797A for each scan advance step. LAST CHANNEL is followed by Channel 1. Jumper W4 on the HP 12798B must be in position 8. (Continuous Scan Mode is similar in operation except that the "I" command need be given only once--when the mode is begun or when some parameter such as range or last channel is changed.)

- a. If the scanner is not Home, issue an "@4I18," command to the HP 12798B card in I/O slot 4 to send the scanner Home, or press the HOME button on the front panel of the scanner. If the scanner is Home (no CID illuminated) and is in Ready condition (R is illuminated), proceed to Step "c".
- If the scanner is Home and is not in Ready condition, proceed to Step "b" and then to Step "c". (Press RESET on the coupler/controller if it is desired to remove the scanner from the Ready condition so that Step "b" may be included in the first scan advance

- b. Issue an "E" command to the HP 12797A BCD Input Card. The Ready light (R) will come on immediately.
- c. Issue the appropriate "I" command to the HP 12798B BCD Output Card (e.g., @4I11/2721/20, for Step mode, Last Channel = 27, Range = 10V, Filter not enabled). The Ready light will go off and the next Channel ID light will come on. After the expiration of the CHANNEL DELAY (set on the front panel of the scanner), the HP 3480A/B will measure the voltage on the HP 3485A channel shown by the illuminated Channel ID. [In this case, the CHANNEL DELAY begins when the comma (or other end of transmission character) following the "I" command causes the HP 12798B to issue a Store Data signal to the HP 3485A.]

ċ

- d. Issue an "O" command to the HP 12797A BCD Input Card. The data stored on the card by the HP 3480A/B will be transferred to the ASCII bus in the coupler/controller. If the HP 3480A/B has not yet transferred the data to the HP 12797A, the HP 12797A will halt the coupler/controller until the data has been transferred and output to the ASCII bus.
- e. Repeat Steps "b" through "d" for each reading desired. When the voltage on the selected LAST CHANNEL has been measured, the next scan advance operation will advance the scanner to Channel 1.

SINGLE SCAN MODE

One "I" command allows one complete scan from Channel 1 to the selected LAST CHANNEL, with only an "E" and an "O" command required for each step in the scan cycle. LAST CHANNEL is not followed by Channel 1 unless another "I" command is given. Jumper W4 on the HP 12798B should be in position 8.

- a. If the scanner is in Ready condition (R is illuminated), proceed to Step "b" and then directly to Step "d", skipping Step "c". If the scanner is not in Ready condition, proceed to Step "b" and then to Step "c". (Press RESET on the coupler/controller if it is desired to remove the scanner from the Ready condition so that Step "c" may be included in the first Continuous Scan operation.)
- b. Issue the appropriate "I" command to the HP 12798B BCD Output Card (e.g., @411202320, for the Single Scan mode, with Last Channel = 23, Range = 10V and Filter not enabled). If the scanner was in Ready condition (R illuminated) during Step "a", R will go off and Channel ID = 1 will come on. After the expiration of the Channel Delay (set on the front

- panel of the scanner), the HP 3480A/B will measure the voltage on the HP 3485A channel shown by the illuminated Channel ID. [In this case, the CHANNEL DELAY begins when the comma (or other end of transmission character) following the "I" command causes the HP 12798B to issue a Store Data signal to the HP 3485A.] Proceed to Step "d". If the scanner was not in Ready condition during Step "a", no lights come on at this time; proceed to Step "c"
- Issue an "E" command to HP 12797A. The previous Channel ID light will go off (if any was lit) and the next Channel ID light will come on. After the expiration of the Channel Delay, begun by the "E" command, the HP 3480A/B will measure the voltage on the scanner channel shown by the illuminated Channel ID.
- Issue an "O" command to the HP 12797A BCD Input Card. The data stored on the card by the HP 3480A/B will be transferred to the ASCII bus in the coupler/controller. If the HP 3480A/B has not yet transferred the data to the HP 12797A, the HP 12797A will halt the coupler/controller until the data has been transferred and output to the ASCII bus.

d.

e. Repeat Steps "c" and "d" for each reading desired. Do not perform Steps "c" and "d" after the LAST CHANNEL measurement is complete without first performing Step "b" to begin a new scan cycle. Then proceed to Steps "c" and "d" as usual. If this restriction is violated, the coupler/controller system will halt until either the RESET or the START button on the coupler/controller is pressed.

NOTE

Once the regular cycle is started in Single Scan mode ("I" command, "E", "O", "E, "O"...) another "I" command at any point before an "E" command will send scanner to Home and the following "E" command will advance the scanner to Channel 1.

CONTINUOUS SCAN MODE

One "I" command allows continuous cycling of the scanner with only an "E" and an "O" command to the HP 12797A required for each step in the scan cycle. LAST CHANNEL is followed by Channel 1. Jumper W4 on the HP 12798B should be in position 8.

- If the scanner is in Ready condition (R is illuminated), proceed to Step "b" and then directly to Step "d", skipping Step "c". If the scanner is not in Ready condition, proceed to Step "b" and then to "c". (Press RESET on the coupler/controller if it is desired to remove the scanner from the Ready condition so that Step "c" may be included in the first Continuous Scan operation.)
- Ď. Issue the appropriate "I" command to the HP to Step "d" command causes the HP 12798B to issue a of transmission character) following the "I" DELAY begins when the comma (or other end the HP 3485A channel shown by the illuminated Delay (set on the front panel of the scanner) during Step "a", R goes off and Channel ID = 1 scanner was in Ready condition (R illuminated) Store Data signal to the HP $3485A.\]$ Proceed Channel the HP 3480A/B will measure the voltage on comes on. 37, Range = 10V and Filter not enabled). for the Continuous mode, with Last Channel = 12798B BCD Output Card (e.g., @4I1403720, Ħ After the expiration of the Channel [In this case, the CHANNEL

If the scanner was not in Ready condition during Step "a", no lights come on at this time; proceed to Step "c".

- c. Issue an "E" command to HP 12797A. The previous Channel ID light will go off (if anywas lit) and the next Channel ID light will come on. After the expiration of the Channel Delay, begun by the "E" command, the HP 3480A/B will measure the voltage on the scanner channel shown by the illuminated Channel ID.
- d. Issue "O" command to the HP 12797A BCD Input Card. The data stored on the card by the HP 3480A/B will be transferred to the ASCII bus in the coupler/controller. If the HP 3480A/B has not yet transferred the data to the HP 12797A, the HP 12797A will halt the coupler/controller until the data has been transferred and output to the ASCII bus.
- e. Repeat Steps "c" and "d" for each reading desired. When the voltage on the selected LAST CHANNEL has been measured and transferred to the ASCII bus, the next "E" command will advance the scanner to Channel 1.

canne1 , no ner HP HP Pche his nel <u>ب</u> Ġ, and then to "c" (Press RESET on the coupler/controller if it is desired to remove the scanner from the Ready condition so that Step "c" may be included in the first Continuous Scan is not in Ready condition, proceed to Step "b" to Step "d", skipping Step "c". If the scanner If the scanner is in Ready condition (R is illuminated), proceed to Step"b" and then directly 2798B

Store Data signal to the HP 3485A.] Proceed DELAY begins when the comma (or other end of transmission character) following the "I" scanner was in Ready condition (R illuminated) during Step "a", R goes off and Channel ID=1 for the Continuous mode, with Last Channel = 37, Range = 10V and Filter not analysis. to Step ' the HP 3485A channel shown by the illuminated Channel ID. [In this case, the CHANNEL command causes the HP 12798B to issue a the HP 3480A/B will measure the voltage on Delay (set on the front panel of the scanner), comes on. Issue the appropriate "I" command to the HP BCD Output Card (e.g., @4I14\0372\00eta, After the expiration of the Channel

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If the scanner was not in Ready condition during Step "a", no lights come on at this time; proceed to Step "c".

c. After the expiration of the Channel Delay, begun by the "E" command, the HP 3480A/B will Issue an "E" command to HP 12797A. The previous Channel ID light will go off (if any was shown by the illuminated Channel ID. measure the voltage on the scanner channel lit) and the next Channel ID light will come on.

will tion 8

- ċ coupler/controller until the data has been transferred and output to the ASCII bus. 3480A/B has not yet transferred the data to the HP 12797A, the HP 12797A will halt the Issue "O" command to the HP 12797A BCD ASCII bus in the coupler/controller. the HP 3480A/B will be transferred to the input Card. The data stored on the card by If the HP
- CHANNEL has been measured and transferred to the ASCII bus, the next "E" command will advance the scanner to Channel 1. Repeat Steps "c" and "d" for each reading de-When the voltage on the selected LAST

Jumpern cycle. nd to the of the

advance the scanner to Channel 1. and the following "E" command will Continuous mode ("I" command, "E", mand will send the scanner to Home mand at any point before an "E" com-Once the regular cycle is started in "E", "O" . . .) another "I' com-

RANDOM MODE

An "I" command to the HP 12798B is given between an "E" and an "O" command to the HP 12797A for each reading on the selected Random Channel. The on the HP 12798B should be in position 8. desired channel is set by the program. Jumper W4

- ы . If the scanner is in Ready condition (R is illuminated), proceed to Step "c". If scanner is cluded in the first Random Scan operation.) not in Ready condition, proceed to Step "b" (Press RESET on the coupler/controller if it is desired to remove the scanner from the condition so that Step "b" may be in-
- Ċ. immediately. Input Card. Issue an "E" command to the HP 12797A BCD The Ready light (R) will come on
- c. Channel ID light will come on. After the expiration of the Channel Delay (set on the front panel of the scanner), the HP 3480A/B will measure the voltage on the HP 3485A channel indicated by the illuminated Channel ID. [In this case, the CHANNEL DELAY begins when the HP 3485A. the HP 12798B to issue a Store Data signal to the comma (or other end of transmisssion character) following the "I" command causes for the Random mode with the Random Channel = 15, Range = 10V and Filter not enabled). Issue the appropriate "T" command to the HP The Ready light will go off and the selected 12798B BCD Output Card (e.g., @4I1011520
- <u>d</u> 3480A/B has not yet transferred the data to the HP 12797A, the HP 12797A will halt the coupler/controller until the data has been Issue an "O" command to the HP 12797A BCD transferred and output to the ASCII bus bus in the HP coupler/controller. HP 3480A/B will be transferred to the ASCII Input Card. The data stored on the card by the If the HP
- Ф. Repeat Steps "b" through "d" for each desired channel or for each reading on the same seected channel

initiates scan

NOTE

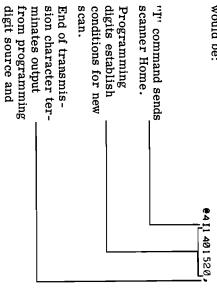
priate programming digits for Home mode (e.g., @4II8,). the "I" command followed by the approner should be sent Home between scan modes. If $W4 = \overline{8}$, the scanner will go scanner automatically goes Home as soon as the HP 12798B receives an "I" changed to a different mode the scannot automatically go Home upon receipt by the HP12798B of an "I" command. ming digits are transmitted to the both cases except that with W4 = 8, the stead of 8. Operation is identical in However, whenever the scan mode is command (even before any programjumper W4 should be in position 8 in conjunction with the Step Mode, If it is desired to use the Random Mode Home only when the HP12798B is given When W4 = $\overline{8}$, the scanner does

HOME MODE

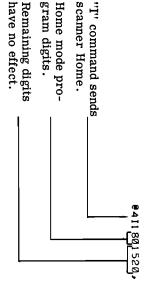
all channels open. An "I" command to the HP 12798B is given to send the scanner Home, i.e., scan counter set to zero,

$\mathbf{W4} = \mathbf{8}.$

ample, suppose the scanner is at channel 23 and it is desired to initiate a new scan of 15 scanner to Home. If another set of program digits is to be given, they may be issued immediately after the "I" command. For exfore, it is not necessary to issue the Enable Program (1) and the Home mode (8 \emptyset) programming digits to the HP 12798B to send the When W4 is set to position 8 the issuance of card instruction and associated program word channels in continuous scan mode. The I/Oany programming digits are issued. Therescanner to go Home immediately, even before an "I" command to the HP 12798B causes the would be:



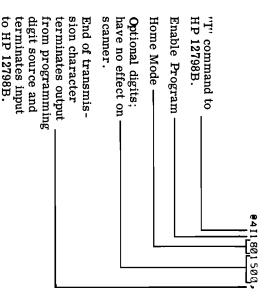
command. The programming digits following the Enable Program (1) and Home mode digits programming digits may be issued with an "I" tiating a new scan operation, the Home mode sired to send the scanner Home without inidigits with every "I" command, and it is de-If program considerations make it desirable any desired digits. to issue the same number of programming (80) will not affect the Home mode and may be



ن $W4 = \overline{8}.$

nate the input to the HP 12798B. When W4 is set to $\overline{8}$ the issuance of an "I" command to the HP 12798B does not automatically send the scanner Home. Therefore, it is necessary to issue the Enable Program (1) the programming digit source and to termiscanner will go Home as soon as the 8 is with an "I" command to the HP 12798B. and the Home mode (80) programming digits should be issued to terminate the output from (or other end of transmission character) omitted or issued if desired, but they will or may be omitted. Remaining digits may be issued. have no effect on the scanner. The digit following the 8 may be zero The comma

A typical Home command would be:

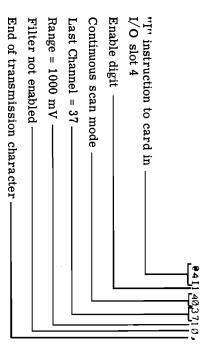


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OPERATING NOTES

In the example I/O instructions in the following notes, the HP 12798B is assumed to be in I/O slot 4. The programming digits are assumed to come from a device which generates a Done signal (pulls NDNE low) when it transmits a comma (,) to indicate the end of a transmission. The control card and some I/O cards must transmit some other character (e.g., reverse slash (\)) or space () in order to activate the Done signal. Consult the appropriate coupler/controller or I/O card manual to determine when the Done signal is activated (NDNE pulled low). In the Scan Mode example below, the reference to the comma following the "I" command actually refers to the appropriate end of the transmission character of the I/O card transmitting the programming digits to the HP 12798B.

Typical instruction used in OPERATING NOTES



STEP MODE

An "I" command to the HP 12798B is given between an "E" and an "O" command to the HP 12797A for each scan advance step. LAST CHANNEL is followed by Channel 1. Jumper W4 on the HP 12798B must be in position 8. (Continuous Scan Mode is similar in operation except that the "I" command need be given only once-when the mode is begun or when some parameter such as range or last channel is changed.)

- command to the HP 12798B card in I/O slot 4 to send the scanner Home, or press the HOME button on the front panel of the scanner. If the scanner is Home (no CID illuminated) and is in Ready condition (R is illuminated), proceed to Step "c".
- If the scanner is Home and is not in Ready condition, proceed to Step "b" and then to Step "c". (Press RESET on the coupler/controller if it is desired to remove the scanner from the Ready condition so that Step "b" may be included in the first scan advance

- b. Issue an "E" command to the HP 12797A BCD Input Card. The Ready light (R) will come on immediately.
- c. Issue the appropriate "I" command to the HP 12798B BCD Output Card (e.g., @4I1102720, for Step mode, Last Channel = 27, Range = 10V, Filter not enabled). The Ready light will go off and the next Channel ID light will come on. After the expiration of the CHANNEL DELAY (set on the front panel of the scanner), the HP 3480A/B will measure the voltage on the HP 3485A channel shown by the illuminated Channel ID. [In this case, the CHANNEL DELAY begins when the comma (or other end of transmission character) following the "I" command causes the HP 12798B to issue a Store Data signal to the HP 3485A.]

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- d. Issue an "O" command to the HP 12797A BCD Input Card. The data stored on the card by the HP 3480A/B will be transferred to the ASCII bus in the coupler/controller. If the HP 3480A/B has not yet transferred the data to the HP 12797A, the HP 12797A will halt the coupler/controller until the data has been transferred and output to the ASCII bus.
- e. Repeat Steps "b" through "d" for each reading desired. When the voltage on the selected LAST CHANNEL has been measured, the next scan advance operation will advance the scanner to Channel 1.

SINGLE SCAN MODE

One "I" command allows one complete scan from Channel 1 to the selected LAST CHANNEL, with only an "E" and an "O" command required for each step in the scan cycle. LAST CHANNEL is not followed by Channel 1 unless another "I" command is given. Jumper W4 on the HP 12798B should be in position 8.

- a. If the scanner is in Ready condition (R is illuminated), proceed to Step "b" and then directly to Step "d", skipping Step "c". If the scanner is not in Ready condition, proceed to Step "b" and then to Step "c". (Press RESET on the coupler/controller if it is desired to remove the scanner from the Ready condition so that Step "c" may be included in the first Continuous Scan operation.)
- b. Issue the appropriate "I" command to the HP 12798B BCD Output Card (e.g., @4112\(\textit{g}\)232\(\textit{g}\), for the Single Scan mode, with Last Channel = 23, Range = 10V and Filter not enabled). If the scanner was in Ready condition (R illuminated) during Step "a", R will go off and Channel ID = 1 will come on. After the expiration of the Channel Delay (set on the front

panel of the scanner), the HP 3480A/B will measure the voltage on the HP 3485A channel shown by the illuminated Channel ID. [In this case, the CHANNEL DELAY begins when the comma (or other end of transmission character) following the "I" command causes the HP 12798B to issue a Store Data signal to the HP 3485A.] Proceed to Step "d". If the scanner was not in Ready condition during Step "a", no lights come on at this time; proceed to Step"c'

- Issue an "E" command to HP 12797A. The previous Channel ID light will go off (if any was lit) and the next Channel ID light will come on. After the expiration of the Channel Delay, begun by the "E" command, the HP 3480A/B will measure the voltage on the scanner channel shown by the illuminated Channel ID.
- d. Issue an "O" command to the HP 12797A BCD Input Card. The data stored on the card by the HP 3480A/B will be transferred to the ASCII bus in the coupler/controller. If the HP 3480A/B has not yet transferred the data to the HP 12797A, the HP 12797A will halt the coupler/controller until the data has been transferred and output to the ASCII bus.
- e. Repeat Steps "c" and "d" for each reading desired. Do not perform Steps "c" and "d" after the LAST CHANNEL measurement is complete without first performing Step "b" to begin a new scan cycle. Then proceed to Steps "c" and "d" as usual. If this restriction is violated, the coupler/controller system will halt until either the RESET or the START button on the coupler/controller is pressed.

NOTE

Once the regular cycle is started in Single Scan mode ("I" command, "E", "O", "E, "O"...) another "I" command at any point before an "E" command will send scanner to Home and the following "E" command will advance the scanner to Channel 1.

CONTINUOUS SCAN MODE

One "I" command allows continuous cycling of the scanner with only an "E" and an "O" command to the HP 12797A required for each step in the scan cycle. LAST CHANNEL is followed by Channel 1. Jumper W4 on the HP 12798B should be in position 8.

If the scanner is in Ready condition (R is illuminated), proceed to Step "b" and then directly to Step "d", skipping Step "c". If the scanner is not in Ready condition, proceed to Step "b" and then to "c". (Press RESET on the coupler/controller if it is desired to remove the scanner from the Ready condition so that Step "c" may be included in the first Continuous Scan operation.)

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b. Issue the appropriate "I" command to the HP 12798B BCD Output Card (e.g., @4114\(\text{\text{\text{9}}\)372\(\text{\text{\text{\text{\text{0}}}}\), for the Continuous mode, with Last Channel = 37, Range = 10V and Filter not enabled). If scanner was in Ready condition (R illuminated) during Step "a", R goes off and Channel ID = 1 comes on. After the expiration of the Channel Delay (set on the front panel of the scanner), the HP 3480A/B will measure the voltage on the HP 3485A channel shown by the illuminated Channel ID. [In this case, the CHANNEL DELAY begins when the comma (or other end of transmission character) following the "I" command causes the HP 12798B to issue a Store Data signal to the HP 3485A.] Proceed to Step "d".

If the scanner was not in Ready condition during Step "a", no lights come on at this time; proceed to Step "c".

- c. Issue an "E" command to HP 12797A. The previous Channel ID light will go off (if anywas lit) and the next Channel ID light will come on. After the expiration of the Channel Delay, begun by the "E" command, the HP 3480A/B will measure the voltage on the scanner channel shown by the illuminated Channel ID.
- d. Issue "O" command to the HP 12797A BCD Input Card. The data stored on the card by the HP 3480A/B will be transferred to the ASCII bus in the coupler/controller. If the HP 3480A/B has not yet transferred the data to the HP 12797A, the HP 12797A will halt the coupler/controller until the data has been transferred and output to the ASCII bus.
- e. Repeat Steps "c" and "d" for each reading desired. When the voltage on the selected LAST CHANNEL has been measured and transferred to the ASCII bus, the next "E" command will advance the scanner to Channel 1.

a. If the scanner is in Ready condition (R is illuminated), proceed to Step "b" and then directly to Step "d", skipping Step "c". If the scanner is not in Ready condition, proceed to Step "b" and then to "c". (Press RESET on the coupler/controller if it is desired to remove the scanner from the Ready condition so that Step "c" may be included in the first Continuous Scan operation.)

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b. Issue the appropriate "I" command to the HP 12798B BCD Output Card (e.g., @4114\()372\()\), for the Continuous mode, with Last Channel = 37, Range = 10V and Filter not enabled). If scanner was in Ready condition (R illuminated) during Step "a", R goes off and Channel ID = 1 comes on. After the expiration of the Channel Delay (set on the front panel of the scanner), the HP 3480A/B will measure the voltage on the HP 3485A channel shown by the illuminated Channel ID. [In this case, the CHANNEL DELAY begins when the comma (or other end of transmission character) following the "I" command causes the HP 12798B to issue a Store Data signal to the HP 3485A.] Proceed to Step "d".

If the scanner was not in Ready condition during Step "a", no lights come on at this time; proceed to Step "c".

- c. Issue an "E" command to HP 12797A. The previous Channel ID light will go off (if anywas lit) and the next Channel ID light will come on. After the expiration of the Channel Delay, begun by the "E" command, the HP 3480A/B will measure the voltage on the scanner channel shown by the illuminated Channel ID.
- d. Issue "O" command to the HP 12797A BCD Input Card. The data stored on the card by the HP 3480A/B will be transferred to the ASCII bus in the coupler/controller. If the HP 3480A/B has not yet transferred the data to the HP 12797A, the HP 12797A will halt the coupler/controller until the data has been transferred and output to the ASCII bus.
- e. Repeat Steps "c" and "d" for each reading desired. When the voltage on the selected LAST CHANNEL has been measured and transferred to the ASCII bus, the next "E" command will advance the scanner to Channel 1.

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NOTE

Once the regular cycle is started in Continuous mode ("I" command, "E", "O", "E", "O' . . .) another "I" command at any point before an "E" command will send the scanner to Home and the following "E" command will advance the scanner to Channel 1.

RANDOM MODE

An "I" command to the HP 12798B is given between an "E" and an "O" command to the HP 12797A for each reading on the selected Random Channel. The desired channel is set by the program. Jumper W4 on the HP 12798B should be in position 8.

- a. If the scanner is in Ready condition (R is illuminated), proceed to Step "c". If scanner is not in Ready condition, proceed to Step "b". (Press RESET on the coupler/controller if it is desired to remove the scanner from the Ready condition so that Step "b" may be included in the first Random Scan operation.)
- b. Issue an "E" command to the HP 12797A BCD Input Card. The Ready light (R) will come on immediately.
- c. Issue the appropriate "I" command to the HP 12798B BCD Output Card (e.g., @4IIØ1152Ø, for the Random mode with the Random Channel = 15, Range = 10V and Filter not enabled). The Ready light will go off and the selected Channel ID light will come on. After the expiration of the Channel Delay (set on the front panel of the scanner), the HP 3480A/B will measure the voltage on the HP 3485A channel indicated by the illuminated Channel ID. [In this case, the CHANNEL DELAY begins when the comma (or other end of transmission character) following the "I" command causes the HP 12798B to issue a Store Data signal to the HP 3485A.]
- d. Issue an "O" command to the HP 12797A BCD Input Card. The data stored on the card by the HP 3480A/B will be transferred to the ASCII bus in the HP coupler/controller. If the HP 3480A/B has not yet transferred the data to the HP 12797A, the HP 12797A will halt the coupler/controller until the data has been transferred and output to the ASCII bus.
- Repeat Steps "b" through "d" for each desired channel or for each reading on the same selected channel.

operation.

initiates scan

NOTE

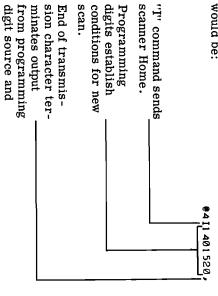
mode (e.g., @4I18,). priate programming digits for Home ner should be sent Home between scan modes. If $W4 = \overline{8}$, the scanner will go by the HP12798B of an "I" command. card). soon as the HP 12798B receives an "I" command (even before any programscanner automatically goes Home as both cases except that with W4 = 8, the stead of 8. Operation is identical in in conjunction with the Step Mode, the "I" command followed by the approchanged to a different mode the scan-However, whenever the scan mode is not automatically go Home upon receipt ming digits are transmitted to the jumper W4 should be in position 8 in-If it is desired to use the Random Mode Home only when the HP12798B is given When W4 = $\overline{8}$, the scanner does

HOME MODE

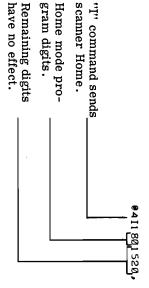
An "I" command to the HP 12798B is given to send the scanner Home, i.e., scan counter set to zero, all channels open.

a. W4 = 8.

When W4 is set to position 8 the issuance of an "I" command to the HP 12798B causes the scanner to go Home immediately, even before any programming digits are issued. Therefore, it is not necessary to issue the Enable Program (1) and the Home mode (8\$\textit{g}\$) programming digits to the HP 12798B to send the scanner to Home. If another set of program digits is to be given, they may be issued immediately after the "I" command. For example, suppose the scanner is at channel 23 and it is desired to initiate a new scan of 15 channels in continuous scan mode. The I/O card instruction and associated program word would be:



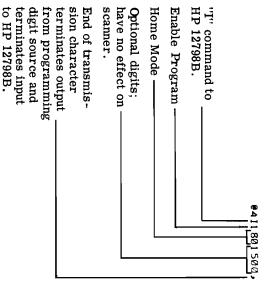
If program considerations make it desirable to issue the same number of programming digits with every "I" command, and it is desired to send the scanner Home without initiating a new scan operation, the Home mode programming digits may be issued with an "I" command. The programming digits following the Enable Program (1) and Home mode digits (80) will not affect the Home mode and may be any desired digits.



b. $W4 = \overline{8}$.

When W4 is set to $\overline{8}$ the issuance of an "I" command to the HP 12798B does not automatically send the scanner Home. Therefore, it is necessary to issue the Enable Program (1) and the Home mode (80) programming digits with an "I" command to the HP 12798B. The scanner will go Home as soon as the 8 is issued. The digit following the 8 may be zero or may be omitted. Remaining digits may be omitted or issued if desired, but they will have no effect on the scanner. The comma (or other end of transmission character) should be issued to terminate the output from the programming digit source and to terminate the input to the HP 12798B.

A typical Home command would be:



- 2-36. HP 2575A COUPLER/CONTROLLER
- 2-37. To connect a peripheral device to a BCD output card installed in the HP 2575A, proceed as follows:
- a. Set the HP 2575A LINE ON/OFF switch to OFF.
- b. Locate the rear cover on the back of the instrument. See Figure 2-3 in HP 2575A Operating and Service Manual. Release the rear cover latches and remove the cover.
- c. Hold the connector which mates with the BCD output card with the cable pointing towards the right (away from the fan and power module).
- d. Carefully slide the cable connector onto the pin connections of the BCD output card.
 - e. Check that the card is firmly seated in place.
- f. Engage the two tabs on the rear cover into the slots on the rear panel and lock the rear cover in position.
- g. Connect the other end of the cable to the peripheral device.

2-38. OPERATIONAL CHECK

2-39. GENERAL

- 2-40. Since the BCD output card will not perform any useful function by itself, an operational check of the card requires the use of the HP 2570A (or HP 2575A) Coupler/Controller and certain external equipment. Also, since the user's system components are not known, separate performance checks, specifying a variety of input/output equipment, are provided for the record and program functions of the card. The user should carry out the check applicable to his equipment.
- 2-41. In the event that the card does not operate as described, consult the troubleshooting information contained in Section IV of the manual or consult the nearest Hewlett-Packard Sales and Service Office.

2-42. RECORD MODE CHECK

- 2-43. Equipment Required. The following equipment is required to check operation of the BCD output card when used to interface a digital recorder with the HP 2570A or HP 2575A:
- a. HP 5050A/B, HP 5055A or HP 562A Digital Recorder.
- b. HP 12798B BCD Output Card, HP Part No. 12798-60010 (for HP 5055A); or HP 12798-60012 (for HP 5050A/B or HP 562A).
 - c. HP 02547-6040 cable (for digital recorder).
 - d. HP 12797A BCD Input Card (for data source).

- e. A digital data source such as a DVM, counter, etc.
 - f. Cable for digital data source interface.
 - g. HP 2570A (or HP 2575A) Coupler/Controller.
- 2-44. System Set-Up. The test set-up is illustrated in Figure 2-13. To assemble, proceed as follows:

NOTE

The test system described is for use with an HP 2570A. If an HP 2575A is employed, the same I/O slot numbers, coupler/controller test program, etc., may be used.

- a. Set HP 2570A LINE ON/OFF switch to OFF.
- b. Check that the BCD input card is correctly programmed for the data source for not more than 10 characters.
- c. Insert the BCD input card into I/O slot 2 in the HP 2570A Coupler/Controller.
- d. Connect the appropriate interface cable between the data source and the BCD input card.
- e. Program the BCD output card to operate in the record mode with the HP digital recorder in use. See Figure 2-5 or 2-6.
- f. Place the BCD output card in I/O slot Ø. Connect the recorder to the card with cable HP 02547-6040.
- g. Remove the control panel and control card from the HP 2570A.
- h. Program instructions @ØK @ 2E @ 2O @ 2E @ 2O @ 2N @ 2E @ 2O @ ØI @ 2E @ 2O @ 2E @ 2O on the control card as shown in Figure 2-13. Set SINGLE/CONT jumper W1 to SINGLE and START jumper W2 to MAN.
- i. Replace the control card in the HP 2570A and fit the control panel on the front of the control card.
- 2-45. <u>Checkout Procedure.</u> After the system has been assembled, check the record mode of operation as follows:
- a. Switch the digital data source to ON and connect a suitable signal source to its input. Adjust the instrument for a readout.
- b. Connect the HP 2570A to an ac power source and set LINE ON/OFF switch to ON.
 - c. Energize the digital recorder.
- d. Set the RUN/STOP switch on the HP 2570A control panel to RUN, and press the START button. Check that the following action now occurs: Digital source takes a reading and the readout is printed in the format programmed on the BCD input card. This

procedure should be repeated 3 times with the recorder presenting 3 lines of readout data. If the digital recorder presents 1, 2, 4, or 5 lines of data, a possible malfunction exists in the card as detailed in the following table. However, before assuming that a malfunction exists, carefully check the test program on the diode pinboard for correct programming and the other parts of the test set-up for correct operation. If the card still fails to operate correctly, consult the troubleshooting information contained in Section IV of this manual or contact your nearest Hewlett-Packard Sales and Service Office.

Readout Lines	Possible Malfunction
1	K command not set
2	I command not set
4	I command not reset
5	K command not reset by N command

2-46. PROGRAM MODE CHECK

- 2-47. Equipment Required. The following equipment is required to check operation of the BCD output card when used to program an instrument:
 - a. HP 2570A (or HP 2575A) Coupler/Controller.
- b. HP 12798-60010 (positive-true output) or HP 12798-60011 (ground-true output) or HP 12798-60012 (+12V-true output) BCD Output Card.
 - c. HP 412A VTVM or equivalent.
- 2-48. System Set-Up. To prepare the system for test, proceed as follows:
 - a. Set the HP 2570A LINE ON/OFF switch to OFF.

NOTE

The test system described is for use with an HP 2570A. If an HP 2575A is employed, the same I/O slot number, test program, etc., may be used.

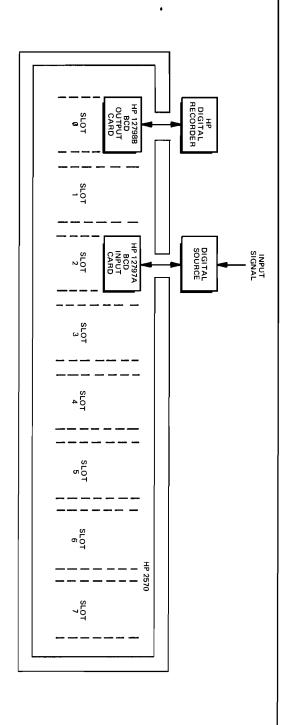
b. Set the BCD output card jumpers as detailed below:

Jumper	Position	Jumper	Position
W1	Ī	W6	В
W2	$\overline{2}$	W7	L
w3	4	W8	5 V
W4	8	W9	$\overline{\mathtt{S}}$
W5	PGM	W51	Ħ

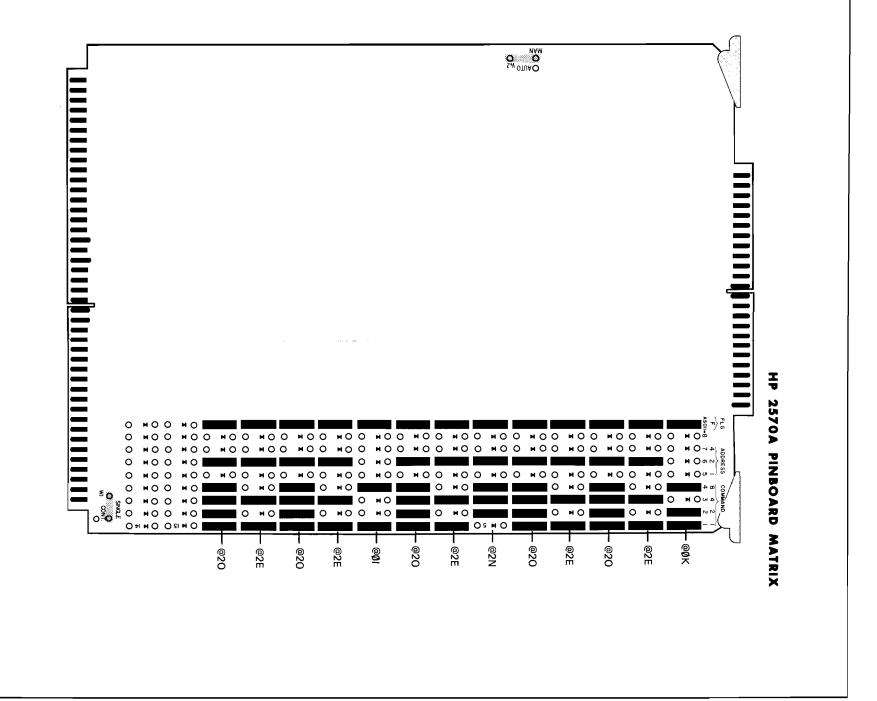
- c. Insert the BCD output card into I/O slot \emptyset in the HP 2570A.
- d. Remove the control panel and control card from the HP 2570A.
- e. Program @ØI, ASCII Sp (space), ASCII-(minus), ASCII 4, ASCII G, and ASCII \(\chi\) (reverse slash) on the control card as shown in Figure 2-14.
- f. Replace the control card in the HP 2570A and fit the control panel on the front of the control card.
- 2-49. <u>Check-out Procedure</u>. After the system has been assembled, check the program mode of operation as follows:
- a. Connect the HP 2570A to an ac power source and set LINE ON/OFF switch to ON.
- b. Set the RUN/STOP switch on the HP 2570A to RUN.
- c. Operate the HP 2570A START switch and check the output lines on the BCD output card as are detailed in Table 2-11.
- d. If the card fails to operate as described above, above, refer to Section IV of this manual for trouble-shooting information or contact your nearest Hewlett-Packard Sales and Service Representative.

Table 2-11. Program Mode Check Readout

Output Pin	BCD Weight	Output(1)	
19	1	0	
20	2	0	
W	4	1	
X	8	0	
17	1	1	
18	2	1	
U	4	1	
V	8	1	
15	1	0	
16	2	0	
S	4	0	
T	8	0	
13	1	0	
14	2	0	
P	4	0	
R	8	0	
11	1	0	
12	2	0	
M	4	0	
N	8	0	
① Output Levels: HP 12798-60010 HP 12798-60011			
"1" = + 5V "0" = + 0.4 HP 12798-6 "1" = + 12V "0" = + 0.4	'1'' = + 0.4V±0.4V '0'' = + 5V ±0.5V		



COUPLER/CONTROLLER PROGRAM



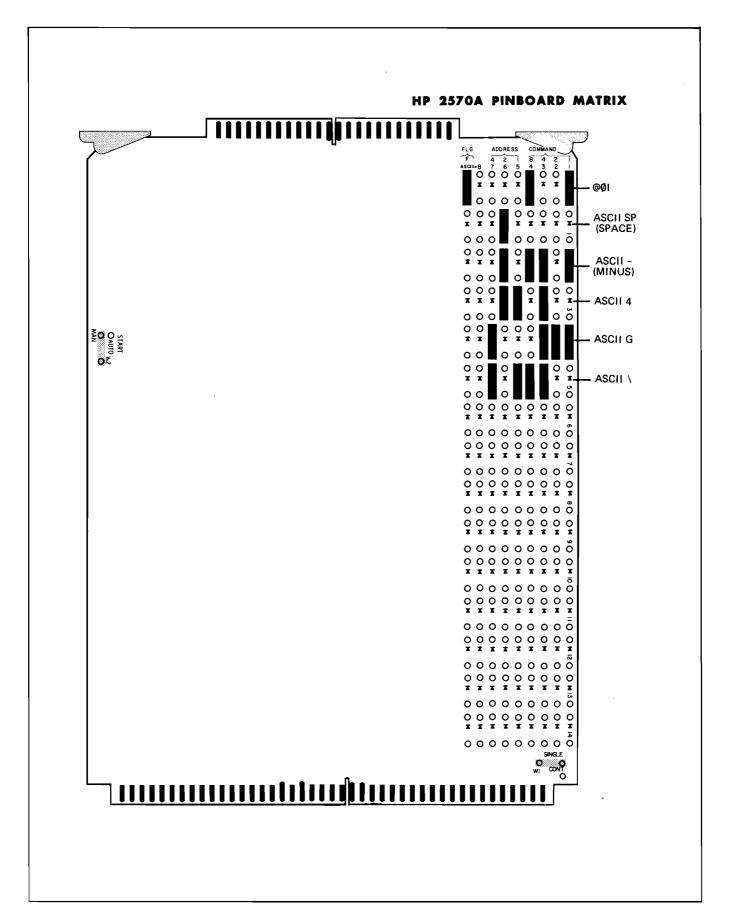


Figure 2-14. Program Mode Check

12798B Section III

SECTION III PRINCIPLES OF OPERATION

3-1. GENERAL INFORMATION

3-2. Figure 4-3 is a schematic of the BCD output card and Figure 3-1 shows circuit timing. Table 2-1 lists the pin connections between the card and the I/O equipment. Refer also to Figure 4-4 for a brief description, including block diagrams, of the integrated circuit elements used in the BCD output card. Table 3-1 defines the signal mnemonics internal to the BCD output card and Table 4-1 in the HP 2570A and HP 2575A Manuals defines the coupler/controller mnemonics.

3-3. BASIC CIRCUIT DESCRIPTION

3-4. The card accepts HP 2570A backplane bit-parallel, character-serial ASCII data which has been output from the coupler/controller pinboard matrix or from a source such as the HP 12797A BCD Input Card. This character information is translated into 8421 BCD by the data input logic on the BCD output card and clocked sequentially by a decade counter into a storage register on the card. Ten 4-bit data latches in the register permit up to 10 characters of information to be stored.

The card is jumper programmable by W5 to operate in either a Record (RCD) or Program (PGM) mode. When the Record (RCD) position is selected, the card converts, on receiving an I or K command from the HP 2570A, the ASCII signals on the backplane to 8421 BCD. This information is clocked digitby-digit into the storage/output register. The last character is accompanied with or followed by a Done (NDNE) signal that initiates a Store Data pulse. This signal causes the recorder to print the bit-parallel, character-parallel data appearing on the output lines of the register. The decade counter is returned to zero when the Store Data command is issued. Upon receipt of the Store Data command, the recorder returns a Busy signal to the card and if an attempt is made to enter data before the Busy signal ends, the Wait-Busy (NWIB) line is activated. This suspends the HP 2570A instruction program until the recorder has completed the printout operation. When the recorder has no further need for the data on the output lines, the Busy signal is removed, and the storage register is cleared to a BCD preset pattern programmed with jumpers W1, W2, W3, and W4 if the data was input to the card with the K command. If the data was input with an I command, the storage register will not be preset until another I command is received. Therefore, if a K command follows an I command, those latches which are specifically changed will still contain the digit from the previous data word. Wait-Busy is also deactivated when Busy is removed and the HP 2570A instruction program is allowed to continue to the next instruction. The I command is reset after one word is output; the K command allows the card to continue to output words until the card is addressed with an N command.

3-6. With jumper W5 set to the PGM (program) position, operation of the card is basically similar to the record mode previously discussed. The card converts, upon receipt of an I command from the HP 2570A, the ASCII signals on the backplane to 8421 BCD and these are sequentially clocked into the register. The last digit in the word is either accompanied by or followed with a Done (NDNE) signal that initiates a Store Data pulse. The Store Data signal informs the programmed device that it may execute the programming information contained on the BCD output lines. When the device receives the Store Data signal, it returns a Busy pulse to the card and depending on the positions of jumpers W9 and W51, the card may be programmed to halt the main instruction program of the HP 2570A until the instrument is programmed. If the HP 2570A is allowed to operate and an attempt is made to alter the output register code before the Busy pulse is released, the HP 2570A is halted. If the programmed device is incapable of generating a Busy signal, the BCD output card will allow programming to continue approximately 300 ms after the Store Data pulse. The K command is not programmable in the PGM mode of operation.

3-7. DETAILED CIRCUIT DESCRIPTION

3-8. CONTROL CIRCUITRY

3-9. In order to describe operation of the control logic on the card, assume that Jumper W5 is set to the RCD (Record) position and the circuitry on the card is in a system normalize (Reset) state. This occurs when ac power is first applied to the HP 2570A, or when the RESET momentary switch on the HP 2570A is pressed. Either of these actions causes System Normalize signal NSYN applied at pin AA of the card to go low. This establishes initial conditions for operation which are summarized as follows:

Input Command FF U46D, U46C Reset

Continuous Command FF U56D, U56C Reset

End of Data FF U36A, U36B Reset

End of Word FF U37C, U37D Reset

Decade Counter U86 Reset to 0000

Data Storage Limit FF U26A Set

Data Storage elements U84, U83, U82, U72, U62, U52, U42, U32, U22, U12

Preset to bit pattern programmed by jumpers W1, W2, W3, and W4 Section III 12798B

- 3-10. The \overline{I} (input one word) and \overline{K} (input words until N command) signals from the Command Decode logic on the BCD output card alert it to input data from the HP 2570A. Assume that the I command is addressed to the I/O slot occupied by the card being described. This causes the Toutput (pin 6) of Command Decode NAND gate U43A to go low. This signal, inverted by U57C, is applied to input pin 13 of NAND gate U14A, and at Strobe (STB) the gate is enabled causing its output (pin 12) to go low and set Input Command flip-flop U46D, U46C. The resetside output (U46C-11) of the flip-flop holds pin 5 of NOR gate U46B low, and the resulting high output at pin 6 of U46B, designated DTA, sets the logic on the card to accept data. (Input pin 4 of gate U46B remains high since the Continuous Command flip-flop stays in the reset state.)
- 3-11. The next command given by the HP 2570A that instructs a data source to output data causes the source interface card to activate the Interrupt Program Control (NIPC) line when data is available. When this occurs, information appears in ASCII on the HP 2570A backplane.
- 3-12. Operation of the BCD output card is now controlled by the HP 2570A character cycle Strobe (NSTB) and End of Character (NEOC) signals. At STB, NAND gate U25B and driver/inverter U37A generate the Data Strobe (DSTB) signal which strobes (clocks) the first 8421 BCD character from the Data Input Logic into storage. Gate U25B has a number of other inputs which also control production of the Data Strobe signal. The function of these signals are as follows:

U25B Input	Signal	Function
12	DTA	Enable signal from Input Command FF.
10	ADINH	Signal derived from Data Storage Limit FF U26A. The flip-flop is reset when the 10th character is entered into the data register and prevents additional characters from being stored.
13	FPST	Reset-side output of End of Word FF stops Data Strobe after last character.
CR24		Prevents non-valid characters from being entered into the data register.

3-13. At End of Character (EOC) gate U76A generates Advance signal FADV, a signal that clocks (advances) decade counter U86. Gate U76A also has a number of additional inputs which perform the following functions:

U76A Input	Signal	<u>Function</u>
1	NCOM	Prevents advancing of counter when system is in compacting mode.
4	DTA	Enable signal from Data Input FF.
2	NTNS	Prevents advancing of counter when system is in transfer mode.
5	ADINH	Output of Data Storage Limit FF. Prevents advancing the counter beyond the 10th digit.
CR47	IPC	Prevents advancing counter when Interrupt Program control is not activated.
CR49		Prevents advancing counter when non-valid character is input to card.

- 3-14. Successive character cycles clock information into the register in a similar manner until the last character in the word is placed on the backplane. When the data source activates the Done (NDNE) line this signal is employed by the BCD output card to initiate the Store Data command. First NDNE is inverted and NOR "tied" at pin 1 of U46A with STB to produce DNE which is "ANDed" with DTA at gate U46A. The resulting low output at pin 3 of U46A sets End of Data flip-flop U36A, U36B. The high set-side output at pin 3 is connected to input pin 10 of NAND gate U36D and at EOC, the gate is enabled and sets End of Word flip-flop U37C, U37D. The set-side output (pin 11) of the flip-flop, designated PST, is coupled to inverter U15B in the Store Data Interface and causes a dc level change at Store Data output pin 22 on the card. The direction of the level change and the amplitude are programmed with jumpers W7 and W8, respectively.
- 3-15. End of Word flip-flop output PST also fires 2μ sec Counter Reset one-shot U36C, U16C whose output (U16C-11) generates signal FPRT. This pulse:
 - a. Resets decade counter U86 to zero and
- b. Produces signal FRST via NOR U66A and inverter U67B which resets the Input Command and End of Data flip-flops.
- 3-16. The End of Word flip-flop controls the production of the Wait-Busy signal via Wait-Busy generator U76B and associated circuitry.
- 3-17. The Wait-Busy signal is generated by the combined action of NAND gate U76B and NOR gate U14C. All inputs to U76B are high when the card is addressed to input data and U14C is activated if an attempt is

Table 3-1. HP 12798B Logic Signals

Mnemonic	Signal	Schematic Grid Reference	Test Point	Description
ADINH	Advance Inhibit	E-8	TP7	Signal inhibits advance of decimal decoder U86 when too much data is input.
AS1 to AS4		A-6		8421 BCD data output by Data Input Logic for entry into Data Storage register.
DSTB	Data Strobe	E-9	TP5	Strobes data from Data Input Logic into Data Storage register.
DTA	Data	A-3	TP6	Enables data to be input to card from coupler/controller backplane.
FADV	False Advance	D-6	TP2	Advance (clock) signal for data storage Decade Counter U86.
FAS1 to FAS7	ASCII	A-1, B-1		ASCII bits 1 through 7, internal to BCD output card.
FCLR	Clear	E-4	TP3	Resets selected flip-flops on card as well as causing a PCDE pulse. Occurs on leading or trailing edge of Busy signal as desired via setting of jumper W6.
FE	Letter E	A-4		Decode for ASCII E. Part of Data Input Logic.
FPRT	False Print	C-5		Designates when Store Data command has been issued. Also initiates reset pulse (PRT) for counter U86.
FPST	Preset	C-4		Reset-side output of End of Word FF.
FN	Minus	A-4		Decode for ASCII - (minus) character. Part of Input Data Logic.
FRST	False Reset	C-2		Resets selected logic on card at end of character entry. Signal originates in card.
FSP	Space	B-4		Decode for ASCII space. Part of Data Input Logic.
FSYN	System Normalize	C-1		Initializes card logic at power turn-on or at a later time.
IDSTB	Inhibit Data Strobe	B-5	TP8	Inhibits storage of data when non-valid data is input to card.
LTR	Letter	B-4		Part of Data Input Logic circuitry.
PCDE	Precode	D-5		Enables preset bit pattern programmed by jumpers W1, W2, W3, and W4. Precode occurs at EOC of I command or with FCLR when K command is activated.
PGM	Program	В-3		Enables card program mode of operation.
PRT	Print	C-5	TP4	Resets data storage Decade Counter U86.

Table 3-1. HP 12798B Logic Signals (Continued)

Mnemonic	Signal	Schematic Grid Reference	Test Point	Description
PST	Preset	C-4		Set-side output of End of Word FF. Preset generates Store Data signal.
RCD	Record	C-4		Enables card record mode of operation.
$ \begin{array}{c c} C1, C2, C4, C8 \\ \hline \overline{C1}, \overline{C2}, \overline{C4}, \overline{C8} \end{array} $		C-6	1	8421 BCD output of Data Storage Decade Counter U86.
Ø to 9		D-12, C-12, B-12, A-12, D-8, C-8, B-8, A-8		10 mutually exclusive outputs of decimal decoder that clock data digit by digit into Data Storage register.
	Logic Ground	E-14	TP1	Logic ground common to system.

made to input data before the peripheral equipment is ready to accept it. The output at pin 8 of U14C is connected via inverter/driver U16D to pin U of the card.

3-18. DATA STORAGE

3-19. The data storage section of the card includes compiling logic and character encoding circuitry that translates ASCII data from the coupler/controller to 8421 BCD; a decade counter and decoder that clocks the BCD data, digit-by-digit into 10 storage/output latches; and line drivers that interface the output of the storage latches to the BCD output terminals on the card.

3-20. The compiling and character encoding logic is designated Data Input Logic in the card schematic. The circuitry consists of a number of NAND gates which translate the coupler/controller ASCII backplane data to 8421 BCD. The circuit has two modes of operation, programmed by the PGM/RCD jumper W5. When the PGM (program) position is selected, the card accepts a) ASCII characters 0 through 9 and converts them to equivalent 8421 BCD and b) characters B through G for conversion to equivalent 10 through 15 BCD code respectively; when the RCD position is selected, the card, in addition to accepting characters 0 through 9, also accepts the following ASCII characters and converts them to the 8421 BCD listed below:

ASCII Character	BCD 8421
Space	1010
E	1111
- (minus)	1011

3-21. The decade counter consists of U86, an HP 1820-0055 integrated circuit element that is externally

connected at pins 1 and 12 to form a standard decade counter. The 8421 BCD output from U86 is coupled to a decimal decoder comprised of 10 NAND gates (U75B, U75A, U74B, U74A, U65B, U65A, U64B, U64A, U55B and U55A), strobed by Data Strobe signal DSTB. The 10 mutually exclusive outputs of the decoder, designated of through 9, are coupled via NOR gates (U73C, U73D, U73A, U73B, U63D, U63C, U63B, U63A, U53D, and U53B) to the clock inputs of storage elements U84, U83, U82, U72, U62, U52, U42, U32, U22, and U12. The decoder outputs sequentially clock the decoded ASCII signals $\overline{AS1}$, $\overline{AS2}$, $\overline{AS3}$, $\overline{AS4}$ from the Data Input Logic into the latches contained in the storage elements. An alternative input to the NOR gates, Precode signal PCDE clocks the preset pattern, programmed by jumpers W1 through W4 in the Data Input Logic, into the storage element latches.

3-22. Decade counter U86 is reset to zero by signal PRT applied to pins 2 and 3 and clocked (advanced) by signal FADV connected to pin 14.

3-23. In the reset state, counter U86 output pins 12, 9, 8, and 11, corresponding to BCD bits 1, 2, 4, and 8 are low; bits $\overline{1}$, $\overline{2}$, $\overline{4}$, and $\overline{8}$ output by inverters U85C, U85A, U85D, and U85B are high. The signals are identified as U86 outputs by the prefix C. The appropriate barred and unbarred signals applied to the decimal decoder NAND gates provide a Ø to 9 count. For example, at reset, bits C1, C2, C4 and C8 applied to pins 9, 13, 12 and 10 respectively of NAND gate U75B are all high and produce a zero decode (v low) when Data Strobe pulse DSTB goes high. The resulting low output pulse at pin 8 of U75B, coupled to pin 12 of NOR gate U73C, produces a high clock input to data storage element U84. A low Preset (PCDE) pulse at pin 13 of NOR gate U73C will produce a similar clock pulse.

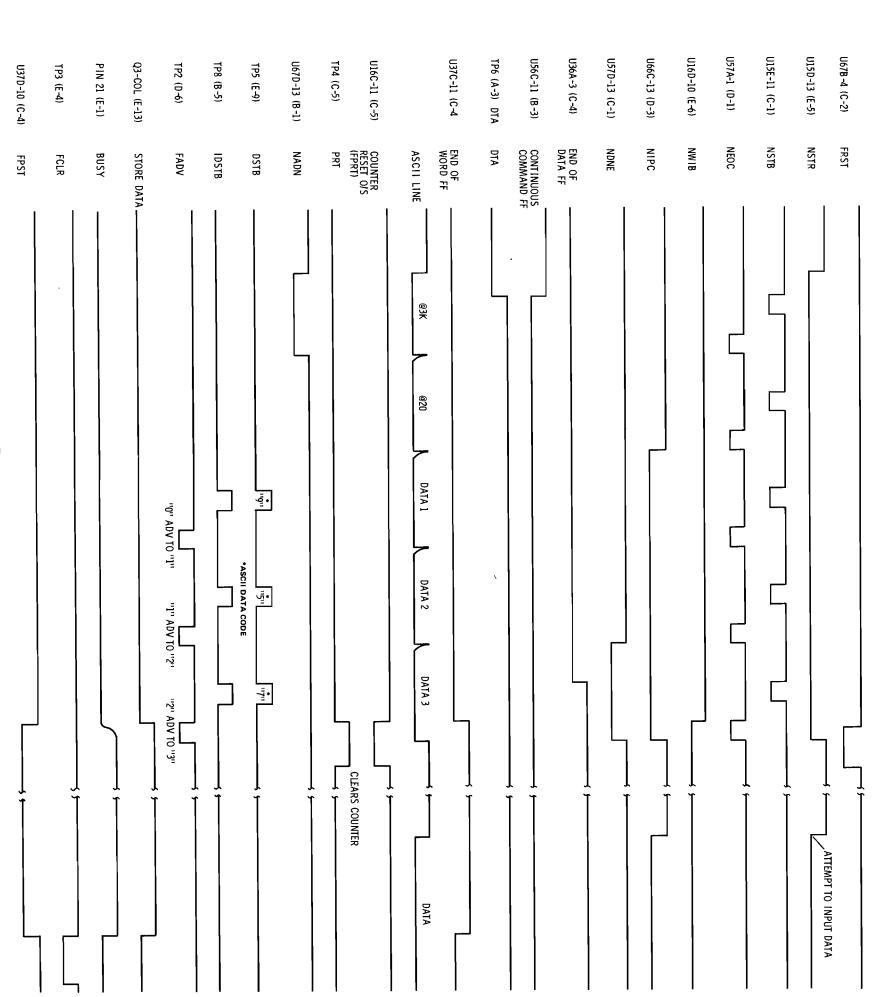
3-24. The four bistable latches that comprise the storage element have complementary \mathbf{Q} and $\overline{\mathbf{Q}}$ outputs. Information present at the data (D) input is transferred

to the Q output when the clock is high and the Q output follows the data as long as the clock remains high. When the clock goes low, the information present at the data input at the time that the transition occurs is retained at the output until the clock goes high again.

3-25. The Q and \overline{Q} outputs of the data storage latches are connected to terminals 'H' and 'L' respectively of jumpers W49 through W10 on the card. These jumpers permit the card to be hardwired for a positive-true or ground-true BCD output. The output lines selected by W49 through W10 are connected to inverting interface gates contained in integrated circuit elements U71, U61, U51, U41, U21, and U11.

Resistors R40 through R1 comprise pullup resistors for the interface gates. Jumper W50 allows the true state of the data output bit voltage to be programmed for + $5V \pm 0.5V$ or + $12V. \pm 1.0V$.

3-26. Since the operation of all 10 storage latches is similar, only the latch for the most significant digit (MSD) will be described in detail. The data storage latches for the MSD BCD bits 8, 4, 2, and 1 are U84D, U84C, U84B, and U84A respectively. The output jumpers consist of W49 through W46. The interface gates are U71F, U71C, U71B, and U71A; pull-up resistors for the gates are R40 through R37. The BCD 8421 data is output at pins X, W, 20 and 19 on the card's 48-pin PC connector.



Note: Jumper settings for waveforms illustrated: W5 "RCD", W6 "A", W7 "H", W9 "S", W51 " \overline{H} ".

Figure 3-1. Timing Diagram

SECTION IV SERVICING INFORMATION

4-1. INTRODUCTION

4-2. This section contains maintenance and servicing information for the HP 12798B BCD Output Card. This includes a checkout procedure for the card, troubleshooting information, and a schematic diagram.

4-3. PLUG-IN CARD

4-4. The HP 12798-60010 (positive-true output), HP 12798-60012 (+12V-true output), and HP 12798-60011 (ground-true output) BCD Output Cards are identified by the HP part number etched or stamped on the face of the card. See Figure 4-2.

4-5. LOCATION

4-6. The BCD output card can plug into any of the eight I/O slots in the HP 2570A Coupler/Controller or four I/O slots in the HP 2575A Coupler/Controller. The pin assignment for the I/O slot connections is given in Table 4-2 in the HP 2570A and HP 2575A Operating and Service Manuals.

4-7. PARTS LIST

4-8. A parts list for the BCD output card is included in Section V, and a components location drawing is included in this section. See Figure 4-2.

4-9. SCHEMATIC DIAGRAM

4-10. Figure 4-3 is a schematic of the BCD output card and Figure 4-4 contains a brief description of the integrated circuit elements used on the card.

4-11. TEST EQUIPMENT

4-12. The test equipment required for performance checking is listed in Table 5-2 in the HP 2570A and HP 2575A Operating and Service Manuals.

4-13. REPAIRS AND REPLACEMENT

4-14. REPAIRING CARD

4-15. Card should be taken when repairing or replacing printed circuit components. Refer to Section V in the HP 2570A and HP 2575A Operating and Service Manuals for general information regarding repair and replacement of PC components.

4-16. ADJUSTMENTS

4-17. There are no adjustments on the BCD output card.

4-18. CARD CHECKOUT PROCEDURE

- 4-19. Since the BCD output card will not perform any useful function by itself, an operational check of the card requires the use of a coupler/controller and a data source for input data to the card. As the user's system components are not known, the following equipment is suggested as a basis for a checkout of the BCD output card:
 - a. HP 2570A (or HP 2575A) Coupler/Controller.
 - b. BCD Input card, HP 12797A.
- c. A data source such as a DVM, counter, etc., with associated data cable.
- d. HP digital recorder, and HP 02547-6040 cable. Note: Use of the recorder is optional.
 - e. HP 412A Voltmeter, or equivalent instrument.
- 4-21. The procedure employs the HP 12804A Diagnostic Control Panel described in Section IV of the HP 2570A and HP 2575A Coupler/Controller Operating and Service Manuals. In the event that the diagnostic control panel is not available, the coupler/controller backplane signals may be monitored at the HP 2570A/2575A control card signal points shown in Figure 4-1.

4-22. CHECKOUT PROCEDURE

- 4-23. To verify operation of the BCD output card, carry out the following steps:
- a. Set the HP 2570A LINE ON/OFF switch to OFF.
- b. Set the jumpers on the BCD output card as follows:

Jumper	Position	Jumper	Position
W1	ī	W6	Α
W2	2	W7	Н
W3	4	W8	12V
W4	8	W9	s
W5	RCD if card is to be used as data logger; PGM if it is to be used as device control	W51	Ħ

c. Insert the BCD output card into I/O slot Ø and the BCD input card (suitably programmed for the data source) into I/O slot 3 in the HP 2570A.

- d. Set the HP 2570A LINE ON/OFF switch to ON.
- e. Measure the voltage between ground (pin BB) and pins 1 through 20, A through X, and 22 on the front of the BCD output card. (Table 2-1 lists the pin assignment and pin numbering system on the card.) Check that:
 - All 8 and 4 BCD weights are +5V ±0.5V on card 12798-60010, +12V ±1V on card 12798-60012, and 0.4 ±0.4V on card 12798-60011.
 - 2) All 2 and 1 BCD weights are +0.4V ±0.4V on cards 12798-60010 and 12798-60012, +5V ±0.5V on card 12798-60011.
 - 3) The voltage at pin 22 is $\pm 0.4V \pm 0.4V$.
- f. Measure the voltage between 1) pins AA and BB, and 2) pins 23 and BB. Check that the voltage in 1) is $+7.2V \pm 1.0V$ and in 2) $+0.7V \pm 0.3V$.
- g. Set the HP 2570A LINE ON/OFF switch to OFF. Remove the control panel and control card from the coupler/controller.
- h. Program the following instruction program on the HP 2570A control card pinboard matrix:

Program Step	Instruction
ø	@ 3E
1	@ ø ī
2	@3O
3	@ 3 E
4	@ ø K
5	@30
6	@ 3E
7	@øn
8	@3O

- i. Set SINGLE/CONT jumper W1 to CONT and MAN/AUTO jumper W2 to MAN.
- j. Replace the control card in the HP 2570A and fit the diagnostic control panel in place.
- k. Set the diagnostic control panel switches as follows:

RUN/STOP	STOP
1 MHz/MAN	1 MHz

- 1. Set HP 2570A LINE ON/OFF switch to ON and press the START switch. If the HP digital recorder is connected to the BCD output card, carry out all succeeding steps; if not, proceed to step r.
- m. Turn the recorder ON/OFF switch to ON and press the STEP button on the control panel a number

- of times. Note each data character as it appears on the ASCII code indicators until the recorder prints.
- n. Check the printout against the input data. Refer to Table 4-1. Note that the first character input to the recorder appears in the extreme left-hand column and the characters that follow read from left to right.
- o. Press the MANUAL PRINT button on the recorder. Note that the corresponding printout is all V's (assuming no special character wheels on the recorder).
- p. Set the RUN/STOP switch to RUN. The recorder should now run continuously.

CHECK POINTS

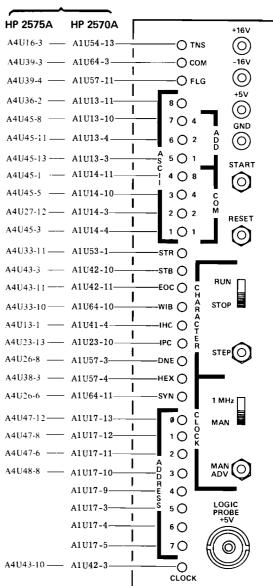


Figure 4-1. Control Card Check Points
For Diagnostic Control Panel

- q. Set the RUN/STOP switch to STOP and disconnect the recorder from the BCD output card. Proceed to step u.
- r. Repeatedly press the STEP button until a data character (FLG character is not lit) appears on the ASCII code indicators.
- s. Measure the output voltages at pins X, W, 19, and 20 with respect to ground (BB) on the BCD output card. The output code should correspond to the PGM MODE 8421 bits shown in Table 4-1.
- t. Repeat steps r and s, measuring the appropriate output pins until the last (10th) character code appears on the diagnostic control panel ASCII code indicators. Note that the Done (DNE) indicator is lit.
- u. Connect an oscilloscope to pins 22 and BB (ground). Set the RUN/STOP switch to RUN.

v. Adjust the oscilloscope to display a positive pulse train. Check that the waveform specifications are as follows:

Pulse Width:

150 ms to 400 ms

Pulse Amplitude:

12V ±2V

Rise Time:

 $< 1 \text{V}/\mu \text{s}$

w. Any deviation in card performance from that detailed in the preceding steps indicates a circuit malfunction. Refer to Table 4-2 for assistance in locating the defective component.

NOTE

Before troubleshooting the card, ensure that the correct supply voltages are available to the card from the coupler/controller backplane. At pins 12, 13, N and P check for $+5V \pm 250$ mV; at pins 18 and V check for $+16V \pm 800$ mV.

Table 4-1. Input/Output Codes

ASCII C	haracter	ASCII Code Bits	Output C	haracter	BCD Output	Code Weight
RCD Mode	PGM Mode	7 6 5 4 3 2 1	RCD① Mode	PGM2 Mode	RCD Mode 8421	PGM Mode 8421
0	0	0 1 1 0 0 0 0	0	0	0000	0000
1	1	0 1 1 0 0 0 1	1	1	0001	0001
2	2	0 1 1 0 0 1 0	2	2	0010	0010
3	3	0 1 1 0 0 1 1	3	3	0011	0011
4	4	0 1 1 0 1 0 0	4	4	0100	0100
5	5	0 1 1 0 1 0 1	5	5	0101	0101
6	6	0 1 1 0 1 1 0	6	6	0110	0110
7	7	0 1 1 0 1 1 1	7	7	0111	0111
8	8	0 1 1 1 0 0 0	8	8	1000	1000
9	9	0 1 1 1 0 0 1	9	9	1001	1001
E		1 0 0 0 1 0 1	*		1111	
+		0 1 0 1 0 1 1			1100③	
-		0 1 0 1 1 0 1	-		1011	
Space		0 1 0 0 0 0 0	Space		1010	ļ
	В	1 0 0 0 0 1 0		10		1010
	С	1 0 0 0 0 1 1		11		1011
	D	1 0 0 0 1 0 0		12		1100
	E	1 0 0 0 1 0 1		13		1101
	F	1 0 0 0 1 1 0		14		1110
	G	1 0 0 0 1 1 1		15		1111

- ① Character on HP 5050A/B or HP 5055A standard print wheel.
- 2 BCD equivalent code.
- 3 Preset value determined by jumpers W1, W2, W3, and W4.

Table 4-2. HP 12798B BCD Output Card Troubleshooting Table

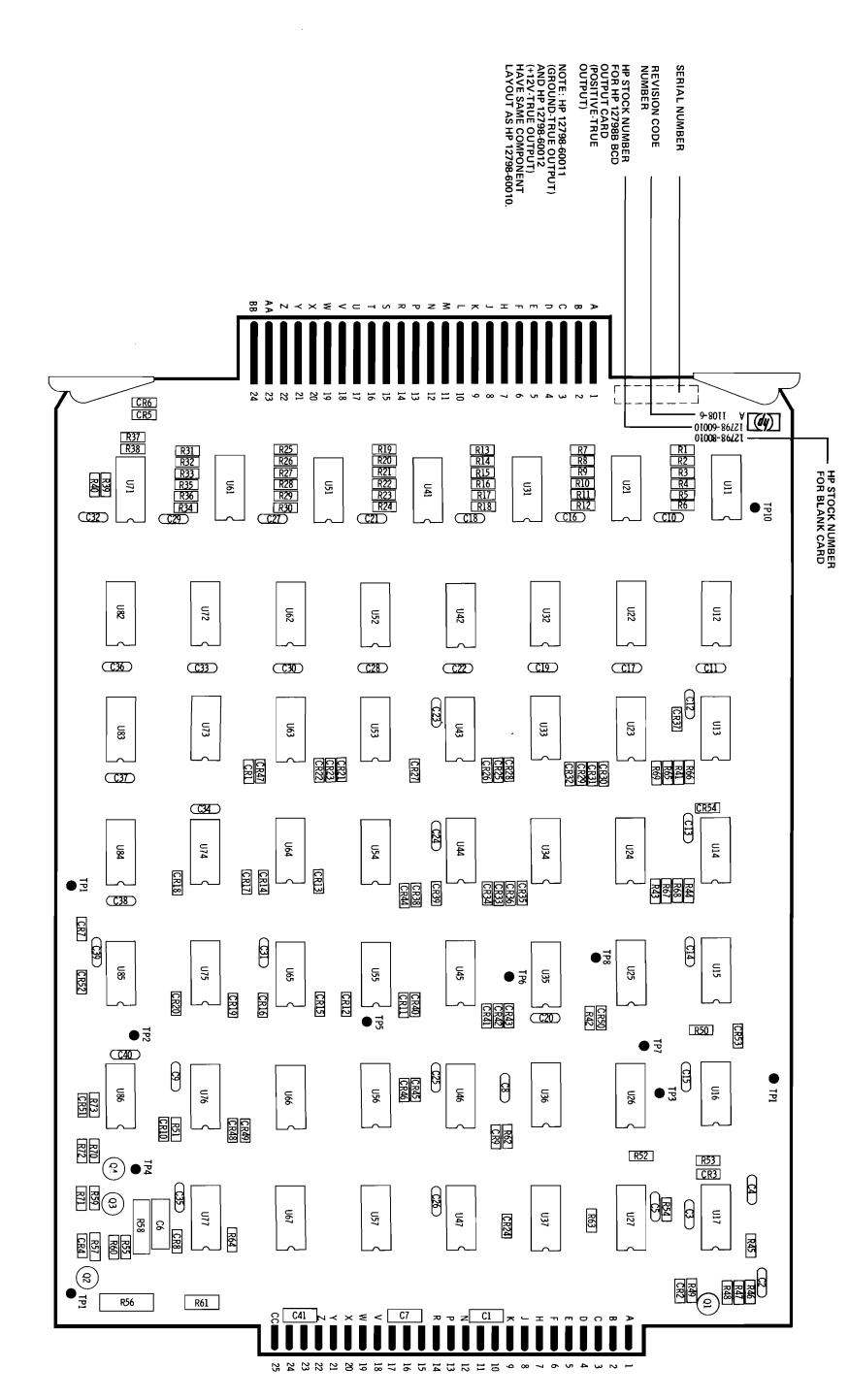
	_	,					_				
		C. Decimal Decoder malfunction		·			B. Improper strobing of data.			A. Improper data code at output	Problem
Decade Counter U86 (C-6)	NAND gate U76A (D-6)	Backplane	NOR gates U53 (A-8. B-8) U63 (C-8, D-8, A-12) U73 (B-12, C-12, D-12)	NAND gates U55 (A-7, B-7) U64 (C-7) U65 (D-7, A-11) U74 (B-11, C-11) U75 (C-11, D-11)	Data Strobe generator U25B (E-8) and driver/ inverter U37A (E-9)	NAND Gate U35A (B-5) and inverter U15E (C-1)	NSTB (C-1)	Strobe	Data Storage Register elements U12 (A-8) U22 (B-8) U32 (B-8, C-8) U42 (C-8) U42 (C-8) U52 (D-8) U52 (D-8) U62 (A-12) U72 (B-12) U82 (B-12, C-12) U83 (C-12) U84 (D-12)	Line drivers U11 (A-9, B-9) U21 (B-9, C-9) U31 (C-9, D-9) U41 (D-9, A-13) U51 (B-13) U61 (C-13) U71 (D-13)	Possible Cause
Verify output code cycles from 0000 to 1001. Also verify reset to 0000 with PRT.	Verify desired signal on output pin 6 of U76A; a 1 μ s \leq + 0.6V pulse coincident with End of Character EOC. Note: Monitor U76A pin 6 at TP2 (FADV).	Verify Compact (NCOM). Flag (NFLG), Transfer (NTNS) are \geq +2.5V and Interrupt Program Control (NIPC) is \leq +0.6V simultaneously and pin 4 of NAND gate U76A (D-6) is \geq +2.5V.	Verify proper operation of NOR gates.	Verify proper operation of decode strobe gates.	Verify proper signal is obtained at pin 8 of U25B and pin 3 of U37A. Note: Monitor pin 3 at TP5 (DSTB).	Verify proper operation of gate, ie., \geq + 2.5V on pin 6 of U35A and pin 10 of U15E for proper data code on backplane.	Verify Strobe (NSTB) signal is applied at pin Y.	Verify proper strobing of data into Data Storage register (See "B" below).	Verify proper input voltage to Data Storage register.	Verify proper input drivers.	Remarks

		E. Failure of Store Data Signal to occur.						D. Card fails to accept data	C. (Continued)	Problem
flip-flop U37C, U37D (C-4) Inverter U15B (E-11)	End of Data flip-flop U36A, U36B (C-4) NAND gate U36D (C-4) End of Word	NDNE input (C-1) Inverter U57D (C-1) NAND gate U46A (C-3)		Continuous Command flip-flop U56D, U56C (B-3)	Input Command flip-flop U46D, U46C (A-3) NOR gate U46B (A-3)	U57C (A-2) NAND gate U14A (A-3)	Command Decode NAND gates U43A (A-2) and U44A (B-2)	Backplane	Inverters U85C (C-6) U85A (C-6) U85D (C-6) U85B (C-6)	Possible Cause
11 for \geq + 2.5V, V . of U15B for \geq + l of U71E for \leq +	flop at set-side output + 0.6V at End of cha y output of gate U36D. > + 2.5V reset side) signal input at pin signal at output pin 1 during the $1 \mu s$ that of U46A is $\leq + 0.6V$	Verify operation of NOR gate at output pin 6 (\geq + 2.5V) for K command. Check at TP 6 (DTA).	Note: Monitor signal at TP 6 (DTA) Verify operation of flip-flop at reset-side output pin 11 (\leq + 0.6V) for K command.	Verify operation of flip-flop at reset-side output pin 11 \leq + 0.6V for I command. Verify operation of NOR gate at output pin 6 (\geq + 2.5V) for I command.	0.6V at output pin 12 at Str	Check for \leq + 0.6V at output pin 6 of U43A for an I command $(1 \ \mu s) \leq$ + 0.6V at output pin 6 of U44A for a K command (usually $\approx 5 \mu s$).	Verify ASCII inputs NAS1, NAS2, NAS3, and NAS4; Address NADN; Strobe NSTB and Transfer NTNS are of proper value simultaneously. Check NTNS \geq +2.5V, NADN and NSTB (1 μs) \leq 0.6V.	Verify proper operation at output pins 6 ($\overline{1}$ bit), 2 ($\overline{2}$ bit), 12 ($\overline{4}$ bit) and 4 ($\overline{8}$ bit) respectively.	Remarks

Table 4-2. HP 12798B BCD Output Card Troubleshooting Table (Continued)

Problem	Possible Cause	Remarks
E. (Continued)	Transistor Q4 (E-12)	Verify transistor Q4 is operating correctly.
	Transistor Q3 (E-13)	Verify operation of transistor Q3 in accordance with settings of jumpers W7 and W8.
F. Failure of Busy signal to inhibit data.	Transistor Q2 (E-1)	Verify proper operation of Q2 when Busy applied at pin 21 of card.
	Inverter U15C (E-2)	Verify \leq + 0.6V signal at input pin U15C-5 when Busy released; \geq + 2.5V signal at output pin U15C-6 when Busy released.
	Jumper W51 (E-5)	Check that jumper W51 is in H position.
	Gate U14C (E-6)	Verify ≥ + 2.5V signal at input pin 10 when Busy released; ≤ + 0.6V signal at input pin 8 when Busy released.
	Inverter/Driver U16D (E-6)	Verify \leq + 0.6V signal at input pins 8 and 9 when Busy released; \geq + 2.5V at output pin 10 when Busy released.
G. Reference voltage values incorrect.	Diode CR5 Zener	Check for + 7.2V ±1.0V across diode.
	Diode CR6	Check for + 0.7V ±0.3V across diode.





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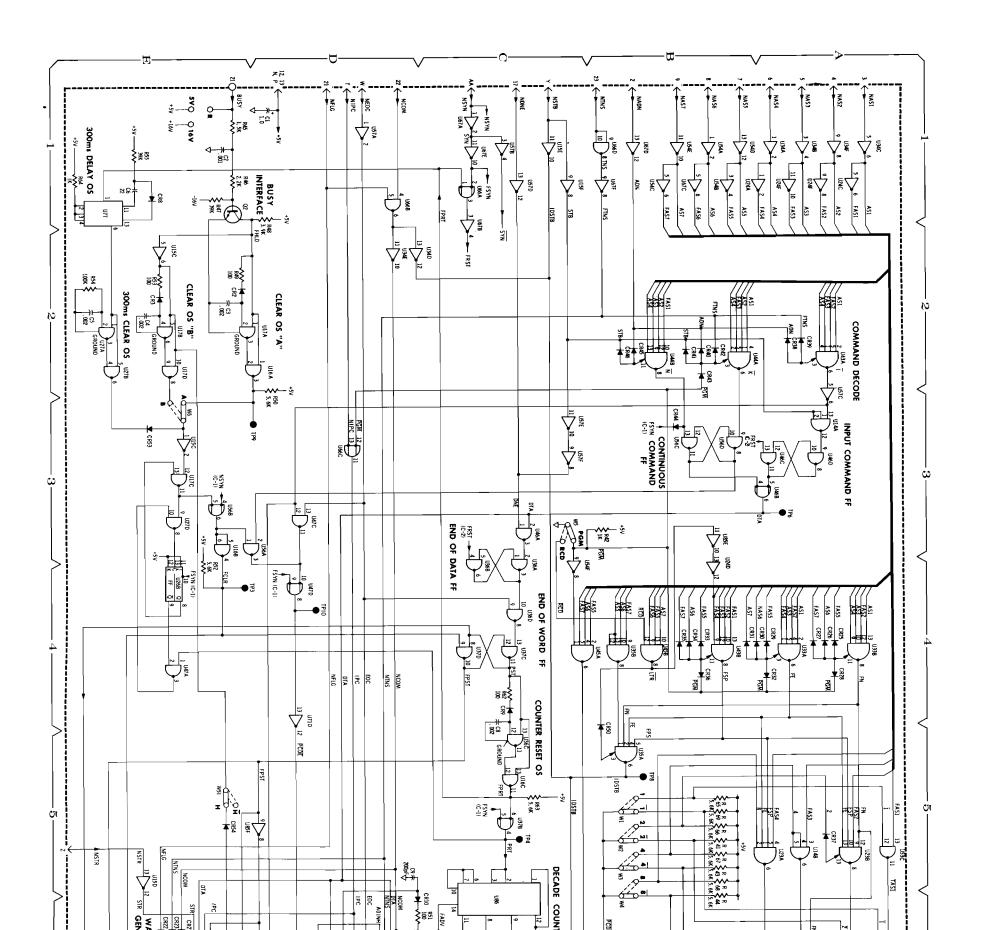
U268 E-4 U27A E-2	ш ш · ·		 			•						:	:		•						:	•							:	:		•	· ·	:	•			п.								· ·				· ·					3	Þ	
U46D	U46B U46C																																		U34D																				U27C		
 Б.А.З 4	ΑΑ ώώ	 C.3	В С 4 4	B.//		2 4 D C	P. 1	A-2	C-8	၄. 8	ငု့	C-8	. A-13	A-13	A-13	۲ ۲	> (و و	D-9	C-4	C-4		D [F <u>.</u> 9	•	•	C-4	ငှ	C-4	C-4	8-4	B-5	A-1			- -	 	> ;	Α-1	A-4	A-4	B-80	c a	0 0	0 0	P 80	C&	D-8		7 C	D C	;> c ;> c	ا ن	က ယ	Б	. E-2	
U65B	U64B 	U64A	U63D		- COS	1830	LIGOD :	U62C	U62B	U62A	U61F	U61E	U61D	0010		161B	1161A	U57F	U57E	U57D	U5/C		57R	U57A	U56D .	U56C	U56B	U56A	U55B	U55A	U54F	U54E	0540		2046	5 5	11544	11530	U53C .	U53B	U53A	U5ZD			53B	U52A	U51F	U51E	U61D		T C C C C C C C C C C C C C C C C C C C		151A .	U47D	U47C	U47B	
		. c-7	A-13		0 d		Δ-12	A-12	. A-12	. A-12	. C-13	. C-13	C-13	: :		3 c	C-13	C-3	C-3	C1	A-3	• (<u>P</u> .1	မာ ယ	В-З	E-3	D-3	В-7	A-7	•	E-1					D (, 50	A-5	A-8	A-6				ا م	D-8	В-13	В-13	B-13			֓֞֞֞֞֜֞֞֞֞֞֞֞֓֓֓֞֞֞֓֓֞֞֞֜֞֞֓֓֓֞֞֞֜֓֓֞֞֞֞֓֡֓֞֞֡	13	D-4	 Ο .	၁ ၈	
	U86C .	U86B	186A		0000	1 B5D .	1850	1185R	U85A	U84D .	U84C .	U84B	U84A	U83D	U83C		183B .	11834	U82D	U82C	U82B	U82A	1037	U 77	U 7 6B	U76A	U75B	U75A	U74B	U74A	U/3D	U/3C	U/35 .	C/3A			1720	1172B	U72A	U71F	U71E	0/10			1171R	U71A	U67F	U67E	U6/U		1670	1167R	11674	U66D .	U66C	U66B	
	ဂ ဂ ၈ ၈	 C-6	ეი	n o	ი ი	ດ ດ	ე (ب ن ن ن	C-6	. D-12	. D-12	. D-12	. D-12	C-12			0 0 1 1 1 1 1	C-13	. В-12	. C-12	. в-12	. 6-12	o r	יי א <u>ר-</u>	р Б	D-6	D-11	. C-11	C-11	B-11	C-12	. D-12	. B-12		. 6-12		0 0 13 6	R 13		Τ.		U-4	. <u>-</u>		13 (D-13	BD	C:1	α.			2 <u>:</u>		 	၂ (သ -	<u> </u>	
																																														10									TP2	_	

	card designat	card designation and output
ne connector.	Card	Output
nnector.	HP 12798-60010	Positive-true (BCD OUT PC
efers to grid location	HP 12798-60011	Ground-true

2		NO.
denotes 50-pin backplane connector.	Unless otherwise specified, resistance in ohms and capacitance in microfarads.	NOTES:

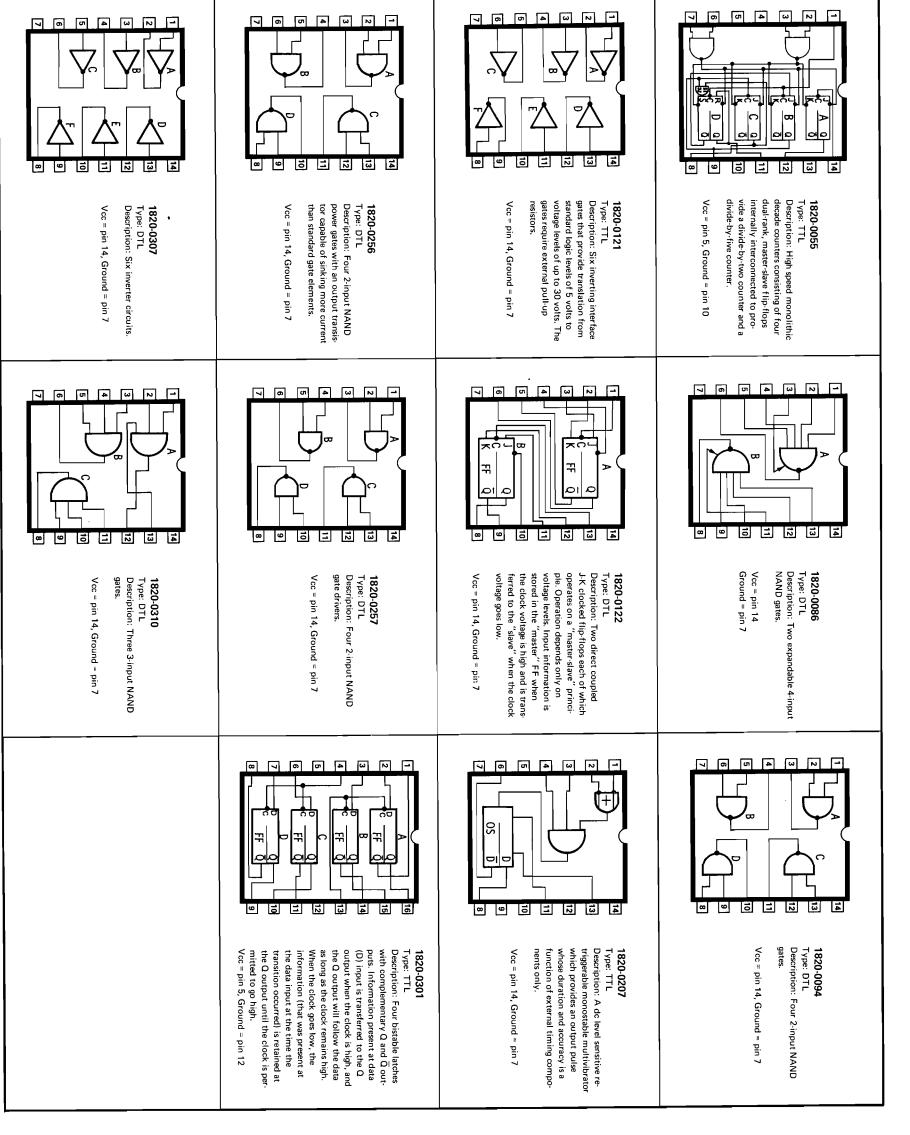
- denotes 48-pin front con denotes 50-pin backplane
- All logic is positive-true.
- Information in parentheses ref on schematic.
- Resistors R1 through R40 are 10K ohm, ±5%, 1/4W.

card designat	card designation and output as follows:	t: Getermine
Card	Output	Jumper Settings
HP 12798-60010	Positive-true (BCD OUT POS = 1)	W10 through W49: "H" W50: "5V"
HP 12798-60011	Ground-true (BCD OUT GND = 1)	W10 through W49: "L" W50: "5V"
HP 12798-60012	+12V-true (BCD OUT +12 = 1)	W10 through W49: "H' W50: "12V"



4-7/4-8

Section IV



SECTION V REPLACEABLE PARTS

5-1. INTRODUCTION

- 5-2. This section contains a list of information for ordering replacement parts. Table 5-1 lists parts alpha-munerically by reference designation. It also provides:
 - a. HP stock numbers
 - b. A general description of the parts
- c. Typical manufacturer of the part expressed as a five-digit code (a list of manufacturers and their code numbers appears in Table 5-2).
- d. Manufacturer's part, stock, or drawing number
 - e. Total quantities used.

5-3. ORDERING INFORMATION

- 5-4. When ordering replacement parts, each part must be identified by the Hewlett-Packard stock number. To order a part that is not listed in the tables, include the following information:
 - a. Instrument model number
 - b. Instrument serial number
 - c. Description of the part
 - d. Function and location of the part.
- 5-5. Address your order or inquiry to your local Hewlett-Packard Sales and Service Office (listed at the rear of this manual).
- 5-6. If parts are ordered from the original manufacturer, a complete description should be included with each manufacturer's part number. Many numbers listed are type numbers only, and descriptions are needed to facilitate selection.

				REFERENCE DES	GIGNATION	S				
Α	= assembly	F		fuse	MP	=	mechanical part	TP	=	test point
В	= motor	FL	=	filter	P	=	plug	U	=	integrated circuit
BT	= battery	J	=	jack	Q	=	transistor	V	=	vacuum tube, neon
C	= capacitor	K	=	relay	R	=	resistor			bulb, photocell, etc.
CP	= coupler	L	=	inductor	RT	=	thermistor	VR	=	voltage regulator
CR	= diode	LS	=	loudspeaker	S	=	switch	w	=	cable; jumper
DL	= delay line	M	=	meter	T	=	transformer	X	=	socket
DS	 device signaling (lamp) 	MK	=	microphone	TB	=	terminal board	Y	=	crystal
E	= misc electronic part							Z	=	tuned cavity; network
				ABBREVIA	TIONS					
A	= amperes	GL	=	glass	N/C	=	normally closed	RH	=	right hand
AFC	= automatic frequency control	GND	=	ground(ed)	NE		neon	RMO		rack mount only
AL	= aluminum	Н	_	handa.	NI PL	=	nickel plate	RMS	=	root-mean-square
AMPL	= amplifier			henries	N/O		normally open	RWV		reverse working voltage
BFO	= beat frequency oscillator	HDW HEX		hardware	NOM	=	nominal	S-B	_	slow-blow
BE CU	= beryllium copper	HEX HR		hexagonal	NPO	=	negative positive zero	SCR	_	
BP	= bandpass			hour(s)			(zero temperature	SE SE	=	silicon-controlled rectific selenium
BRS	= brass	HZ	_	hertz			coefficient)	SECT		JC101114111
BWO	= backward wave oscillator	IC	=	integrated circuit	NPN	=	negative-positive-	SEMICOND	=	section(s) semiconductor
D 110	backward wave oscillator	IMPG		impregnated			negative	SEMICOND		
CCW	= counterclockwise	INCAND		incandescent	NRFR	~	not recommended for	SIL		silicon silver
CD PL	= cadmium plate	INCL		include(s)			field replacement	SL	_	
CER	= ceramic	INS		insulation(ed)	NSR	=	not separately	SPG	=	J
CL	 capacitive logic 	INT		internal			replaceable			-FB
CMO	= cabinet mount only	INTCON		interconnecting				SPL	=	-F
COM	= common				OBD	=	order by description	SST STL	=	
COMP	= composition	K	=	kilo (10 ³)	ОН	=	oval head	21 L	=	steel
COMPL	= complete			, ,	ox	=	oxide	TA	=	tantalum
CONN	= connector	LH	=	left hand				TD	=	time delay
CRT	= cathode-ray tube	LIN	=	linear	P	==	pico (10-12)	TGL		toggle
CTL	= complementary transistor	LK WASH	=	lock wash	PC		printed circuit	THD		thread
	logic	LPF	=	low pass filter	PHL		Phillips	TI	=	titanium
CU	= copper				PK		peak	TOL		tolerance
CW	= clockwise	M	=	milli (10-3)	P-P		peak-to-peak	TSTR	=	
DEPC	= deposited carbon	MEG		meg (106)	PIV		peak inverse voltage	TTL		transistor-transistor logic
DTL	 deposited carbon diode-transistor logic 	MET FLM		metal film	PNP		positive-negative-	TWT		traveling wave tube
DIL	- Glode-Hallsistor logic	MET OX		metallic oxide			positive			
ELECT	= electrolytic	MFR		manufacturer	P/O	=	part of			_
ENCAP	= encapsulated	MHZ		megahertz	POLY		polystyrene	U	=	micro (10-6)
EXT	= external	MINTR		minature	PORC		porcelain	V	=	volts
F	= 6de	MOM		momentary	POS		position(s)	VAR	=	variable
r FH	= farads = flat head	MOS		metal oxide	POT		potentiometer	VDCW	=	dc working volts
rn FILH				semiconductor	PT		point	W/	_	with
	= fillister head	MTG	=	mounting	PWV		peak working voltage	w/ W	=	
FXD	= fixed	MY		mylar	•		r	WIV		
G	= giga (109)	-		,	RECT	~	rectifier	WIV WW	=	
ĞE	= germanium	N	=	nano (10-9)	RF		radio frequency	ww W/O	=	wirewound without

Table 5-1. Replaceable Parts

Reference Designation	HP Part Number	rt Number		Mfr Code	Mfr Part Number	
	12798-60010	1	BCD OUTPUT CARD WITH POSITIVE TRUE LOGIC	04404	12798-60010	
C1 C2	0180-0291 0160-0938	3 1	C:FXD ELECT 1.0 UF 10% 35VDCW C:FXD MICA 1000 PF 5%	56289 72136	1500105X9035A2-0YS RDM15E102J1C	
C3— C5 C6	0160-2225 0180-0228	4 1	C:FXO MICA 2000 PF 5% 300VDCW C:FXD ELECT 22 UF 10% 15VDCW	72136 56289	RDM19F2^2J3C 1500^26X9^1582-DYS	
C 7 C 8 C 9	0180-0291 0160-2225 0140-0198	1	C:FXO ELECT 1.0 UF 10% 35VDCW C:FXD MICA 2000 PF 5% 300VDCW C:FXD MICA 200 PF 5%	56289 72136 72136	1500105X9035A2-0YS RDM19F202J3C RDM15F201J3C	
C10- C40	0160-2055	31	C:FXD CER 0.01 UF +80-20% 100VDCW	56289	C023F101F103ZS22-COH	
C41 CR1 CR2 CR3 CR4	0180-0291 1901-0081 1910-0016 1910-0016 1902-0031	42 10 1	C:FXD ELECT 1.0 UF 10% 35VDCW DIDDE:SILICON 50 VOLTS WORKING OIODE:GERMANIUM 100MA/0.85V 60PIV OIODE:GERMANIUM 100MA/0.85V 60PIV DIODE BREAKDOWN:12.7V 5%	56289 07263 93332 93332 28480	1500105X9035A2-DYS F91415 D2361 D2361 1902-0031	
CR5 CR6	1902-3119 1901-0081	1	DIODE:BREAKDDWN 6.49V 2% DIODE:SILICON 50 VOLTS WORKING	04713 07263	SZ10939-129 FD1415	
CR7— CR10	1910-0016		DIGDE:GERMANIUM 100MA/0.85V 60PIV	93332	D2361	
CR11 CR43 CR44 CR45	1901-0081 1910-0016		DIODE:SILICON 50 VOLTS WORKING DIODE:GERMANIUM 100MA/0.85V 60PIV	07263 93332	FD1415 D2361	
CR51	1901-0081		DIODE:SILICON 50 VOLTS WORKING	07263	F01415	
CR52 — CR54 01 —	0 1910-0016		DIODE:GERMANIUM 100MA/0.85V 60PIV	93332	D2361	
04	1854-0215	4	TSTR:SI NPN	80131	2N3934	
R1 — R40 R41 R42 R43	0683-1035 0683-5625 0683-1025 0683-5625	42 11 2	R:FXD COMP 10K OHM 5% 1/4W R:FXO COMP 5600 OHM 5% 1/4W R:FXD COMP 1000 OHM 5% 1/4W R:FXD COMP 5600 OHM 5% 1/4W	01121 01121 01121 01121	CB 1235 CB 5625 CB 1225 CB 5625	
R44 R45 R46 R47 R48	0683-5625 0683-1525 0683-2225 0683-3935 0683-3925	1 1 2 1	R:FXD COMP 5600 OHM 5% 1/4W R:FXD COMP 1500 OHM 5% 1/4H R:FXD COMP 2.2K OHM 5% 1/4W R:FXD COMP 39K OHM 5% 1/4W R:FXD COMP 3900 OHM 5% 1/4W	01121 01121 01121 01121 01121	CB 5625 CB 1525 CB 2225 CB 3935 CB 3925	
R 49 R 50 R 51 R 52 R 53	0683-1015 0683-5625 0683-1015 0683-5625 0683-1015	4	R:FXD COMP 100 OHM 5% 1/4W R:FXD COMP 5600 OHM 5% 1/4W R:FXD COMP 100 OHM 5% 1/4W R:FXD COMP 5600 OHM 5% 1/4W R:FXD COMP 100 OHM 5% 1/4W	01121 01121 01121 01121 01121	08 1015 08 5625 08 1015 08 5625 08 1015	
R54 R55 R56 R57 R56	0683-1045 0683-3935 0689-4705 0683-4715 0761-0054	1 1 2 1	R:FXD COMP 100K OHMS 5% 1/4W R:FXD COMP 39K OHM 5% 1/4W R:FXD COMP 47 OHM 5% 1/4W R:FXD COMP 470 OHM 5% 1/4W R:FXD MET 0X 330 OHM 5% 1W	01121 01121 01121 01121 01121 28480	CB 1045 CB 3935 CB 4715 CB 4715 CB 4715	
R59 R60 R61 R62 R63	0683-1215 0683-1035 0686-2015 0683-1015 0683-5625	1	R:FXD COMP 123 OHM 5% 1/4W R:FXD COMP 10K OHM 5% 1/4W R:FXD COMP 233 OHM 5% 1/2W R:FXD COMP 103 OHM 5% 1/4W R:FXD COMP 5630 OHM 5% 1/4W	01121 01121 01121 01121 01121	CR 1215 CR 1035 CR 2015 CR 1015 CR 5625	
R64 R65 —	0683-1025		R:FXD COMP 1000 OHM 5% 1/4W	01121	CR 1925	
R69 R7D R7L	0683-5625 0683-6815 0683-1035	1	R:FXD COMP 5600 OHM 5% 1/4W R:FXD COMP 680 OHM 5% 1/4W R:FXD COMP 10K OHM 5% 1/4W	01121 01121 01121	CB 5625 CB 6815 CB 1935	
R72 R73 U11 U12 U13	0683-4715 0683-5125 1820-0121 1820-0301 1820-0257	1 7 10 2	R:FXD COMP 470 OHM 5% 1/4W R:FXD COMP 5100 OHM 5% 1/4W IC:TTL HEX INTERFACE LAMP DRIVER IC:TTL QUAD LATCH BUFF STORAGE IC:OTL QUAD 2-INPUT BUFFER	01121 01121 18324 01295 04713	CB 4715 CB 5125 N8T90A SN4463 SC8162PK	
U14 U15 U16 U17 U21	1820-0310 1820-0307 1820-0256 1820-0094 1820-0121	1 7 1 10	IC:DTL TRIPLE 3-INPUT NAND GATE IC:DTL HEX INVERTER RL:6K IC:DTL QUAD 2-INPUT POWER GATE IC:DTL QUAD 2-INPUT NAND NOR GATE IC:TTL HEX INTERFACE LAMP DRIVER	07263 07263 04713 07263 18324	U6A996259X U6A993659X MC858P U6A994659X N8T90A	
U22 U23 U24 U25 U26	1820-0301 1820-0086 1820-0307 1820-0086 1820-0122	13	IC:TTL QUAO LATCH BUFF STORAGE IC:DTL DUAL 4-INPUT NAND NOR GATE IC:DTL HEX INVERTER RL:6K IC:DTL DUAL 4-INPUT NAND NOR GATE IC:DTL DUAL 4-FROM NOR GATE IC:DTL DUAL JK FF	01295 04713 07263 04713 07263	SN4463 MC830P U6A993659X MC830P U6A909359X	

Table 5-1. Replaceable Parts (Continued)

Reference Designation	HP Part Number	Qty	Description	Mfr Code	Mfr Part Number
J27 J31 J32	1820-0094 1820-0121 1820-0301		[C:DTL QUAD 2-INPUT NAND NOR GATE C:TTL HEX INTERFACE LAMP DRIVER C:TTL QUAD LATCH BUFF STORAGE	07263 18324 01295	U6A994659X NBT 90A SN4463
J33 J34	1820-0086 1820-0307		IC:DTL DUAL 4-INPUT NAND NOR GATE IC:DTL HEX INVERTER RL:6K	04713 07263 04713	MC830P U6A993659X MC830P
J35 J36 J37 J41 J42	1820-0086 1820-0094 1820-0257 1820-0121 1820-0301		IC:DTL DUAL 4-INPUT NAND NOR GATE IC:DTL QUAD 2-INPUT NAND NOR GATE IC:DTL QUAD 2-INPUT BUFFER IC:TTL HEX INTERFACE LAMP DRIVER IC:TTL QUAD LATCH BUFF STORAGE	07263 07263 04713 18324 01295	N68994659X SC8162PK N8T9DA SN4463
U43— U45 U46 U47 U51	1820-0086 1820-0094 1820-0094 1820-0121		IC:DTL DUAL 4-INPUT NAND NOR GATE IC:DTL QUAD 2-INPUT NAND NOR GATE IC:DTL QUAD 2-INPUT NAND NOR GATE IC:TTL HEX INTERFACE LAMP DRIVER	04713 07263 07263 18324	MC830P U6A994659X U6A994659X N8T90A
U52 U53 U54 U55 U56	1820-0301 1820-0094 1820-0307 1820-0086 1820-0094		IC:TTL QUAD LATCH BUFF STORAGE IC:OTL QUAD 2-INPUT NAND NOR GATE IC:DTL HEX INVERTER RL:5K IC:OTL DUAL 4-INPUT NAND NOR GATE IC:DTL QUAD 2-INPUT NAND NOR GATE	01295 07263 07263 04713 07263	SN4463 U6A994659X U6A993659X MC830P U6A994659X
U57 U61 U62 U63 U64	1820-0307 1820-0121 1820-0301 1820-0094 1820-0086		IC:DTL HEX INVERTER RL:6K IC:TTL HEX INTERFACE LAMP DRIVER IC:TTL QUAD LATCH BUFF STORAGE IC:DTL QUAD 2-IMPUT NAND NOR GATE IC:DTL DUAL 4-IMPUT NAND NOR GATE	07263 18324 01295 07263 04713	U6A993659X N8T90A SN4463 U6A994659X MC830P
U66 U67 U71 U72	1820-0086 1820-0094 1820-0307 1820-0321 1820-0321		IC:DTL DUAL 4-INPUT NAND NOR GATE IC:DTL QUAD 2-INPUT NAND NOR GATE IC:DTL HEX INVERTER RL:6K IC:TTL HEX INTERFACE LAMP DRIVER IC:TTL QUAD LATCH BUFF STORAGE	04713 07263 07263 18324 01295	MC830P U6A994659X U6A993659X N8T9PA SN4463
U73	1820-0094		IC:DTL QUAD 2~INPUT NAND NOR GATE	07263	U6A994659X
บ74 — บ76 บ77	1820-0086 1820-0207	1	IC:DTL DUAL 4-INPUT NAND NOR GATE IC:TTL MONOSTABLE MULTIVIBRATOR	04713 07263	MC830P SL12895
U82 — U84 U85 U86	1820-0301 1820-0307 1820-0055	1	IC:TTL QUAD LATCH BUFF STORAGE IC:DTL HEX INVERTER RL:6K IC:TTL DECADE COUNTER	01295 07263 01295	SN4463 U6A99?659X SN4356
w1 — w9	5040-1485	9	CONDUCTOR ASSEMBLY:PLUG-IN JUMPER	28480	50.40-1485
W10 — W50 W51	8159-0005 5040-1485	41	JUMPER WIRE CONDUCTOR ASSEMBLY:PLUG-IN JUMPER	28 480 28 4 80	8159-0005 5040-1485
	12798-60011	1	BCD OUTPUT CARD WITH GROUND TRUE LOGIC (OPTION 020) FOR LIST OF REPLACEABLE PARTS, SEE PARTS LIST FOR 12798-60010.	04404	12798-60011
	12798-60012	,	BCD OUTPUT CARD WITH +12V-TRUE LOGIC (PART OF OPTION 001). FOR LIST OF REPLACEMENT PARTS SEE LIST FOR 12798-60010.	04404	12798-60012

Table 5-1. Replaceable Parts (Continued)

Reference Designation	HP Part Number	Qty	Description	Mfr Code	Mfr Part Number
	02547-6040	1	CABLE (PART OF OPTION 001)	04404	02547-6040
	1251-0086 1251-0171 1251-0335 8120-0081	1 1 1 8	CONNECTOR:50 CONTACT R & P BUSHING:FOR AN TYPE CONNECTOR CONNECTOR:PC 48 CONTACT (2X24) CABLE:SPEC PURP	28480 28480 95238 28480	1251-0086 1251-0171 K 600-13-PCGD 24 8120-0081
	02116-4001 02116-4003	1 1	HOLDER:CONNECTOR CLAMP CABLE:LARGE	28480 28480	02116-4001 02116-4003
	12798-60002	1	CABLE (PART OF OPTION 003)	04404	12798-60002
			LIST NOT YET AVAILABLE		
	12798-60003	1	CABLE (PART OF OPTION 004)	04404	12798-60003
	1251-0218 1251-0352 1251-1042 1251-2384 1251-2417	2 1 1 14 1	CONNECTOR:LOCK POST SUBMINAT TYPE D BUSHING:CIRCULAR CONNECTOR (SIZE 145) RETAINER:CONNECTOR CONTACT:R & P CONNECTOR BODY:R & P CONNECTOR	71468 91737 71468 28480 28480	DAM-15S 18220-6 D B 51221-1 1251-2384 1251-2417
	1251-2518 8120-1281 02116-4001 02116-4003	1 8 1	CONNECTOR:PC 48 (2X24) CONTACT CABLE HOLDER:CONNECTOR CLAMP CABLE:LARGE	7 17 8 5 2 8 4 8 0 2 8 4 8 0 2 8 4 8 0	251-24-30-216 8120-1281 02116-4001 02116-4003
	12798-60005	1	CABLE (PART OF OPTION 007)	04404	12798-60005
	1251-0086 1251-0171 1251-2518 8120-0204	1 1 1 8	CONNECTOR:50 CONTACT R & P BUSHING:FOR AN TYPE CONNECTOR CONNECTOR:PC 48 (2X24) CONTACT CABLE	28480 28480 71785 28480	1251-0086 1251-0171 251-24-30-261 8120-0204
	02116-4001 02116-4003	1 1	HOLDER:CONNECTOR CLAMP CABLE:LARGE	28480 28480	02116-4001 02116-4003
	12798-60006	1	CABLE (PART OF OPTION 008)	04404	12798-60006
	1251-0084 1251-0171 1251-2518 02116-4001 02116-4003	1 1 1 1	CONNECTOR MALE:RACK & PANEL BUSHING:FOR AN TYPE CONNECTOR CONNECTOR:PC 48 (2X24) CONTACT HOLDER:CONNECTOR CLAMP CABLE:LARGE	28480 28480 71785 28480 28480	1251-0084 1251-0171 251-24-30-216 02116-4001 02116-4003
	12798-60009	 1	CABLE (PART OF OPTION 005)	04404	12798-60009
	1251-0086 1251-0171 1251-2518 8120-0204	1 1 1	CONNECTOR:50 CONTACT R & P BUSHING:FOR AN TYPE CONNECTOR CONNECTOR:PC 48 (2X24) CONTACT CABLE	28480 28480 71785 28480	1251-0086 1251-0171 251-24-30-261 8120-0204
	02116-4001 02116-4003	1 1	HOLDER:CONNECTOR CLAMP CABLE:LARGE	28480 28480	02116-4001 02116-4003

Table 5-2. Code List of Manufacturers

MFR. NO.	MANUFACTURER NAME	ADDRESS
01121	Allen Bradley Co.	Milwaukee, Wis. 53204
01295	Texas Instruments Inc. Semiconductor Components Div.	Dallas, Tex. 75231
04404	Hewlett-Packard Co., Automatic Measurement Div.	Palo Alto, Calif. 94306
04713	Motorola Semiconductor Prod. Inc.	Phoenix, Ariz. 85008
07263	Fairchild Camera & Inst. Corp. Semiconductor Div.	Mountain View, Calif. 94040
18324	Signetics Corp.	Sunnyvale, Calif. 94086
28480	Hewlett-Packard Company	Palo Alto, Calif. 94304
56289	Sprague Electric Co.	N. Adams, Mass. 01247
71468	ITT Cannon Electric Inc.	Los Angeles, Calif. 90031
71785	Cinch Mfg. Co. Div. United Carr Fastener Corp.	Chicago, Ill. 60624
72136	Electro Motive Mfg. Co. Inc.	Willimantic, Conn. 06226
80131	Electronic Industries Association. Standard tube or semi-conductor device, any manufacture	Washington, D.C. 20006
91737	ITT Gremar Inc.	Woburn, Mass. 01801
93332	Sylvania Electric Prod. Inc. Semiconductor Div.	Woburn, Mass. 01801
95 23 8	Continental Connector Corp.	Woodside, N.Y. 11377